

# HVL355C

Variable Capacitance Diode for VCO

REJ03G0178-0300 Rev.3.00 Feb 06, 2006

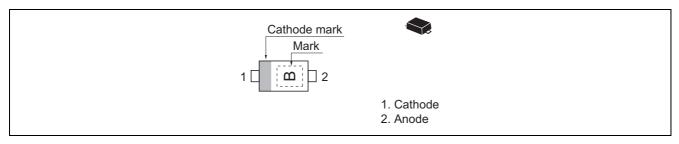
### Features

- High capacitance ratio. (n = 2.35 to 2.55)
- Low series resistance. (rs =  $0.60 \Omega$  max)
- Extremely small Flat Lead Package (EFP) is suitable for surface mount design.

### **Ordering Information**

Type No.	Laser Mark	Package Name	Package Code	
HVL355C	В	EFP	PXSF0002ZA-A	

### **Pin Arrangement**





# **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Value	Unit
Reverse voltage	V <sub>R</sub>	15	V
Junction temperature	Tj	125	°C
Storage temperature	Tstg	–55 to +125	°C

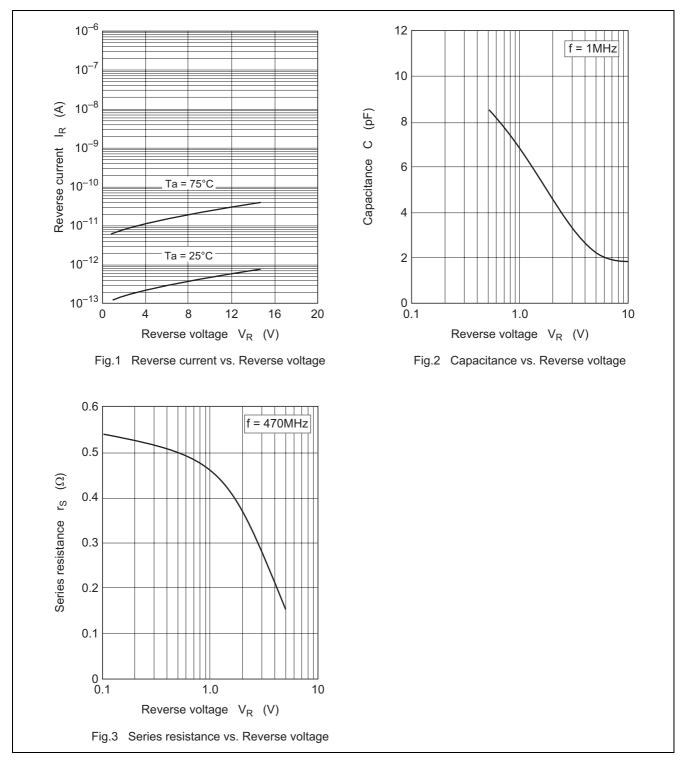
### **Electrical Characteristics**

						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse current	I <sub>R1</sub>	_	_	10	nA	V <sub>R</sub> = 15 V
	I <sub>R2</sub>	—	—	100		V <sub>R</sub> = 15 V, Ta = 60°C
Capacitance	C <sub>1</sub>	6.62	_	7.02	pF	$V_{R} = 1 V, f = 1 MHz$
	C <sub>4</sub>	2.60	_	2.95		$V_R = 4 V, f = 1 MHz$
Capacitance ratio	n	2.35	_	2.55	_	C <sub>1</sub> / C <sub>4</sub>
Series resistance	r <sub>S</sub>		_	0.60	Ω	V <sub>R</sub> = 1 V, f = 470 MHz

Note: For EFP package, the material of lead is exposed for cutting plane. There for, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.

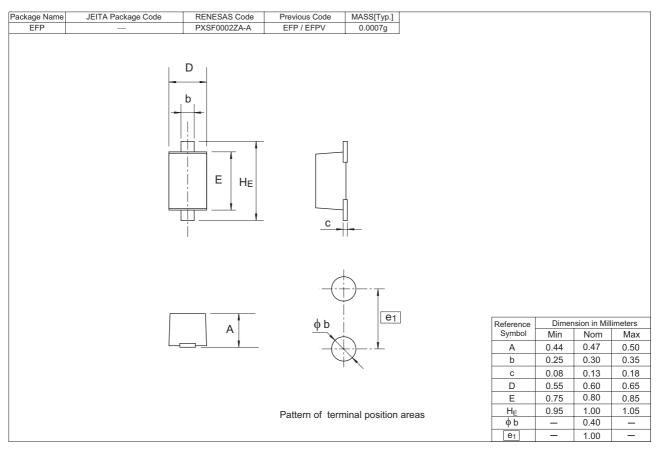


### **Main Characteristic**





### **Package Dimensions**





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