

HVL355C

Variable Capacitance Diode for VCO

REJ03G0178-0300 Rev.3.00 Feb 06, 2006

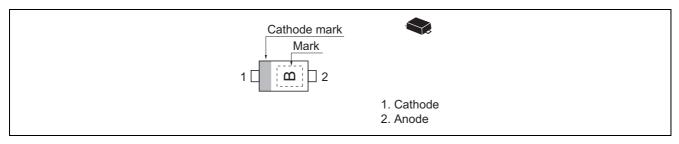
Features

- High capacitance ratio. (n = 2.35 to 2.55)
- Low series resistance. (rs = 0.60Ω max)
- Extremely small Flat Lead Package (EFP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Package Name	Package Code	
HVL355C	В	EFP	PXSF0002ZA-A	

Pin Arrangement





Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Reverse voltage	V _R	15	V
Junction temperature	Tj	125	°C
Storage temperature	Tstg	–55 to +125	°C

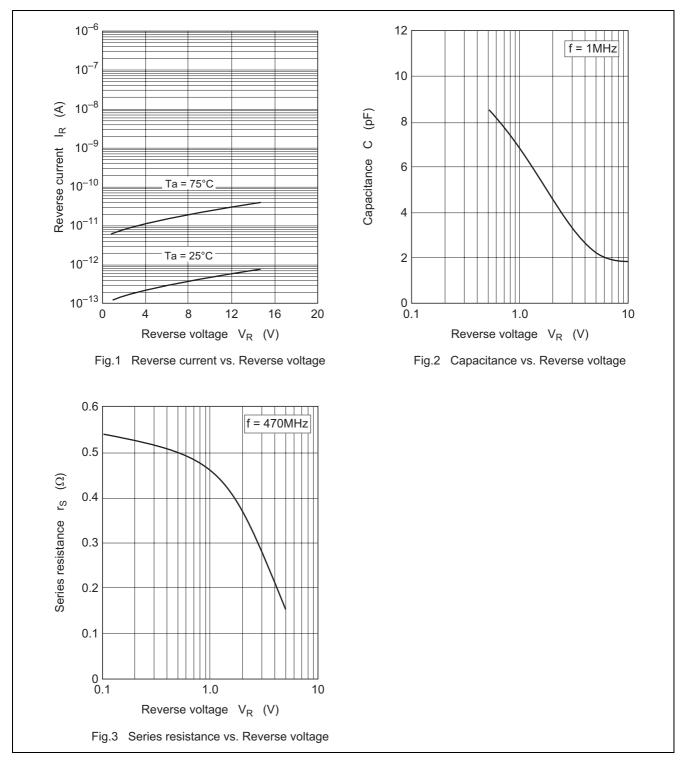
Electrical Characteristics

						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse current	I _{R1}	_	_	10	nA	V _R = 15 V
	I _{R2}	—	—	100		V _R = 15 V, Ta = 60°C
Capacitance	C ₁	6.62	_	7.02	pF	$V_{R} = 1 V, f = 1 MHz$
	C ₄	2.60	_	2.95		$V_R = 4 V, f = 1 MHz$
Capacitance ratio	n	2.35	_	2.55	_	C ₁ / C ₄
Series resistance	r _S		_	0.60	Ω	V _R = 1 V, f = 470 MHz

Note: For EFP package, the material of lead is exposed for cutting plane. There for, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.

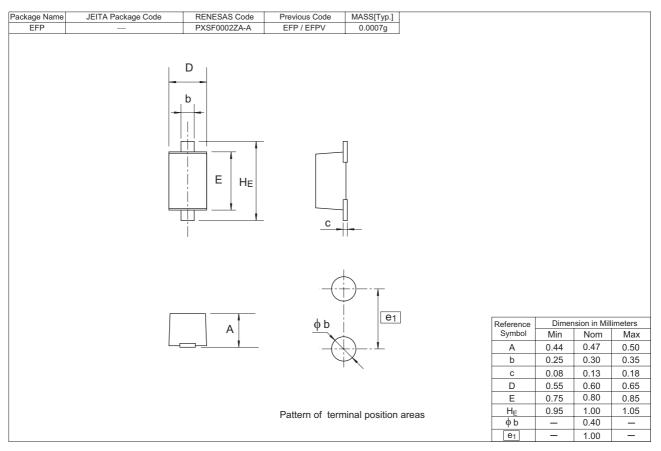


Main Characteristic





Package Dimensions





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Renesas Technology Malaysia Sdn. Bhd Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jalan Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia Tel: <603> 7955-9390, Fax: <603> 7955-9510

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