TOSHIBA FIELD EFFECT TRANSISTOR SILICON P CHANNEL MOS TYPE(U-MOSII)

## TENTATIVE TPC6102

NOTE BOOK PC
PORTABLE EQUIPMENTS APPLICATIONS

- Low Drain - Source ON Resistance : $\mathrm{R}_{\mathrm{DS}(\mathrm{ON})}=\mathrm{m} \Omega$ (Typ.)
- High Forward Transfer Admittance : $\left|\mathrm{Y}_{\mathrm{fs}}\right|=\mathrm{S}$ (Typ.)
- Low Leakage Current : $\mathrm{I}_{\mathrm{DS}}=-10 \mu \mathrm{~A}$ (Max.) ( $\mathrm{V}_{\mathrm{DS}}=-30 \mathrm{~V}$ )
- Enhancement - Model : $\mathrm{V}_{\mathrm{th}}=-0.8 \sim-2.0 \mathrm{~V}\left(\mathrm{~V}_{\mathrm{DS}}=-10 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=-1 \mathrm{~m} \mathrm{~A}\right)$

MAXIMUM RATINGS $\left(\mathrm{Ta}=25^{\circ} \mathrm{C}\right)$

| CHARACTERISTIC | SYMB0L | RATING | UNIT |
| :--- | :---: | :---: | :---: |
| Drain - Source Voltage | $\mathrm{V}_{\mathrm{DS}}$ | -30 | V |
| Drain - Gate Voltage <br> $\left(\mathrm{R}_{\mathrm{GS}}=20 \mathrm{k} \Omega\right)$ | $\mathrm{V}_{\mathrm{DGR}}$ | -30 | V |
| Gate - Source Voltage | $\mathrm{V}_{\mathrm{GS}}$ | $\pm 20$ | V |
| }{} | DC | $\mathrm{I}_{\mathrm{D}}$ | -4.5 |
|  | Pulse | $\mathrm{I}_{\mathrm{DP}}$ | -18 |
| Drain Power Dissipation(Ta $\left.=25^{\circ} \mathrm{C}\right)$ <br> $*$ | $\mathrm{P}_{\mathrm{D}}$ | 2.0 | A |
| Channel Temperature | $\mathrm{T}_{\mathrm{ch}}$ | 150 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature Range | $\mathrm{T}_{\mathrm{stg}}$ | $-55 \sim 150$ | ${ }^{\circ} \mathrm{C}$ |

THERMAL CHARACTERISTICS

| CHARACTERISTICS | SYMBOL | MAX. | UNIT |
| :--- | :---: | :---: | :---: |
| Thermal Resistance, Chanel to <br> Ambient* | $\mathrm{R}_{\mathrm{th}(\mathrm{ch}-\mathrm{a})}$ | 62.5 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |

Note; *Drive operation ; Mount on glass epoxy board (1inch $\left.{ }^{2} \mathrm{X} 0.8 \mathrm{t}\right)(\mathrm{t}=5 \mathrm{~s})$

THIS TRANSISTOR IS AN ELECTROSTATIC SENSITIVE DEVICE. PLEASE HANDLE WITH CAUTION.

INDUSTRIAL APPLICATIONS
UNIT:mm


CIRCUIT CONFIGURATION


Rev. May 12, 2000

ELECTRICAL CHARACTERISTICS ( $\mathrm{Ta}=25^{\circ} \mathrm{C}$ )

| CHARACTERISTICS | SYMBOL | TEST CONDITION | MIN. | TYP. | MAX. | UNIT |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Gate Leakage Current | $I_{\text {GS }}$ | $\mathrm{V}_{\mathrm{GS}}= \pm 16 \mathrm{~V}, \mathrm{~V}_{\mathrm{DS}}=0 \mathrm{~V}$ | - | - | $\pm 10$ | $\mu \mathrm{A}$ |
| Drain Cut-off Current | $\mathrm{I}_{\mathrm{DSS}}$ | $\mathrm{V}_{\mathrm{DS}}=-30 \mathrm{~V}, \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V}$ | - | - | -10 | $\mu \mathrm{A}$ |
| Drain-Source Breakdown Voltage | $\mathrm{V}_{\text {(BR)DSS }}$ | $\mathrm{I}_{\mathrm{D}}=-10 \mathrm{~mA}, \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V}$ | -30 | - | - | V |
|  | $\mathrm{V}_{(\mathrm{BR}) \mathrm{DSX}}$ | $\mathrm{I}_{\mathrm{D}}=-10 \mathrm{~mA}, \mathrm{~V}_{\mathrm{GS}}=20 \mathrm{~V}$ | -15 | - | - | V |
| Gate Threshold Voltage | $\mathrm{V}_{\mathrm{th}}$ | $\mathrm{V}_{\mathrm{DS}}=-10 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=-1 \mathrm{~mA}$ | -0.8 | - | -2.0 | V |
| Drain-Source 0N Resistance | $\mathrm{R}_{\mathrm{DS} \text { (0N) }}$ | $\mathrm{V}_{\mathrm{GS}}=-4.5 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=-2.2 \mathrm{~A}$ | - | 78 | 100 | $\underline{m} \Omega$ |
|  |  | $\mathrm{V}_{\mathrm{GS}}=-10 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=-2.2 \mathrm{~A}$ | - | 48 | 60 |  |
| Forward Transfer Admittance | $\left\|Y_{f s}\right\|$ | $\mathrm{V}_{\mathrm{DS}}=-10 \mathrm{~V}, \mathrm{I}_{\mathrm{D}}=-2.2 \mathrm{~A}$ | 3.0 | 6.0 | - | S |
| Input Capacitance | $\mathrm{C}_{\text {iss }}$ | $\begin{aligned} & V_{D S}=-10 \mathrm{~V}, \quad V_{G S}=0 \mathrm{~V} \\ & \mathrm{f}=1 \mathrm{MHz} \end{aligned}$ | - | 500 | - | p F |
| Reverse Transfer Capacitance | $\mathrm{C}_{\text {rss }}$ |  | - | 110 | - |  |
| Output Capacitance | $\mathrm{C}_{\text {oss }}$ |  | - | 150 | - |  |
| Rise Time | $\mathrm{t}_{\mathrm{r}}$ |  | - |  | - | n s |
| Switching Time | $\mathrm{t}_{\text {on }}$ |  | - |  | - |  |
|  | $\mathrm{t}_{\mathrm{f}}$ |  | - |  | - |  |
| Turn-off Time | $\mathrm{t}_{\text {off }}$ |  | - |  | - |  |
| Total Gate Charge (GateSource Plus Gate-Drain) | $\mathrm{Q}_{\mathrm{g}}$ | $\begin{aligned} & \mathrm{V}_{\mathrm{DD}} \doteqdot-24 \mathrm{~V}, \quad \mathrm{~V}_{\mathrm{GS}}=-10 \mathrm{~V} \\ & \mathrm{I}_{\mathrm{D}}=-4.5 \mathrm{~A} \end{aligned}$ | - | 11 | - | n C |
| Gate-Source Charge | $\mathrm{Qgs}^{\text {g }}$ |  | - | 8.5 | - |  |
| Gate-Drain("Miller") Charge | $Q_{g d}$ |  | - | 2.5 | - |  |

SOURCE - DRAIN DIODE RATINGS AND CHARACTERISTICS ( $\mathrm{Ta}=25^{\circ} \mathrm{C}$ )

| CHARACTERISTICS | SYMBOL | TEST CONDITION | MIN. | TYP. | MAX. | UNIT |
| :--- | :---: | :---: | :---: | :---: | :---: | :---: |
| Continuous Drain Reverse <br> Current | $\mathrm{I}_{\mathrm{DR}}$ | - | - | - | -4.5 | A |
| Pulse Drain Reverse Current | $\mathrm{I}_{\text {DRP }}$ | - | - | - | -18 | A |
| Diode Forward Voltage | $\mathrm{V}_{\text {DSF }}$ | $\mathrm{I}_{\mathrm{DR}}=-4.5 \mathrm{~A}, \mathrm{~V}_{\mathrm{GS}}=0 \mathrm{~V}$ | - | - | 1.2 | V |

