

S525

## N-Channel MOS-Fieldeffect Triode, Depletion Mode

#### Features

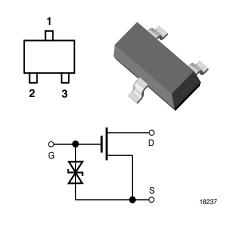
- Integrated gate protection diodes
- Low feedback capacitance
- Low noise figure
- Lead (Pb)-free component
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

#### Applications

High frequency stages up to 300 MHz.

#### **Mechanical Data**

Case: SOT-23 Plastic case Weight: approx. 8.0 mg Pinning: 1 = Source, 2 = Gate , 3 = Drain





Electrostatic sensitive device. Observe precautions for handling.

#### Parts Table

Part Marking		Package		
S525T	LB	SOT-23		

#### **Absolute Maximum Ratings**

T<sub>amb</sub> = 25 °C, unless otherwise specified

Parameter	Test condition	Symbol	Value	Unit	
Drain - source voltage		V <sub>DS</sub>	20	V	
Drain current		Ι <sub>D</sub>	30	mA	
Gate - source peak current		± I <sub>GSM</sub>	10	mA	
Total power dissipation	$T_{amb} \le 60 \ ^{\circ}C$	P <sub>tot</sub>	200	mW	
Channel temperature		T <sub>Ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	- 55 to + 150	°C	

#### **Maximum Thermal Resistance**

Parameter	Test condition	Symbol	Value	Unit	
Channel ambient	1)	R <sub>thChA</sub>	450	K/W	

 $^{1)}$  on glass fibre printed board (25 x 20 x 1.5)  $\text{mm}^3$  plated with 35  $\mu\text{m}$  Cu



### **Electrical DC Characteristics**

 $T_{amb}$  = 25 °C, unless otherwise specified

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Parameter	Test condition	Symbol	Min	Тур.	Max	Unit
Drain - source breakdown voltage	$I_{D} = 10 \ \mu A, \ -V_{GS} = 4 \ V$	V <sub>(BR)DS</sub>	20			V
Gate - source breakdown voltage	$\pm I_{GS} = 10 \text{ mA}, V_{DS} = 0$	$\pm V_{(BR)GSS}$	7.5		12	V
Gate - source leakage current	$\pm V_{GS} = 6 V, V_{DS} = 0$	$\pm I_{GSS}$			50	nA
Drain current	$V_{DS} = 10 \text{ V}, V_{GS} = 0$	I <sub>DSS</sub>	5		14	mA
Gate - source cut-off voltage	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 20 \mu\text{A}$	-V <sub>GS(OFF)</sub>			2.5	V

#### **Electrical AC Characteristics**

 $T_{amb}$  = 25 °C, unless otherwise specified

 $V_{DS}$  = 10 V,  $I_D$  = 10 mA, f = 1 MHz Parameter Test condition Symbol Min Max Unit Тур. Forward transadmittance 14 mS | y<sub>21s</sub> | 16 2.7 Gate input capacitance Cissq pF Feedback capacitance C<sub>rss</sub> 25 fF Output capacitance Coss 1.0 pF Noise figure  $G_{S} = 2 \text{ mS}, G_{L} = 0.5 \text{ mS},$ F 1.0 dB f = 200 MHz  $G_{S} = 2 \text{ mS}, G_{L} = 0.5 \text{ mS},$ G<sub>ps</sub> 25 dB Power gain f = 200 MHz

## Typical Characteristics (Tamb = 25 °C unless otherwise specified)

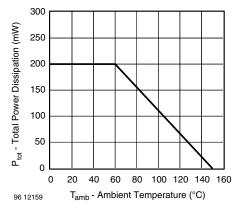


Figure 1. Total Power Dissipation vs. Ambient Temperature

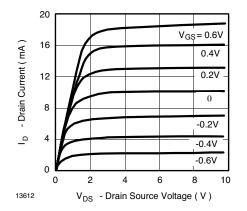


Figure 2. Drain Current vs. Drain Source Voltage



# S525T

## **Vishay Semiconductors**

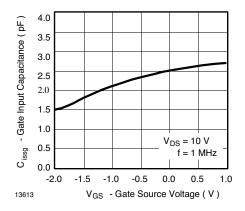


Figure 3. Gate Input Capacitance vs. Gate Source Voltage

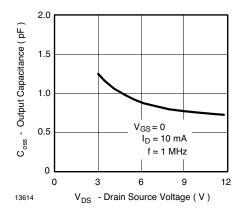


Figure 4. Output Capacitance vs. Drain Source Voltage

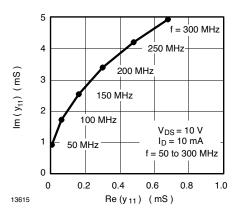


Figure 5. Short Circuit Input Admittance

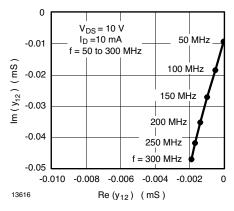


Figure 6. Short Circuit Reverse Transfer Admittance

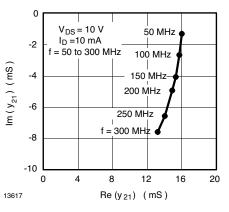


Figure 7. Short Circuit Forward Transfer Admittance

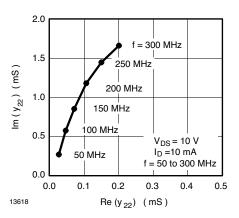


Figure 8. Short Circuit Output Admittance





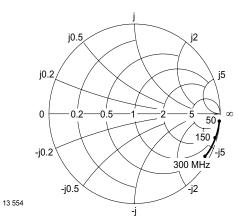


Figure 9. Input Reflection Coefficient

**S**<sub>21</sub>

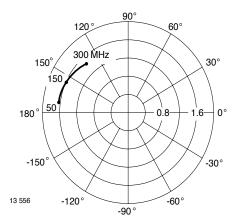
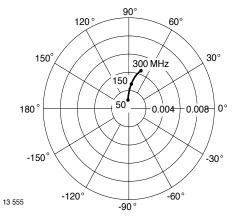


Figure 10. Forward Transmission Coefficient

**S**<sub>12</sub>





**S**<sub>22</sub>

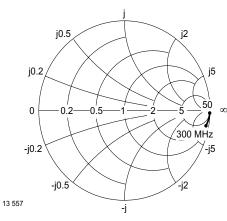


Figure 12. Output Reflection Coefficient

1.15 [0.045]

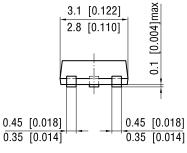
2.6 [0.102] 2.35 [0.093] 0.9 [0.035]

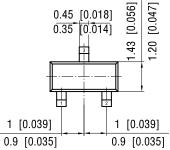
0.9 [0.035]

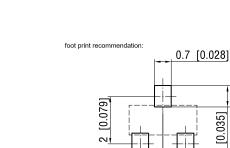
0.95 [0.037]



## Package Dimensions in mm (Inches)







0.95 [0.037]

0.175 [0.007] 0.098 [0.004]

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## **Ozone Depleting Substances Policy Statement**

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively
- 2. Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

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