

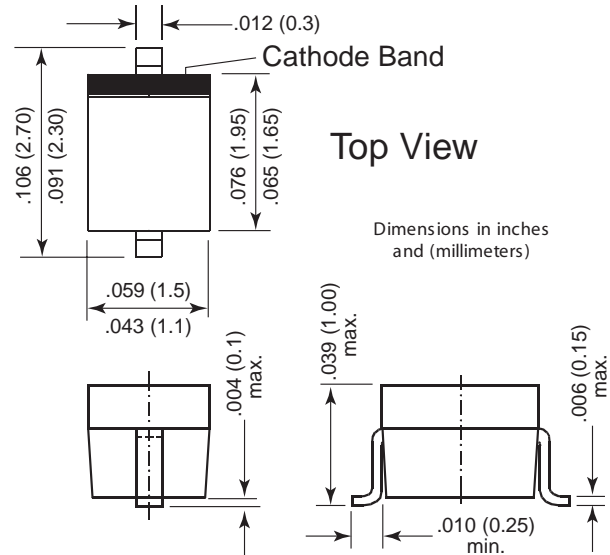
A suffix of "-C" specifies halogen & lead-free

## FEATURES

- \* RoHS Compliant Product
- \* Extremely Low VF
- \* Extremely thin package
- \* Low stored charge
- \* Majority carrier conduction



SOD-323(SC-76)



## MECHANICAL DATA

- \* Case: Molded plastic, JEDEC SOD-323(SC-76)
- \* Terminal : Solder plated, solderable per MIL-STD-750, Method 2026
- \* Polarity : Indicated by cathode band
- \* Mounting Position : Any
- \* Weight : 0.000159 ounce, 0.0045 gram

## MAXIMUM RATING AND ELECTRICAL CHARACTERISTICS

Rating 25°C ambient temperature unless otherwise specified.  
Single phase half wave, 60Hz, resistive of inductive load.  
For capacitive load, derate current by 20%

Parameter	Conditions	Symbol	Min.	Typ.	Max.	Unit
Repetitive Peak Reverse Voltage		$V_{RM}$			45	V
Continuous Reverse Voltage		$V_R$			40	V
Forward Voltage	$I_F = 10\text{mA DC}$	$V_{F1}$			0.34	V
	$I_F = 100\text{mA DC}$	$V_{F2}$			0.55	V
Reverse Current	$V_R = 10\text{V DC}$	$I_R$			30	$\mu\text{A}$
Mean Rectifying Current		$I_o$		0.1		A
Peak forward surge current		$I_{FSM}$		1		A
Capacitance between terminals		$C_T$		6.0		pF
Operating Temperature		$T_J$		125		°C
Storage Temperature		$T_{STG}$	-40		+125	°C

RATING AND CHARACTERISTIC CURVES (SCS501V-40)

● Electrical characteristic curves ( $T_a = 25^\circ\text{C}$ )

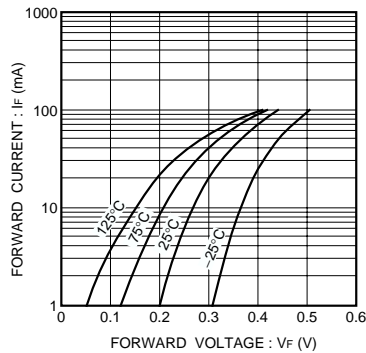


Fig. 1 Forward characteristics

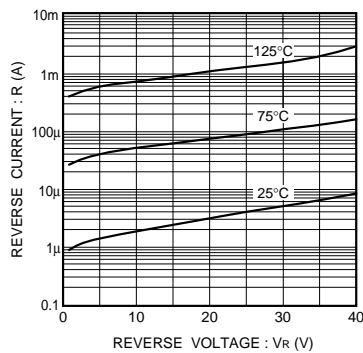


Fig. 2 Reverse characteristics

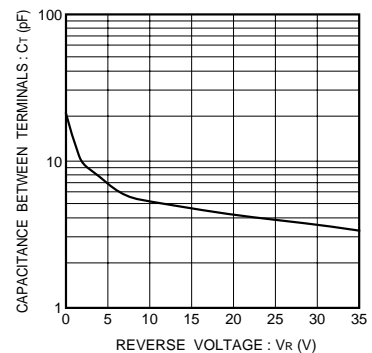


Fig. 3 Capacitance between terminals characteristics

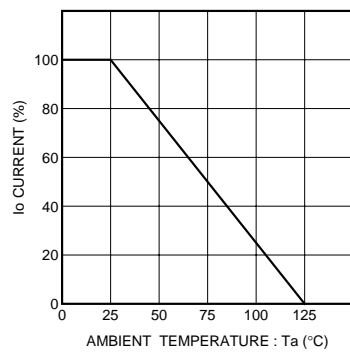


Fig. 4 Derating curve  
(mounting on glass epoxy PCBs)