# **NSL12AW**

# High Current Surface Mount PNP Silicon Low V<sub>CE(sat)</sub> Transistor for Battery Operated Applications

## Features:

- High Current Capability (3 A)
- High Power Handling (Up to 650 mW)
- Low V<sub>CE(s)</sub> (170 mV Typical @ 1 A)
- Small Size

#### **Benefits:**

- High Specific Current and Power Capability Reduces Required PCB Area
- Reduced Parasitic Losses Increases Battery Life

#### **MAXIMUM RATINGS** $(T_A = 25^{\circ}C)$

| Rating                                   | Symbol                            | Max                       | Unit |
|--|-----------------------------------|---------------------------|------|
| Collector-Emitter Voltage                | V <sub>CEO</sub>                  | -12                       | Vdc  |
| Collector-Base Voltage                   | $V_{CBO}$                         | -12                       | Vdc  |
| Emitter-Base Voltage                     | V <sub>EBO</sub>                  | -5.0                      | Vdc  |
| Collector Current – Continuous<br>– Peak | I <sub>C</sub><br>I <sub>CM</sub> | -2.0<br>-3.0              | Adc  |
| Electrostatic Discharge                  | ESD                               | HBM Class 3<br>MM Class C |      |

#### THERMAL CHARACTERISTICS

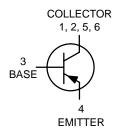
| Characteristic                                    | Symbol                            | Max            | Unit  |
|---|-----------------------------------|----------------|-------|
| Total Device Dissipation T <sub>A</sub> = 25°C    | P <sub>D</sub> (Note 1)           | 450            | mW    |
| Derate above 25°C                                 |                                   | 3.6            | mW/°C |
| Thermal Resistance,<br>Junction to Ambient        | R <sub>θJA</sub> (Note 1)         | 275            | °C/W  |
| Total Device Dissipation T <sub>A</sub> = 25°C    | P <sub>D</sub> (Note 2)           | 650            | mW    |
| Derate above 25°C                                 |                                   | 5.2            | mW/°C |
| Thermal Resistance,<br>Junction to Ambient        | R <sub>θJA</sub> (Note 2)         | 192            | °C/W  |
| Thermal Resistance,<br>Junction to Lead 6         | R <sub>θJL</sub>                  | 105            | °C/W  |
| Total Device Dissipation (Single Pulse < 10 sec.) | P <sub>D</sub> Single             | 1.4            | W     |
| Junction and Storage<br>Temperature Range         | T <sub>J</sub> , T <sub>stg</sub> | –55 to<br>+150 | °C    |

- 1. FR-4, Minimum Pad, 1 oz Coverage
- 2. FR-4, 1" Pad, 1 oz Coverage



http://onsemi.com

# 12 VOLTS 3.0 AMPS PNP TRANSISTOR





CASE 419B SOT-363/SC-88 STYLE 20

#### **DEVICE MARKING**



11 = Specific Device Coded = Date Code

#### **ORDERING INFORMATION**

| Device    | Package | Shipping         |
|-----------|---------|------------------|
| NSL12AWT1 | SOT-416 | 3000/Tape & Reel |

### **NSL12AW**

# **ELECTRICAL CHARACTERISTICS** ( $T_J = 25^{\circ}C$ unless otherwise noted)

| Characteristic  | Symbol                | Min               | Typical                 | Max                        | Unit |
|---|-----------------------|-------------------|-------------------------|----------------------------|------|
| OFF CHARACTERISTICS   |                       |                   |                         |                            |      |
| Collector–Emitter Breakdown Voltage (I <sub>C</sub> = –10 mAdc, I <sub>B</sub> = 0)   | V <sub>(BR)</sub> CEO | -12               | -15                     | _                          | Vdc  |
| Collector–Base Breakdown Voltage (I <sub>C</sub> = -0.1 mAdc, I <sub>E</sub> = 0)   | V <sub>(BR)</sub> CBO | -12               | -25                     | -                          | Vdc  |
| Emitter–Base Breakdown Voltage (I <sub>E</sub> = -0.1 mAdc, I <sub>C</sub> = 0)   | V <sub>(BR)EBO</sub>  | -5.0              | -7.0                    | -                          | Vdc  |
| Collector Cutoff Current $(V_{CB} = -12 \text{ Vdc}, I_E = 0)$  | I <sub>CBO</sub>      | -                 | -0.02                   | -0.1                       | μAdc |
| Collector–Emitter Cutoff Current (V <sub>CES</sub> = -12 Vdc, I <sub>E</sub> = 0)   | I <sub>CES</sub>      | -                 | -0.03                   | -0.1                       | μAdc |
| Emitter Cutoff Current<br>(V <sub>CES</sub> = -5.0 Vdc, I <sub>E</sub> = 0)   | I <sub>EBO</sub>      | _                 | -0.03                   | -0.1                       | μAdc |
| ON CHARACTERISTICS  |                       |                   |                         |                            |      |
| DC Current Gain (Note 3)<br>( $I_C = -0.5 \text{ A}, V_{CE} = -1.5 \text{ V}$ )<br>( $I_C = -0.8 \text{ A}, V_{CE} = -1.5 \text{ V}$ )<br>( $I_C = -1.0 \text{ A}, V_{CE} = -1.5 \text{ V}$ ) | h <sub>FE</sub>       | 100<br>100<br>100 | 180<br>165<br>160       | -<br>300<br>-              |      |
| Collector–Emitter Saturation Voltage (Note 3) $ \begin{array}{l} (I_C=-0.5~A,~I_B=-10~\text{mA})\\ (I_C=-0.8~A,~I_B=-16~\text{mA})\\ (I_C=-1.0~A,~I_B=-20~\text{mA}) \end{array} $            | V <sub>CE(sat)</sub>  | -<br>-<br>-       | -0.10<br>-0.14<br>-0.17 | -0.160<br>-0.235<br>-0.290 | V    |
| Base–Emitter Saturation Voltage (Note 3) (I <sub>C</sub> = -1.0 A, I <sub>B</sub> = -20 mA)   | V <sub>BE(sat)</sub>  | _                 | -0.84                   | -0.95                      | V    |
| Base–Emitter Turn–on Voltage (Note 3)<br>(I <sub>C</sub> = -1.0 A, V <sub>CE</sub> = -1.5 V)  | V <sub>BE(on)</sub>   | _                 | -0.81                   | -0.95                      | V    |
| Cutoff Frequency ( $I_C = -100 \text{ mA}$ , $V_{CE} = -5.0 \text{ V}$ , $f = 100 \text{ MHz}$ )  | f <sub>T</sub>        | _                 | 100                     | _                          | MHz  |
| Output Capacitance<br>(V <sub>CB</sub> = -1.5 V, f = 1.0 MHz)   | C <sub>obo</sub>      | _                 | 50                      | 65                         | pF   |

<sup>3.</sup> Pulsed Condition: Pulse Width < 300 μsec, Duty Cycle < 2%

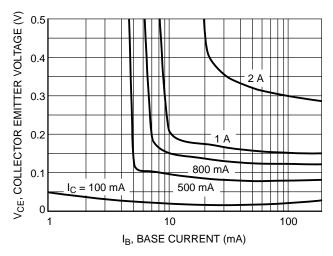


Figure 1. Collector Emitter Voltage versus

Base Current

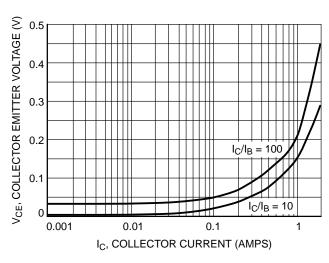


Figure 2. Collector Emitter Voltage versus
Collector Current

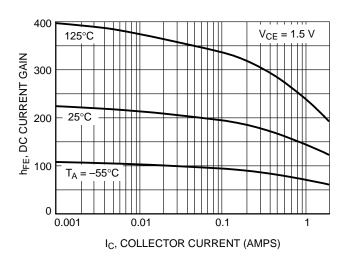


Figure 3. DC Current Gain versus Collector Current

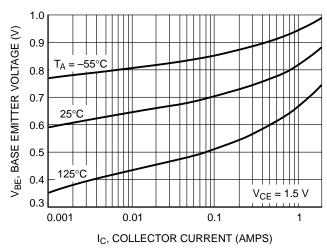


Figure 4. Base Emitter Voltage versus Collector Current

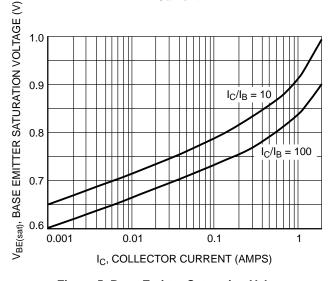


Figure 5. Base Emitter Saturation Voltage versus Base Current

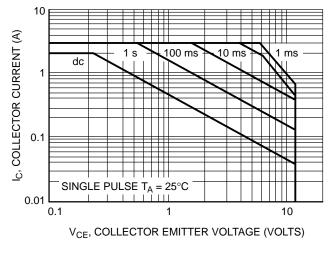


Figure 6. Safe Operating Area

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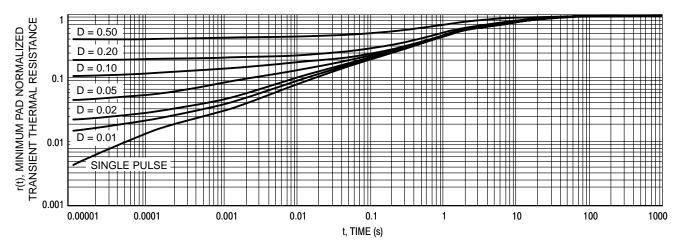
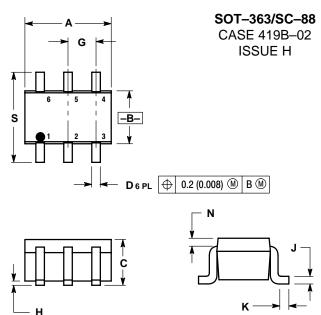


Figure 7. Normalized Thermal Response

#### PACKAGE DIMENSIONS



#### NOTES

- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14 5M 1982
- 2. CONTROLLING DIMENSION: INCH.

|     | INCHES    |       | MILLIN   | IETERS   |  |
|-----|-----------|-------|----------|----------|--|
| DIM | MIN       | MAX   | MIN      | MAX      |  |
| Α   | 0.071     | 0.087 | 1.80     | 2.20     |  |
| В   | 0.045     | 0.053 | 1.15     | 1.35     |  |
| С   | 0.031     | 0.043 | 0.80     | 1.10     |  |
| D   | 0.004     | 0.012 | 0.10     | 0.30     |  |
| G   | 0.026 BSC |       | 0.65 BSC |          |  |
| Н   |           | 0.004 |          | 0.10     |  |
| J   | 0.004     | 0.010 | 0.10     | 0.25     |  |
| K   | 0.004     | 0.012 | 0.10     | 0.30     |  |
| N   | 0.008 REF |       | 0.20     | 0.20 REF |  |
| S   | 0.079     | 0.087 | 2.00     | 2.20     |  |

STYLE 20 PIN 1. COLLECTOR

2. COLLECTOR3. BASE

FMITTER

COLLECTOR

COLLECTOR

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