

Epitaxial planar NPN silicon transistor

Description

• Complex type bipolar transistor

Feature

- Small package save PCB area
- Reduce quantity of parts and mounting cost
- Two 2SC5343 chips in SOT-363 package

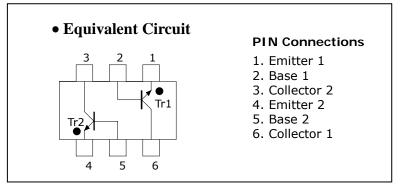
Ordering Information



Type NO.	Marking	Package Code
SUT485J	SS□	SOT-363

 \square : Year & Week Code

Equivalent circuit & PIN Connections



Absolute Maximum Ratings [Tr1, Tr2]

(Ta=25°C)

Characteristic	Symbol	Rating	Unit
Collector-base voltage	V _{CBO}	60	V
Collector-emitter voltage	V _{CEO}	50	V
Emitter-base voltage	V _{EBO}	5	V
Collector current	I _C	150	mA
Collector power dissipation	Pc [*]	200	mW
Junction temperature	T,	150	°C
Storage temperature range	T_{stg}	-55~150	°C

ℜ: Total rating

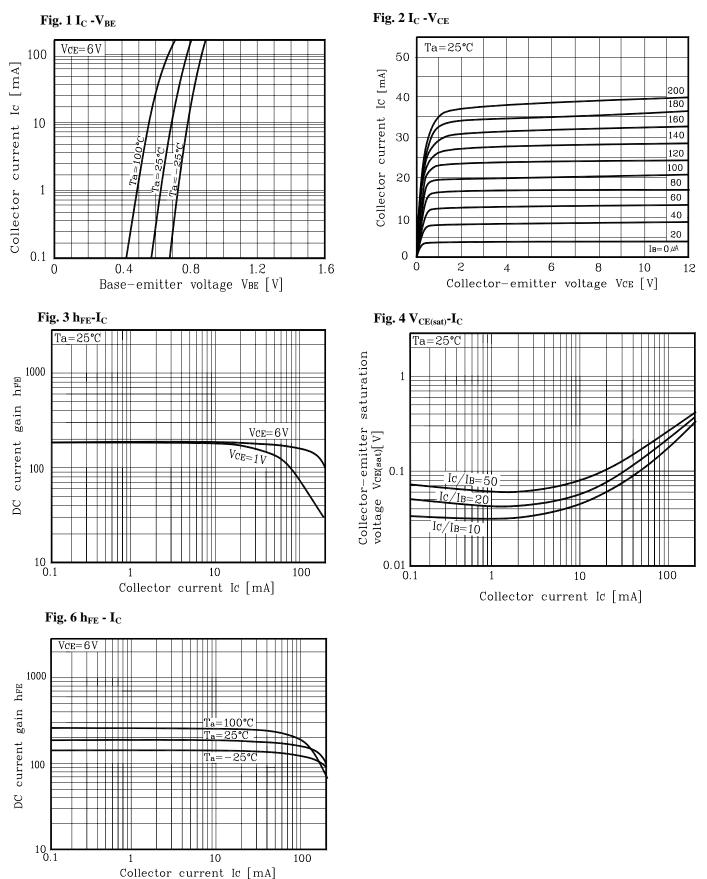
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Electrical Characteristics [Tr1, Tr2]

(Ta=25°C) Symbol **Test Condition** Min. Unit Characteristic Typ. Max. Collector-emitter breakdown voltage 50 -_ v $\mathsf{BV}_{\mathsf{CEO}}$ $I_C=1mA$, $I_B=0$ Collector cut-off current $\mathbf{I}_{\mathsf{CBO}}$ $V_{CB} = 60V, I_E = 0$ _ -0.1 μA Emitter cut-off current V_{EB} =5V, I_C =0 0.1 $\mathbf{I}_{\mathsf{EBO}}$ -μΑ 400 DC current gain h_{FE} V_{CE} =6V, I_{C} =2mA 120 _ _ 0.25 V Collector-emitter saturation voltage V_{CE(sat)} I_C =100mA, I_B =10mA _ _ Base-emitter voltage V_{BE} V_{CE} =6V, I_C =2mA -0.65 -V Transition frequency \mathbf{f}_{T} V_{CE} =10V, I_C =10mA _ 200 _ MHz 2 Collector output capacitance C_{ob} V_{CB} =10V, I_E =0, f=1MHz -pF

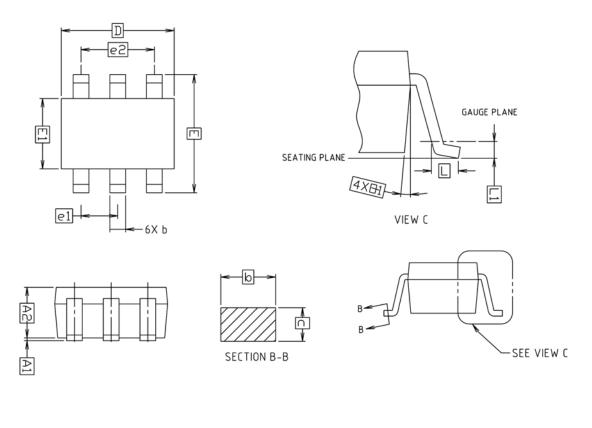
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Electrical Characteristic Curves [Tr1, Tr2]



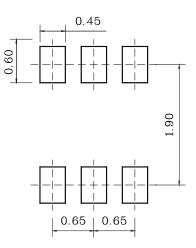
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Outline Dimension



SYMBOL	MILLIMETERS			NOTE
	MINIMUM	NOMINAL	MAXIMUM	NOTE
A1	0.00	-	0.10	
A2	0.90	0.95	1.00	
b	0.25	-	0.40	
с	0.10	-	0.25	
D	1.90	2.00	2.10	
E	1.95	2.10	2.25	
E1	1.15	1.25	1.35	
e1	0.65 BSC			
e2	1.30 BSC			
L	0.25	-	-	
L1	0.15 BSC			

* Recommend PCB solder land [Unit: mm]



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