TOSHIBA Transistor Silicon NPN Epitaxial Planar Type

# 2SC5317FT

VHF-UHF Band Low Noise Amplifier Applications (chip:  $f_T = 16$  GHz series)

Unit: mm

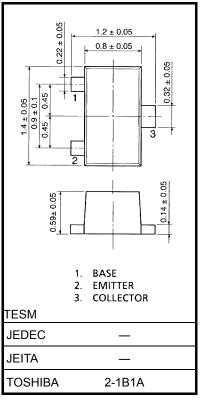
- Low Noise Figure :NF = 1.3dB (f = 2GHz)
- High Gain: $|S21e|^2 = 9dB (f = 2GHz)$

## **Absolute Maximum Ratings (Ta = 25°C)**

Characteristics	Symbol	Rating	Unit
Collector-Base voltage	$V_{CBO}$	8	V
Collector-Emitter voltage	V <sub>CEO</sub>	5	V
Emitter-Base voltage	V <sub>EBO</sub>	1.5	V
Collector-Current	IC	20	mA
Base-Current	ΙΒ	10	mA
Collector Power dissipation	PC	100	mW
Junction temperature	Tj	125	°C
Storage temperature Range	T <sub>stg</sub>	-55~125	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



Weight: 0.0022g (typ.)

## Microwave Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Transition Frequency	fT	V <sub>CE</sub> = 3 V, I <sub>C</sub> = 15 mA	9	_	_	GHz
Insertion Gain	S21e  <sup>2</sup> (1)	V <sub>CE</sub> = 3 V, I <sub>C</sub> = 15 mA, f = 1 GHz	12	15	_	- dB
	S21e 2 (2)	V <sub>CE</sub> = 3 V, I <sub>C</sub> = 15 mA, f = 2 GHz	6	9	_	
Noise Figure ——	NF (1)	V <sub>CE</sub> = 3 V, I <sub>C</sub> = 5 mA, f = 1 GHz	_	0.9	1.8	- dB
	NF (2)	$V_{CE}$ = 3 V, $I_{C}$ = 5 mA, f = 2 GHz	ı	1.3	2.2	

#### **Electrical Characteristics (Ta = 25°C)**

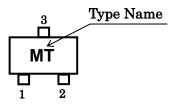
haracteristics	Symbol	CTest Condition	Min	Тур.	Max	Unit
Collector Cut-off Current	I <sub>CBO</sub>	V <sub>CB</sub> = 8 V, I <sub>E</sub> = 0	_	_	1	μΑ
Emitter Cut-off Current	I <sub>EBO</sub>	V <sub>EB</sub> = 1 V, I <sub>C</sub> = 0	_		1	μΑ
DC Current Gain	h <sub>FE</sub>	V <sub>CE</sub> = 3 V, I <sub>C</sub> = 15 mA	50	_	250	
Output Capacitance	C <sub>ob</sub>	V <sub>CB</sub> = 2.5 V, I <sub>E</sub> = 0, f = 1 MHz (Note)	_	0.6	_	pF
Reverse Transistor Capacitance	C <sub>re</sub>		-	0.4	0.85	pF

Note:  $C_{re}$  is measured by 3 terminal method with capacitance Bridge.

# Caution

This device is sensitive to electrostatic discharge. Please handle with caution.

# Marking



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20070701-EN GENERAL

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