



P-Channel Enhancement-Mode Vertical DMOS FETs

Ordering Information

BV _{DSS} / BV _{DGS}	R _{DS(ON)} (max)	V _{GS(th)} (max)	Order Number/Package		
			TO-236AB*	TO-92	Die
-50V	6Ω	-2.0V	TP2105K1	TP2105N3	TP2105ND

Product marking for SOT-23: <div style="border: 1px solid black; padding: 2px; display: inline-block;">P1L*</div> where * = 2-week alpha date code
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*Same as SOT-23. All units shipped on 3,000 piece carrier tape reels.

Features

- Free from secondary breakdown
- Low power drive requirement
- Ease of paralleling
- Low C_{iss} and fast switching speeds
- Excellent thermal stability
- Integral Source-Drain diode
- High input impedance and high gain
- Complementary N- and P-channel devices

Applications

- Logic level interface – ideal for TTL and CMOS
- Solid state relays
- Battery operated systems
- Photo voltaic drive
- Analog switches
- General purpose line driver
- Telecom switches

Absolute Maximum Ratings

Drain-to-Source Voltage	BV _{DSS}
Drain-to-Gate Voltage	BV _{DGS}
Gate-to-Source Voltage	± 20V
Operating and Storage Temperature	-55°C to +150°C
Soldering Temperature*	300°C

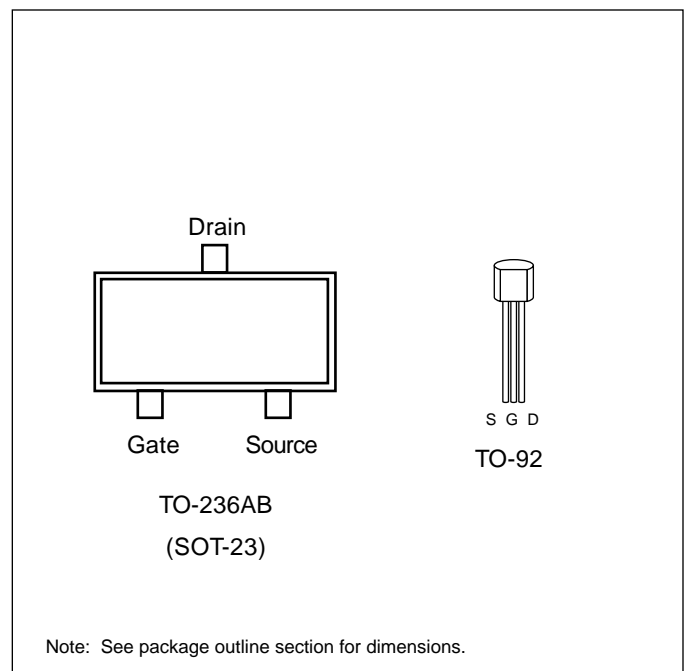
* Distance of 1.6 mm from case for 10 seconds.

Advanced DMOS Technology

These enhancement-mode (normally-off) transistors utilize a vertical DMOS structure and Supertex's well-proven silicon-gate manufacturing process. This combination produces devices with the power handling capabilities of bipolar transistors and with the high input impedance and positive temperature coefficient inherent in MOS structures, these devices are free from thermal runaway and thermally-induced secondary breakdown.

Supertex vertical DMOS FETs are ideally suited to a wide range of switching and amplifying applications where high breakdown voltage, high input impedance, low input capacitance, and fast switching speeds are desired.

Package Options



Note: See package outline section for dimensions.

Thermal Characteristics

Package	I _D (continuous)*	I _D (pulsed)	Power Dissipation @ T _A = 25°C	θ _{ja} °C/W	θ _{jc} °C/W	I _{DR} *	I _{DRM}
SOT-23	-0.16A	-0.8A	0.36W	350	200	-0.16A	-0.8A
TO-92	-0.25A	-1.0A	0.74W	170	125	-0.25A	-1.0A

* I_D (continuous) is limited by max rated T_j.

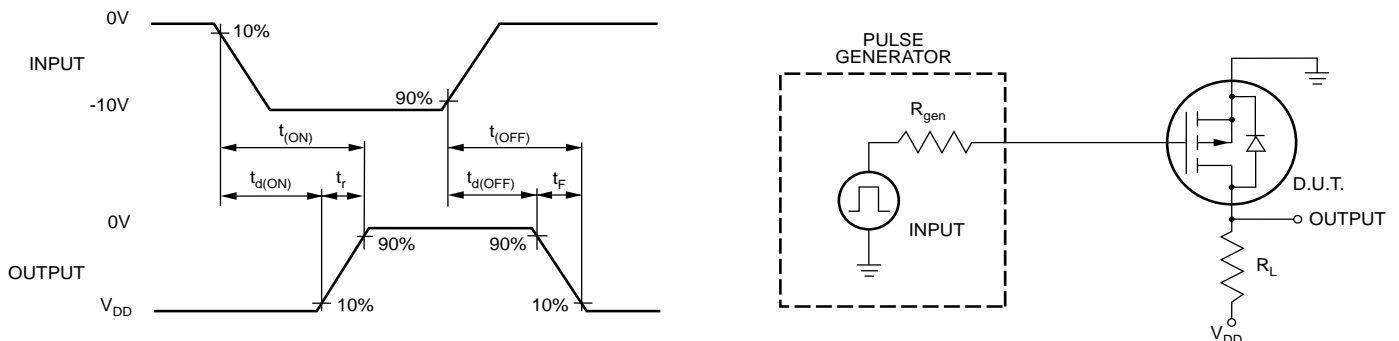
Electrical Characteristics (@ 25°C unless otherwise specified)

Symbol	Parameter	Min	Typ	Max	Unit	Conditions
BV _{DSS}	Drain-to-Source Breakdown Voltage	-50			V	V _{GS} = 0V, I _D = -1.0mA
V _{GS(th)}	Gate Threshold Voltage	-1.0		-2.0	V	V _{GS} = V _{DS} , I _D = -1.0mA
ΔV _{GS(th)}	Change in V _{GS(th)} with Temperature		5.8	6.5	mV/°C	V _{GS} = V _{DS} , I _D = -1.0mA
I _{GSS}	Gate Body Leakage		-1.0	-100	nA	V _{GS} = ±20V, V _{DS} = 0V
I _{DSS}	Zero Gate Voltage Drain Current			-10	μA	V _{GS} = 0V, V _{DS} = Max Rating
				-1	mA	V _{GS} = 0V, V _{DS} = 0.8 Max Rating T _A = 125°C
I _{D(ON)}	ON-State Drain Current	-0.6			A	V _{GS} = -10V, V _{DS} = -25V
R _{DS(ON)}	Static Drain-to-Source ON-State Resistance			10	Ω	V _{GS} = -4.5V, I _D = -50mA
				6	Ω	V _{GS} = -10V, I _D = -0.5A
ΔR _{DS(ON)}	Change in R _{DS(ON)} with Temperature		0.55	1.0	%/°C	V _{GS} = -10V, I _D = -0.5A
G _{FS}	Forward Transconductance	150	200		mS	V _{DS} = -25V, I _D = -0.5A
C _{ISS}	Input Capacitance		35	60	pF	V _{GS} = 0V, V _{DS} = -25V f = 1 MHz
C _{OSS}	Common Source Output Capacitance		22	30		
C _{RSS}	Reverse Transfer Capacitance		8	10		
t _{d(ON)}	Turn-ON Delay Time		4	6	ns	V _{DD} = -25V I _D = -0.5A R _{GEN} = 25Ω
t _r	Rise Time		4	8		
t _{d(OFF)}	Turn-OFF Delay Time		5	9		
t _f	Fall Time		5	8		
V _{SD}	Diode Forward Voltage Drop		-1.2	-2.0	V	V _{GS} = 0V, I _{SD} = -0.5A
t _{rr}	Reverse Recovery Time		400		ns	V _{GS} = 0V, I _{SD} = -0.5A

Notes:

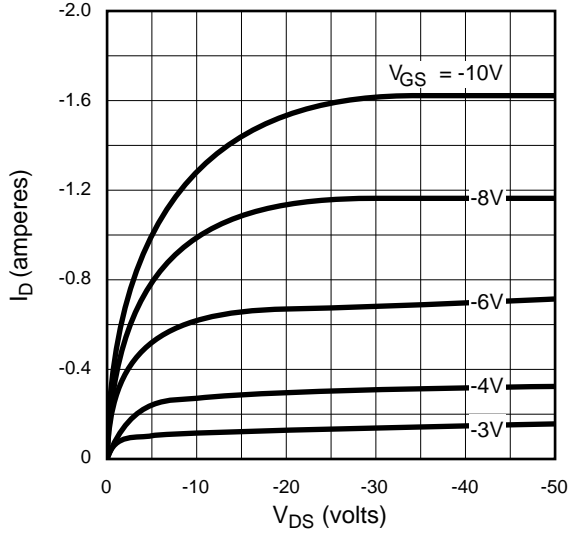
1. All D.C. parameters 100% tested at 25°C unless otherwise stated. (Pulse test: 300μs pulse, 2% duty cycle.)
2. All A.C. parameters sample tested.

Switching Waveforms and Test Circuit

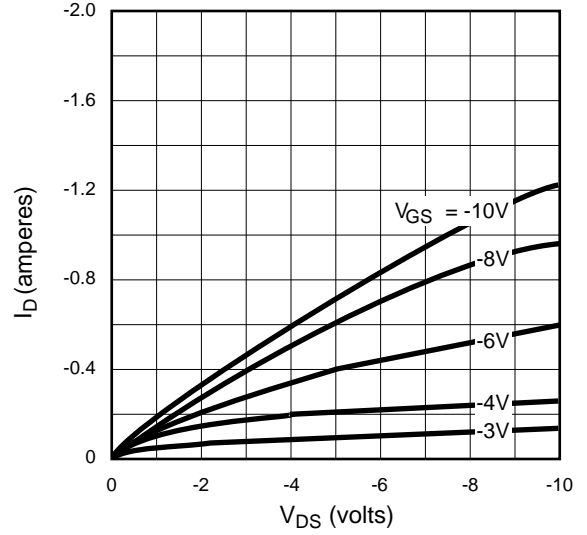


Typical Performance Curves

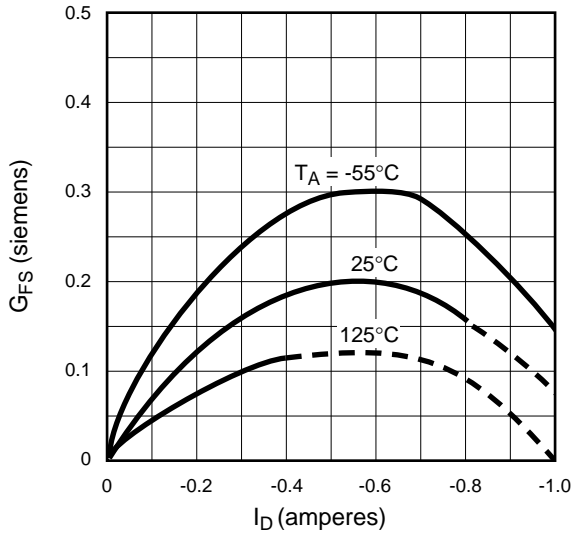
Output Characteristics



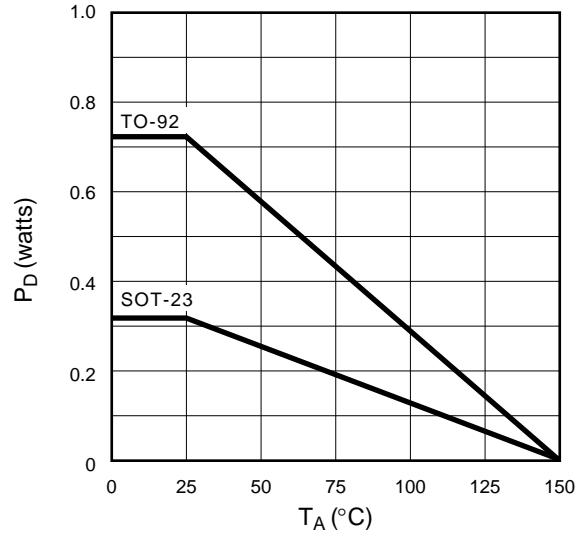
Saturation Characteristics



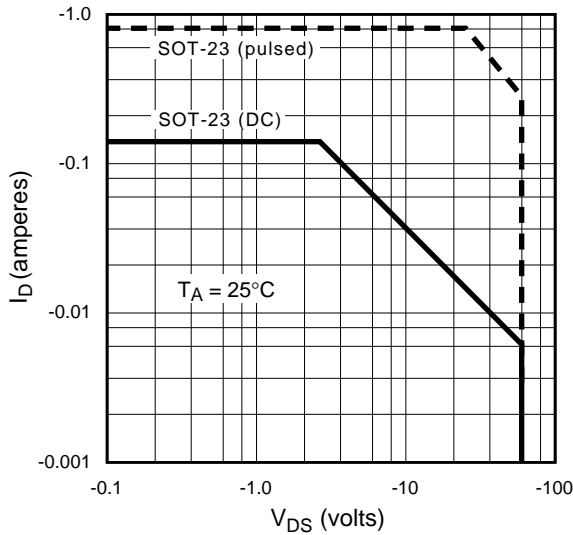
Transconductance vs. Drain Current



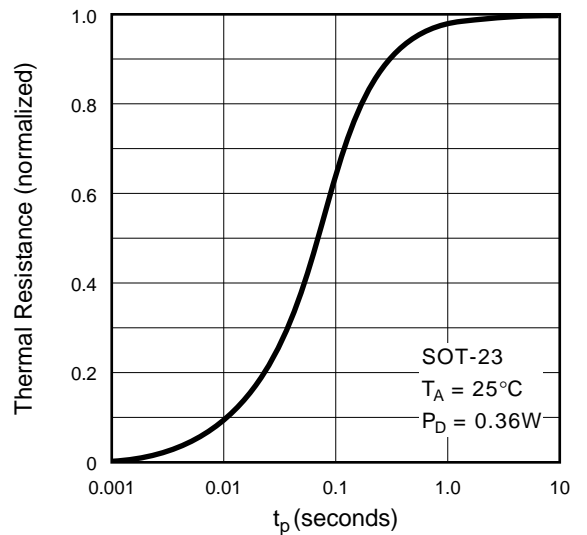
Power Dissipation vs. Temperature



Maximum Rated Safe Operating Area

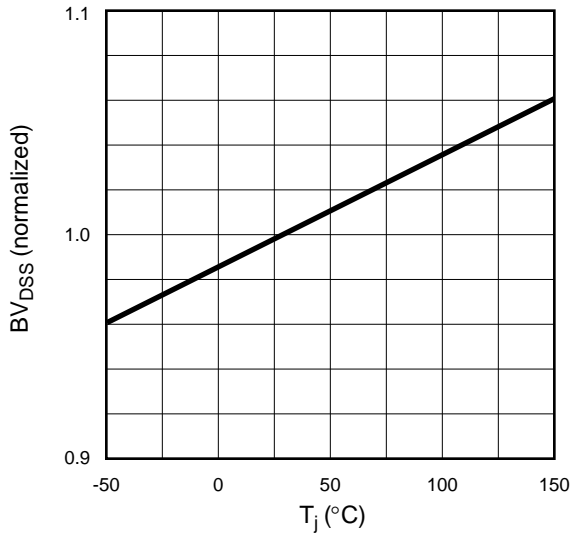


Thermal Response Characteristics

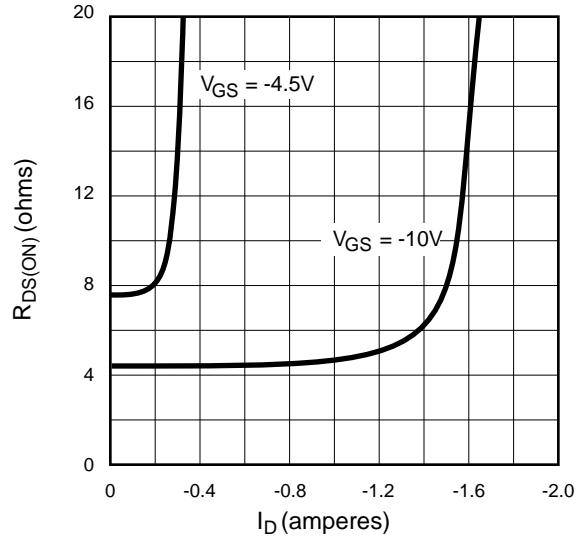


Typical Performance Curves

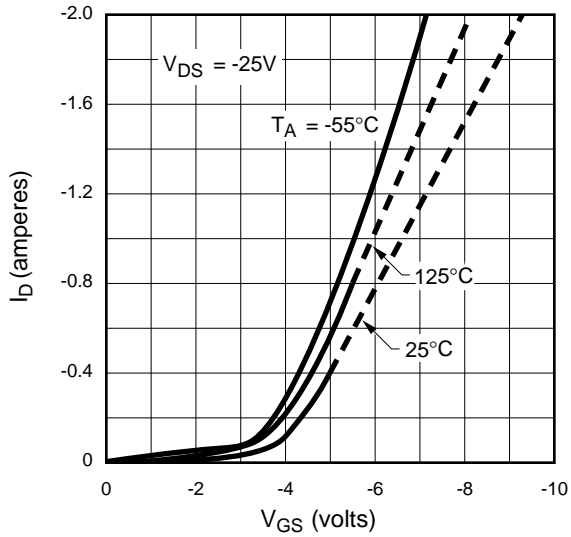
BV_{DSS} Variation with Temperature



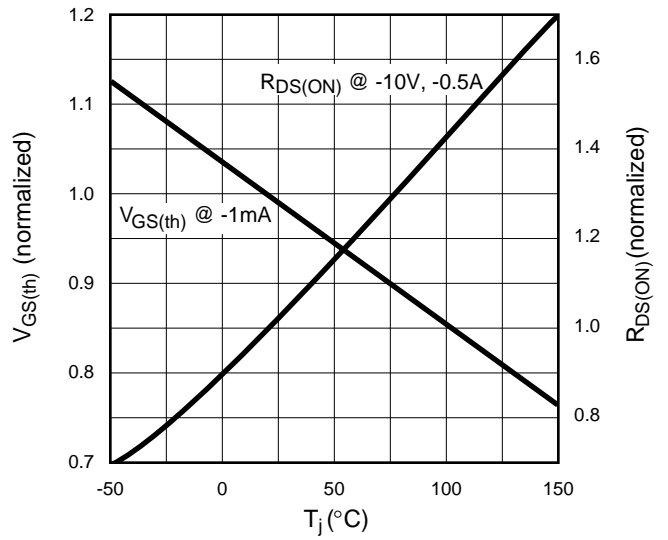
On-Resistance vs. Drain Current



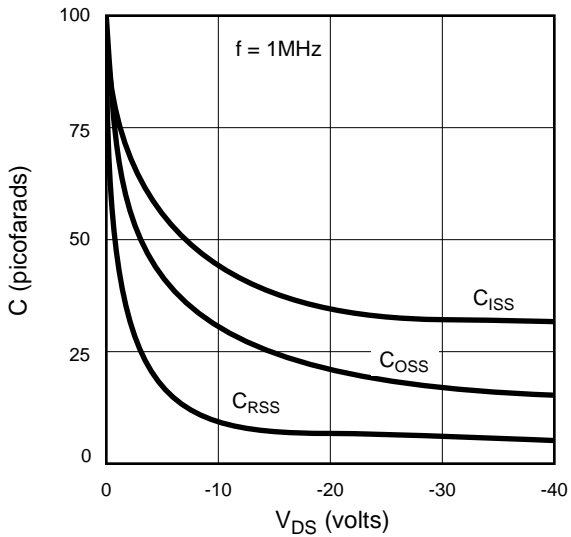
Transfer Characteristics



V_{GS(th)} and R_{DS(ON)} Variation with Temperature



Capacitance vs. Drain-to-Source Voltage



Gate Drive Dynamic Characteristics

