Preferred Device

Power MOSFET 300 mAmps, 20 Volts

P-Channel SC-70/SOT-323

These miniature surface mount MOSFETs low $R_{DS(on)}$ assure minimal power loss and conserve energy, making these devices ideal for use in small power management circuitry. Typical applications are dc-dc converters, power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

Features

- Low R_{DS(on)} Provides Higher Efficiency and Extends Battery Life
- Miniature SC-70/SOT-323 Surface Mount Package Saves Board Space
- Pb-Free Package is Available

MAXIMUM RATINGS (T_{.1} = 25°C unless otherwise noted)

Rating	Symbol	Value	Unit
Drain-to-Source Voltage	V_{DSS}	20	Vdc
Gate-to-Source Voltage - Continuous	V_{GS}	± 20	Vdc
	I _D I _D	300 240 750	mAdc
Total Power Dissipation @ T _A = 25°C (Note 1) Derate above 25°C	P _D	150 1.2	mW mW/°C
Operating and Storage Temperature Range	T _J , T _{stg}	- 55 to 150	°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	833	°C/W
Maximum Lead Temperature for Soldering Purposes, for 10 seconds	TL	260	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

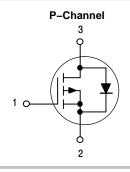
 Mounted on G10/FR4 glass epoxy board using minimum recommended footprint.



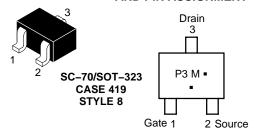
ON Semiconductor®

http://onsemi.com

300 mAMPS, 20 VOLTS $R_{DS(on)} = 2.2 \Omega$



MARKING DIAGRAM AND PIN ASSIGNMENT



P3 = Specific Device Code

M = Date Code*

= Pb-Free Package

(Note: Microdot may be in either location)
*Date Code orientation may vary depending

upon manufacturing location.

ORDERING INFORMATION

Device	Package	Shipping [†]
MMBF2202PT1	SC-70/ SOT-323	3000 Tape & Reel
MMBF2202PT1G	SC-70/ SOT-323 (Pb-Free)	3000 Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Preferred devices are recommended choices for future use and best overall value.

$\textbf{ELECTRICAL CHARACTERISTICS} \ (T_{A} = 25^{\circ}\text{C unless otherwise noted})$

Char	Symbol	Min	Тур	Max	Unit	
OFF CHARACTERISTICS		•				
Drain-to-Source Breakdown Voltage (V _{GS} = 0 Vdc, I _D = 10 μA)	V _{(BR)DSS}	20	_	_	Vdc	
Zero Gate Voltage Drain Current $(V_{DS} = 16 \text{ Vdc}, V_{GS} = 0 \text{ Vdc})$ $(V_{DS} = 16 \text{ Vdc}, V_{GS} = 0 \text{ Vdc}, T_{J} = 0 \text{ Vdc})$	ero Gate Voltage Drain Current $(V_{DS} = 16 \text{ Vdc}, V_{GS} = 0 \text{ Vdc})$ $(V_{DS} = 16 \text{ Vdc}, V_{GS} = 0 \text{ Vdc}, T_{J} = 125^{\circ}\text{C})$		_ _	_ _	1.0 10	μAdc
Gate-Body Leakage Current (V _{GS} =	± 20 Vdc, V _{DS} = 0)	I _{GSS}	_	_	±100	nAdc
ON CHARACTERISTICS (Note 2)		- 1	•	•	•	•
Gate Threshold Voltage $(V_{DS} = V_{GS}, I_D = 250 \mu Adc)$	V _{GS(th)}	1.0	1.7	2.4	Vdc	
	r _{DS(on)}	_ _	1.5 2.0	2.2 3.5	Ω	
Forward Transconductance (V _{DS} = 1	9 _{FS}	_	600	_	mMhos	
DYNAMIC CHARACTERISTICS		•				
Input Capacitance	(V _{DS} = 5.0 V)	C _{iss}	_	50	_	pF
Output Capacitance	(V _{DS} = 5.0 V)	C _{oss}	_	45	_	
Transfer Capacitance	(V _{DG} = 5.0 V)	C _{rss}	_	20	_	
SWITCHING CHARACTERISTICS (Note 3)		•			<u>, </u>
Turn-On Delay Time		t _{d(on)}	_	2.5	_	ns
Rise Time	$(V_{DD} = -15 \text{ Vdc},$	t _r	_	1.0	_	
Turn-Off Delay Time	$R_L = 75 \Omega$, $I_D = 200$ mAdc, $V_{GEN} = -10 V$, $R_G = 6.0 \Omega$)	t _{d(off)}	_	16	_	
Fall Time		t _f	_	8.0	_	
Gate Charge (See Figure 5)	$(V_{DS} = 16 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 200 \text{ mA})$	Q _T	-	2700	_	pC
SOURCE-DRAIN DIODE CHARAC	TERISTICS	•	•	•	•	•
Continuous Current	Is	_	_	0.3	А	
Pulsed Current	I _{SM}	_	-	0.75		
Forward Voltage (Note 3)	V _{SD}	_	1.5	-	V	

^{2.} Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2%.

TYPICAL CHARACTERISTICS

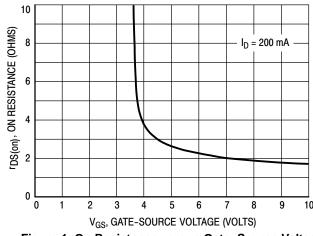


Figure 1. On Resistance versus Gate-Source Voltage

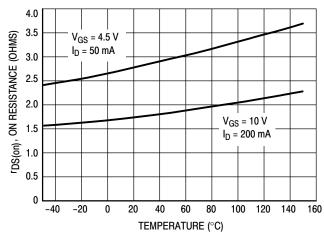


Figure 2. On Resistance versus Temperature

^{3.} Switching characteristics are independent of operating junction temperature.

TYPICAL CHARACTERISTICS

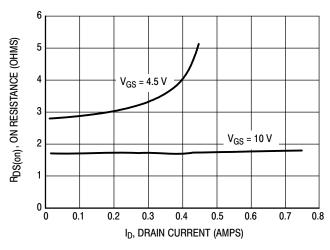


Figure 3. On Resistance versus Drain Current

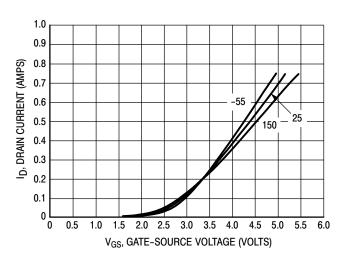


Figure 4. Transfer Characteristics

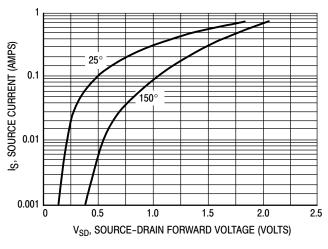


Figure 5. Source-Drain Forward Voltage

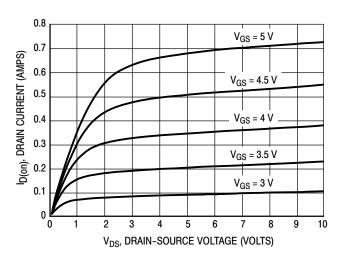


Figure 6. On Region Characteristics

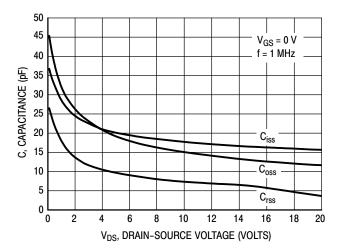
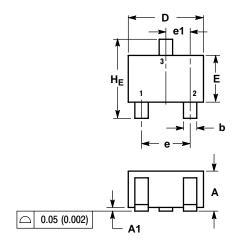
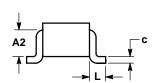


Figure 7. Capacitance Variation

PACKAGE DIMENSIONS

SC-70 (SOT-323) CASE 419-04 ISSUE M





NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M. 1982.
- 2. CONTROLLING DIMENSION: INCH.

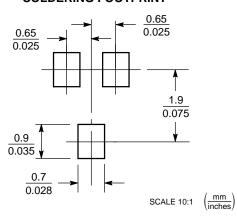
	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.80	0.90	1.00	0.032	0.035	0.040
A1	0.00	0.05	0.10	0.000	0.002	0.004
A2	0.7 REF			0.028 REF		
b	0.30	0.35	0.40	0.012	0.014	0.016
С	0.10	0.18	0.25	0.004	0.007	0.010
D	1.80	2.10	2.20	0.071	0.083	0.087
E	1.15	1.24	1.35	0.045	0.049	0.053
е	1.20	1.30	1.40	0.047	0.051	0.055
e1	0.65 BSC			0.026 BSC		
L	0.425 REF			0.017 REF		
He	2.00	2 10	2 40	0.079	0.083	0.095

STYLE 8:

PIN 1. GATE

2. SOURCE 3. DRAIN

SOLDERING FOOTPRINT*



*For additional information on our Pb—Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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