MBRA140T3

Surface Mount Schottky Power Rectifier

SMA Power Surface Mount Package

... employing the Schottky Barrier principle in a large area metal-to-silicon power diode. State of the art geometry features epitaxial construction with oxide passivation and metal overlay contact. Ideally suited for low voltage, high frequency rectification, or as free wheeling and polarity diodes in surface mount applications where compact size and weight are critical to the system.

- Small Compact Surface Mountable Package with J-Bent Leads
- Rectangular Package for Automated Handling
- Highly Stable Oxide Passivated Junction
- Very Low Forward Voltage Drop
- Guardring for Stress Protection

Mechanical Characteristics:

- Case: Epoxy, Molded
- Weight: 70 mg (approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Shipped in 12 mm tape, 5000 units per 13 inch reel
- Polarity: Cathode Lead Indicated by Either Notch in Plastic Body or Polarity Band
- Marking: B14

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	VRRM VRWM VR	40	V
Average Rectified Forward Current (At Rated V _R , T _C = 95°C)	lo	1.0	Α
Peak Repetitive Forward Current (At Rated V _R , Square Wave, 20 kHz, T _C = 100°C)	I _{FRM}	2.0	A
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I _{FSM}	30	Α
Storage/Operating Case Temperature	T _{stg} , T _C	-55 to +150	°C
Operating Junction Temperature	TJ	-55 to +125	°C
Voltage Rate of Change (Rated V _R , T _J = 25°C)	dv/dt	10,000	V/μs



http://onsemi.com

SCHOTTKY BARRIER RECTIFIER 1.0 AMPERES 40 VOLTS



SMA CASE 403D **PLASTIC**

MARKING DIAGRAM



B14 = Device Code

ORDERING INFORMATION

Device	Package	Shipping
MBRA140T3	SMA	5000/Tape & Reel

MBRA140T3/D

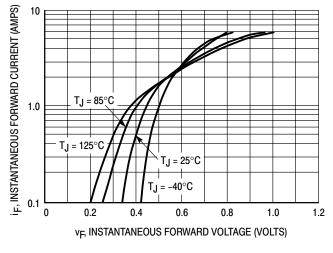
THERMAL CHARACTERISTICS

Characteristic	Symbol	Value	Unit
Thermal Resistance — Junction–to–Lead (Note 1.) Thermal Resistance — Junction–to–Ambient (Note 1.)	$R_{ hetaJL}$ $R_{ hetaJA}$	35 86	°C/W

ELECTRICAL CHARACTERISTICS

Maximum Instantaneous Forward Voltage (Note 2.)		٧F	T _J = 25°C	T _J = 100°C	Volts
see Figure 2 for other Values	(I _F = 1.0 A) (I _F = 2.0 A)		0.55 0.71	0.505 0.74	
Maximum Instantaneous Reverse Current		IR	T _J = 25°C	T _J = 100°C	mA
see Figure 4 for other Values	$(V_R = 40 \text{ V})$ $(V_R = 20 \text{ V})$		0.5 0.1	10 4.0	

- Mounted on 2" Square PC Board with 1" Square Total Pad Size, PC Board FR4.
 Pulse Test: Pulse Width ≤ 250 μs, Duty Cycle ≤ 2.0%.



IF, INSTANTANEOUS FORWARD CURRENT (AMPS) 10 1.0 TJ = 85°C T_{.J} = 25°C 0.6 0.8 1.0 1.2 V_F, MAXIMUM INSTANTANEOUS FORWARD VOLTAGE (VOLTS)

Figure 1. Typical Forward Voltage

Figure 2. Maximum Forward Voltage

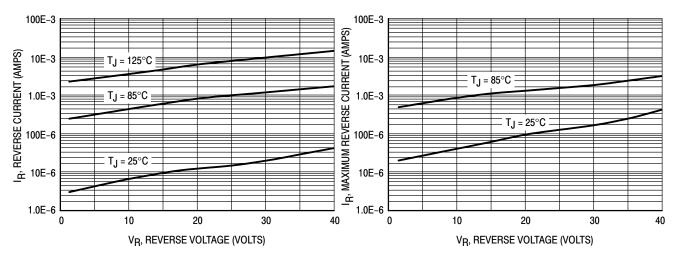
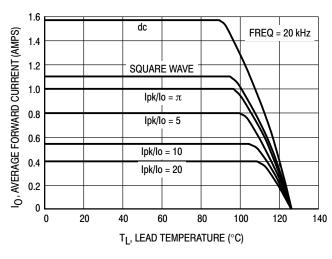


Figure 3. Typical Reverse Current

Figure 4. Maximum Reverse Current

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1.0 P_{FO}, AVERAGE POWER DISSIPATION (WATTS) 0.9 SQUARE WAVE dc 8.0 $lpk/lo = \pi$ 0.7 lpk/lo = 50.6 lpk/lo = 10 0.5 0.4 lpk/lo = 200.3 0.2 0.1 0.2 0.6 8.0 1.0 1.2 1.4 1.6 IO, AVERAGE FORWARD CURRENT (AMPS)

Figure 5. Current Derating

Figure 6. Forward Power Dissipation

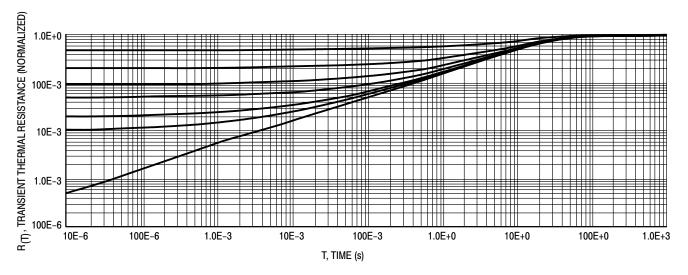


Figure 7. Thermal Response

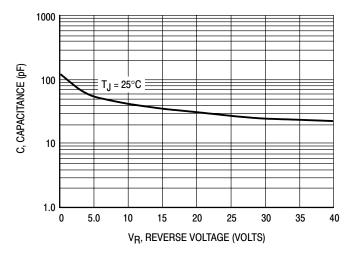
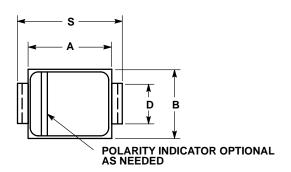


Figure 8. Capacitance

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PACKAGE DIMENSIONS

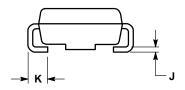
SMA CASE 403D-02 ISSUE A

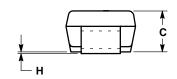


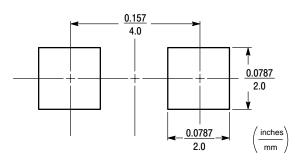
NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: INCH. 403D-01 OBSOLETE, NEW STANDARD IS 403D-02.

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.160	0.180	4.06	4.57	
В	0.090	0.115	2.29	2.92	
С	0.075	0.095	1.91	2.41	
D	0.050	0.064	1.27	1.63	
Н	0.002	0.006	0.05	0.15	
J	0.006	0.016	0.15	0.41	
K	0.030	0.060	0.76	1.52	
S	0.190	0.220	4.83	5.59	







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