Silicon N-Channel MOS FET

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ADE-208-268 1st. Edition

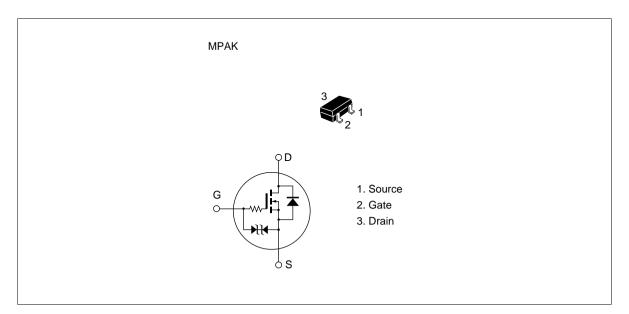
Application

Low frequency power switching

Features

- Low on-resistance
- Small package
- Low drive current
- 4 V gate drive device can be driven from 5 V source.
- Suitable for low signal load switch

Outline





Absolute Maximum Ratings ($Ta = 25^{\circ}C$)

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	30	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	I _D	0.2	А
Drain peak current	I D(pulse) *1	0.4	А
Body to drain diode reverse drain current	l _{DR}	0.2	А
Channel dissipation	Pch*2	150	mW
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes 1. PW \leq 100 μ s, duty cycle \leq 10 %

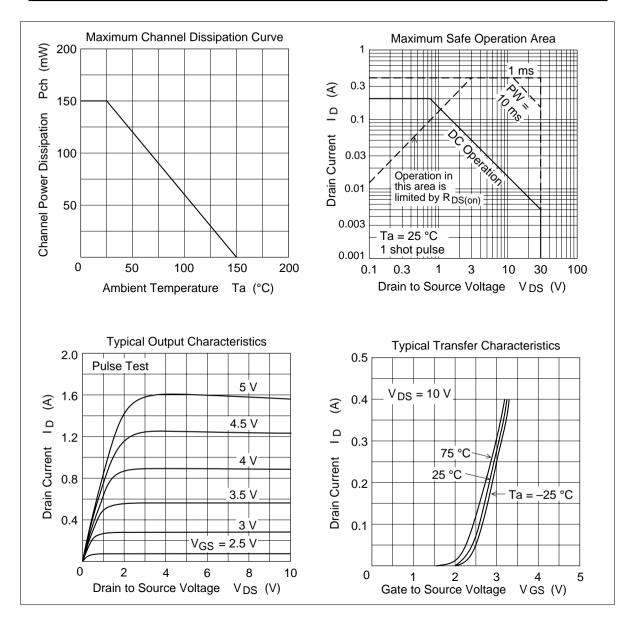
2. Marking is "ZE-".

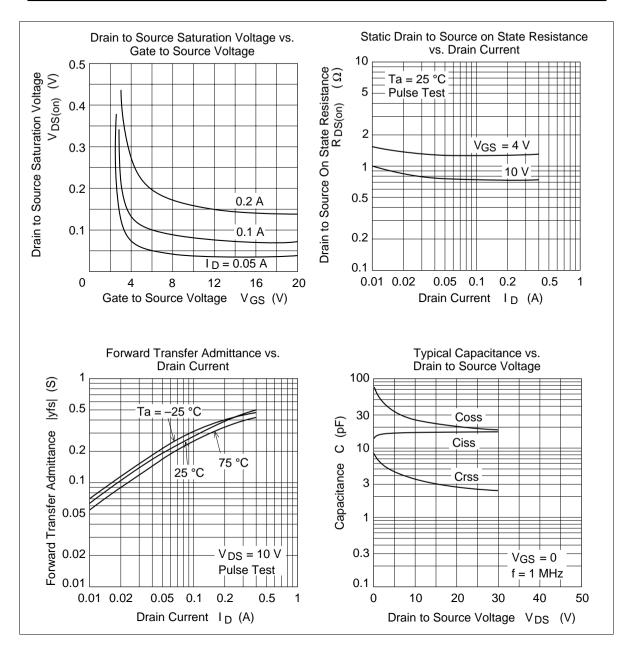
Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{\rm (BR)DSS}$	30	_	_	V	$I_{\rm D} = 100 \ \mu A, \ V_{\rm GS} = 0$
Gate to source breakdown voltage	$V_{\rm (BR)GSS}$	±20	_	—	V	$I_{g} = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I _{GSS}	_	_	±2	μΑ	$V_{GS} = \pm 16 \text{ V}, \text{ V}_{DS} = 0$
Zero gate voltage drain current	I _{DSS}	—	—	1	μΑ	$V_{\rm DS} = 30$ V, $V_{\rm GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	1.0	—	2.0	V	$I_{\rm D} = 10 \ \mu A, \ V_{\rm DS} = 5 \ V$
Static drain to source on state resistance	$R_{\text{DS(on)}}$	_	1.4	2.5	Ω	I _D = 20 mA V _{GS} = 4 V ^{*1}
		_	1.0	1.4	Ω	$I_{D} = 10 \text{ mA}$ $V_{GS} = 10 \text{ V}^{*1}$
Input capacitance	Ciss	—	17.8	—	pF	V _{DS} = 10 V
Output capacitance	Coss	_	25.4	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	3.7	—	pF	f = 1 MHz
Turn-on delay time	t _{d(on)}	—	50	—	ns	I _D = 0.1 A
Rise time	t,	—	125	—	ns	V _{GS} = 10 V
Turn-off delay time	t _{d(off)}	_	660	—	ns	R _L = 100 Ω
Fall time	t _f	_	400	_	ns	PW = 2 μs

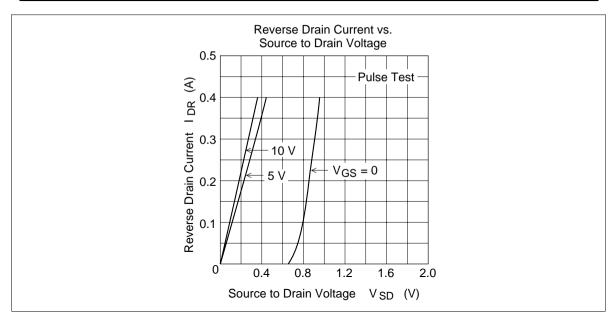
Note 1. Pulse Test

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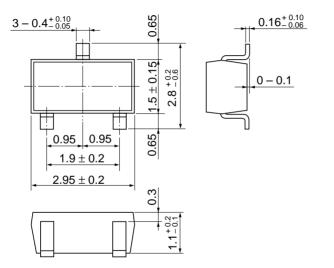


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Unit: mm



Hitachi Code	MPAK
JEDEC	
EIAJ	Conforms
Weight (reference value)	0.011 g

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