

# MOS FIELD EFFECT TRANSISTOR $\mu$ PA1918

## P-CHANNEL MOS FIELD EFFECT TRANSISTOR FOR SWITCHING

#### **DESCRIPTION**

The  $\mu$ PA1918 is a switching device, which can be driven directly by a 4.0 V power source.

This device features a low on-state resistance and excellent switching characteristics, and is suitable for applications such as power switch of portable machine and so on.

#### **FEATURES**

- 4.0 V drive available
- · Low on-state resistance

 $R_{DS(on)1} = 143 \text{ m}\Omega \text{ MAX}. \text{ (Vgs} = -10 \text{ V}, \text{ ID} = -2.0 \text{ A)}$ 

 $R_{DS(on)2} = 179 \text{ m}\Omega$  MAX. (Vgs = -4.5 V, ID = -2.0 A)

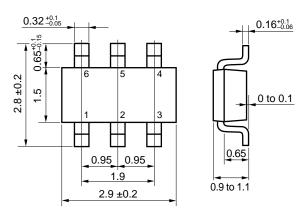
 $R_{DS(on)3} = 190 \text{ m}\Omega \text{ MAX.} \text{ (Vgs} = -4.0 \text{ V, ID} = -2.0 \text{ A)}$ 

#### ORDERING INFORMATION

PART NUMBER	PACKAGE
μPA1918TE	SC-95 (Mini Mold Thin Type)

Marking: TS

#### PACKAGE DRAWING (Unit: mm)

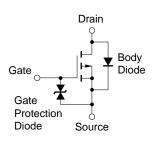


1, 2, 5, 6 : Drain 3 : Gate 4 : Source

#### ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (Ves = 0 V)	VDSS	-60	V
Gate to Source Voltage (Vps = 0 V)	Vgss	∓20	V
Drain Current (DC) (T <sub>A</sub> = 25°C)	ID(DC)	∓3.5	Α
Drain Current (pulse) Note1	ID(pulse)	∓14	Α
Total Power Dissipation	P <sub>T1</sub>	0.2	W
Total Power Dissipation Note2	P <sub>T2</sub>	2.0	W
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	-55 to +150	°C

#### **EQUIVALENT CIRCUIT**



**Notes 1.** PW  $\leq$  10  $\mu$ s, Duty Cycle  $\leq$  1%

**2.** Mounted on FR-4 board,  $t \le 5$  sec.

**Remark** The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

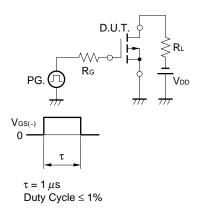
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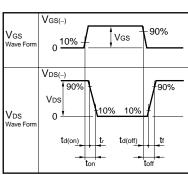


#### **ELECTRICAL CHARACTERISTICS (TA = 25°C)**

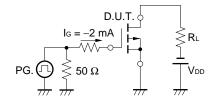
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	Inss	Vps = -60 V, Vgs = 0 V			-1.0	μΑ
Gate Leakage Current	lgss	V <sub>GS</sub> = ∓20 V, V <sub>DS</sub> = 0 V			∓10	μΑ
Gate Cut-off Voltage	V <sub>GS(off)</sub>	$V_{DS} = -10 \text{ V}, \text{ ID} = -1.0 \text{ mA}$	-1.5	-1.9	-2.5	V
Forward Transfer Admittance	<b>y</b> fs	V <sub>DS</sub> = -10 V, I <sub>D</sub> = -2.0 A	3.0	6.2		S
Drain to Source On-state Resistance	RDS(on)1	Vgs = -10 V, ID = -2.0 A		114	143	mΩ
	RDS(on)2	Vgs = -4.5 V, ID = -2.0 A		134	179	mΩ
	RDS(on)3	Vgs = -4.0 V, ID = -2.0 A		142	190	mΩ
Input Capacitance	Ciss	Vps = -10 V		666		pF
Output Capacitance	Coss	V <sub>G</sub> s = 0 V		120		pF
Reverse Transfer Capacitance	Crss	f = 1.0 MHz		58		pF
Turn-on Delay Time	td(on)	V <sub>DD</sub> = -30 V, I <sub>D</sub> = -2.0 A		12		ns
Rise Time	<b>t</b> r	V <sub>GS</sub> = -10 V		5		ns
Turn-off Delay Time	td(off)	$R_G = 10 \Omega$		58		ns
Fall Time	<b>t</b> f			27		ns
Total Gate Charge	Q <sub>G</sub>	V <sub>DD</sub> = -48 V		12		nC
Gate to Source Charge	Qgs	V <sub>G</sub> S = −10 V		1.5		nC
Gate to Drain Charge	Q <sub>GD</sub>	I <sub>D</sub> = -3.5 A		3.5		nC
Body Diode Forward Voltage	V <sub>F(S-D)</sub>	IF = 3.5 A, VGS = 0 V		0.87		V

#### TEST CIRCUIT 1 SWITCHING TIME

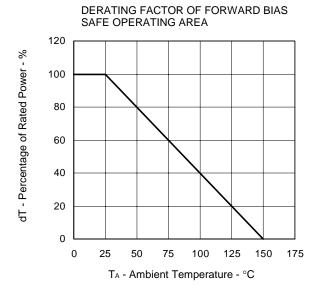


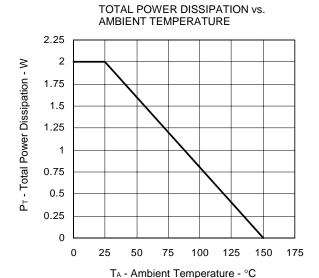


#### **TEST CIRCUIT 2 GATE CHARGE**

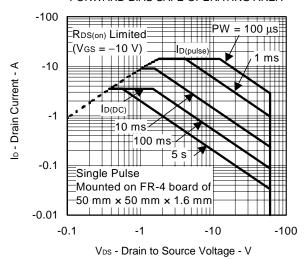


#### TYPICAL CHARACTERISTICS (TA = 25°C)

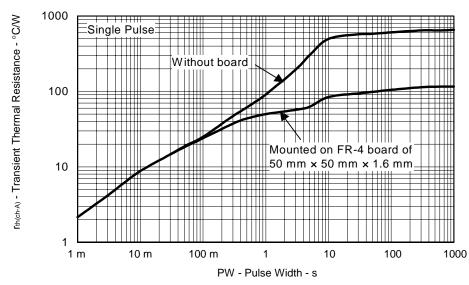




#### FORWARD BIAS SAFE OPERATING AREA

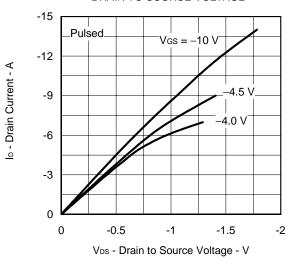


#### TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH

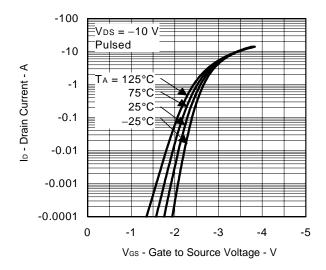


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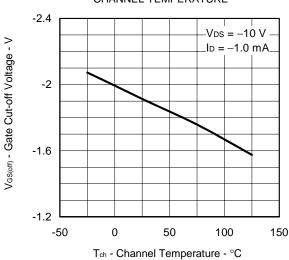
#### DRAIN CURRENT vs. DRAIN TO SOURCE VOLTAGE



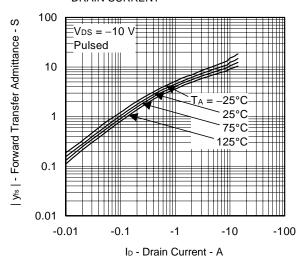
#### FORWARD TRANSFER CHARACTERISTICS



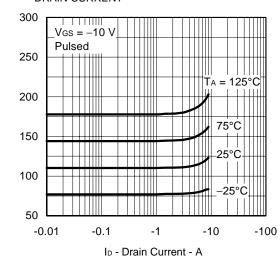
## GATE CUT-OFF VOLTAGE vs. CHANNEL TEMPERATURE



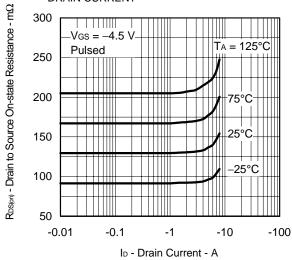
### FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT



## DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT

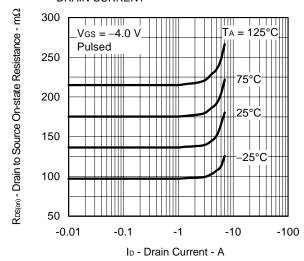


## DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT

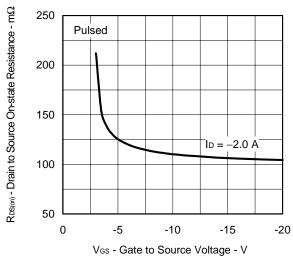


R<sub>DS(m)</sub> - Drain to Source On-state Resistance - mΩ

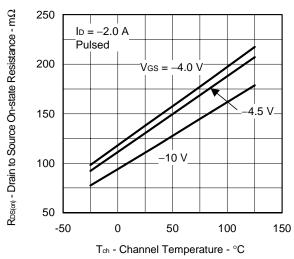
## DRAIN TO SOURCE ON-STATE RESISTANCE vs. DRAIN CURRENT



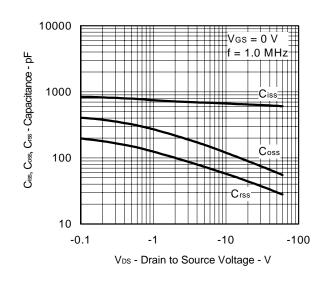
## DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



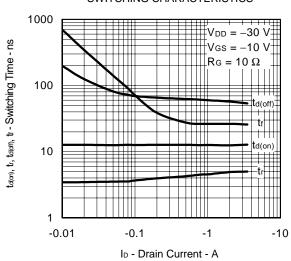
## DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE



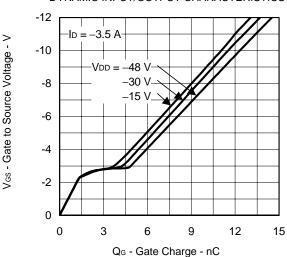
#### CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



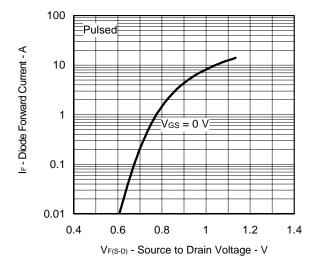
#### SWITCHING CHARACTERISTICS



#### DYNAMIC INPUT/OUTPUT CHARACTERISTICS



#### SOURCE TO DRAIN DIODE FORWARD VOLTAGE



 $\mu$ PA1918



[MEMO]

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