Unit: mm

TOSHIBA Transistor Silicon NPN Epitaxial Planar Type

# MT3S06T

#### VHF~UHF Band Low Noise Amplifier Applications

- Low noise figure:  $NF = 1.6 dB (V_{CE} = 3 V, I_C = 3 mA, f = 2 GHz)$
- High gain:  $|S_{21e}|^2 = 9.5 dB (V_{CE} = 3 V, I_C = 7 mA, f = 2 GHz)$

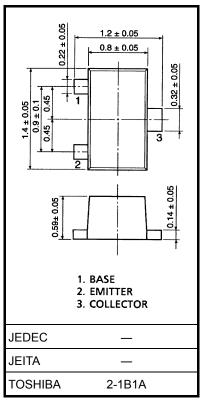
#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	V <sub>CBO</sub>	10	V
Collector-emitter voltage	V <sub>CEO</sub>	5	V
Emitter-base voltage	V <sub>EBO</sub>	1.5	V
Base current	ΙC	15	mA
Collector current	Ι <sub>Β</sub>	7	mA
Collector power dissipation	PC	60	mW
Junction temperature	Tj	125	°C
Storage temperature range	T <sub>stg</sub>	-55~125	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling

Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



Weight: 0.0022 g (typ.)

#### Marking



### Microwave Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Transition frequency	f <sub>T</sub>	$V_{CE} = 3 V, I_{C} = 5 mA$	7	10	—	GHz
Insertion dain	S <sub>21e</sub>   <sup>2</sup> (1)	$V_{CE} = 1 \text{ V}, \text{ I}_{C} = 5 \text{ mA}, \text{ f} = 2 \text{ GHz}$	_	8.5	_	dB
	S <sub>21e</sub>   <sup>2</sup> (2)	$V_{CE} = 3 V, I_{C} = 7 mA, f = 2 GHz$	6.5	9.5	_	
Noise figure	NF (1)	$V_{CE} = 1 \text{ V}, \text{ I}_{C} = 3 \text{ mA}, \text{ f} = 2 \text{ GHz}$	_	1.7	3	dB
	NF (2)	$V_{CE} = 3 V, I_{C} = 3 mA, f = 2 GHz$	_	1.6	3	uD

#### **Electrical Characteristics (Ta = 25°C)**

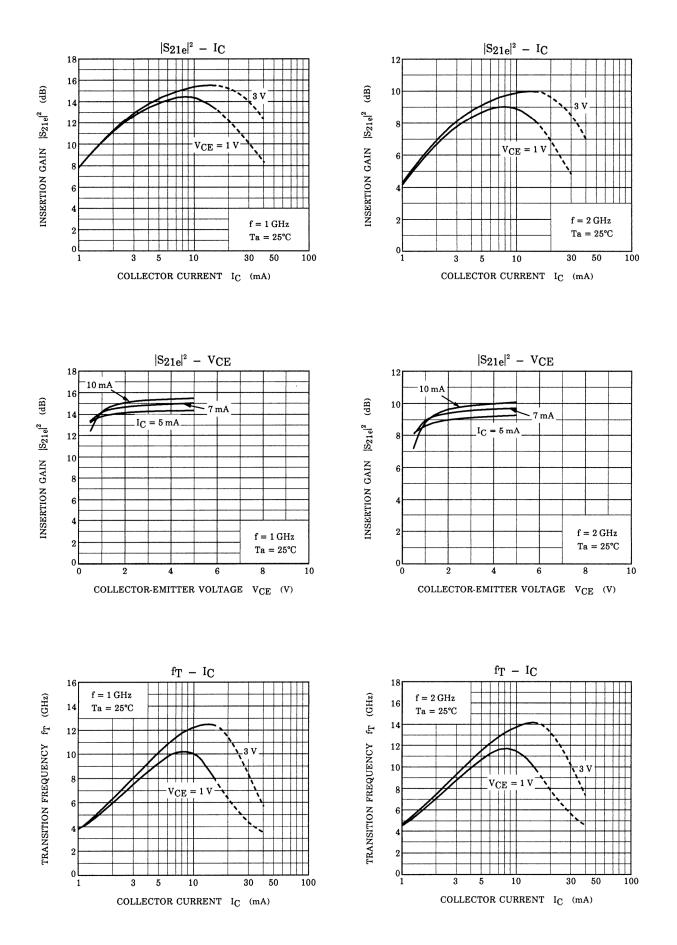
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I <sub>CBO</sub>	$V_{CB}=5~V,~I_{E}=0$	_	—	0.1	μA
Emitter cut-off current	I <sub>EBO</sub>	$V_{EB} = 1 V, I_{C} = 0$	_		1	μA
DC current gain	h <sub>FE</sub>	$V_{CE} = 1 \text{ V}, \text{ I}_{C} = 5 \text{ mA}$	70		140	
Reverse transfer capacitance	C <sub>re</sub>	$V_{CB}=1 \ V, \ I_E=0, \ f=1 \ MHz \qquad (Note)$	_	0.25	0.7	рF

Note:  $C_{re}$  is measured by 3 terminal method with capacitance bridge.

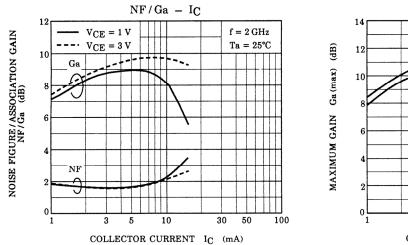
#### Caution

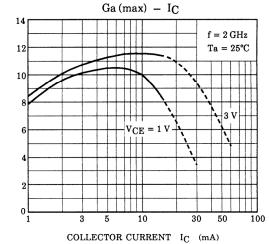
This device is sensitive to electrostatic discharge. Please handle with caution.

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 $C_{re} - V_{CB}$ 0.6 REVERSE TRANSFER CAPACITANCE Cre (pF)  $I_{\rm C} = 0$ f = 1 MHz0.5 0.4 0.3 0.2 0.1 0L 0 3 6 1 2 4 5 Collector-base voltage  $V_{CB}$  (V)

#### **RESTRICTIONS ON PRODUCT USE**

20070701-EN GENERAL

- The information contained herein is subject to change without notice.
- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property.
   In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc.
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