TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7SG32FU

#### 2 Input OR Gate

#### **Features**

• High-level output current:  $I_{OH}/I_{OL} = \pm 8$  mA (min) at  $V_{CC} = 3.0$  V

at v() – 3

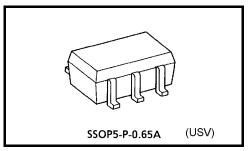
• High-speed operation: t<sub>pd</sub> = 2.4 ns (typ.)

at  $V_{CC} = 3.3 \text{ V}, 15 \text{pF}$ 

Operating voltage range: V<sub>CC</sub> = 0.9~3.6 V

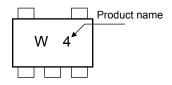
• 5.5-V tolerant inputs.

• 3.6-V power down protection output.

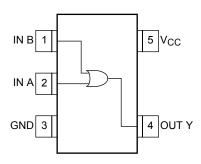


Weight: 0.006 g (typ.)

## Marking



## Pin Assignment (top view)



#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Value	Unit		
Power supply voltage	V <sub>CC</sub>	-0.5~4.6	V		
DC input voltage	V <sub>IN</sub>	-0.5~7.0	V		
DC autout vallage		−0.5~ 4.6 (Note 1)			
DC output voltage	Vout	-0.5~ V <sub>CC</sub> + 0.5 (Note 2)	V		
Input diode current	lıĸ	-20	mA		
Output diode current	lok	-20 (Note 3)	mA		
DC output current	lout	±25	mA		
DC V <sub>CC</sub> /ground current	Icc	±50	mA		
Power dissipation	PD	200	mW		
Storage temperature	T <sub>stg</sub>	-65~150	°C		

Note:

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: V<sub>CC</sub> = 0V

Note 2: High or Low State. IOUT abusolute maximum rating must be observed.

Note 3: V<sub>OUT</sub> < GND

# **IEC Logic Symbol**



## **Truth Table**

Α	В	Υ
L	L	L
L	Н	Н
Н	L	Н
Н	Н	Н

## **Operating Ranges**

Characteristics	Symbol	Value	Unit	
Power supply voltage	V <sub>CC</sub>	0.9~3.6	V	
Input voltage	V <sub>IN</sub>	0~5.5	V	
Output voltage	V	0~3.6 (Note 4)	V	
Output voltage	V <sub>OUT</sub>	0~V <sub>CC</sub> (Note 5)	]	
Output Current		±8.0 (Note 6)	- mA	
	I <sub>OH</sub> /I <sub>OL</sub>	±4.0 (Note 7)		
		±3.0 (Note 8)		
		±1.7 (Note 9)		
		±0.3 (Note 10)		
		±0.02 (Note 11)		
Operating temperature	T <sub>opr</sub>	<b>−40~85</b>	°C	
Input rise and fall time	dt/dV	0~10 (Note 12)	ns/V	

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Note 4:  $V_{CC} = 0V$ 

Note 5: High or Low state.

Note 6: V<sub>CC</sub> = 3.0~3.6 V

Note 7:  $V_{CC} = 2.3 \sim 2.7 \text{ V}$ 

Note 8:  $V_{CC} = 1.65 \sim 1.95 \text{ V}$ 

Note 9:  $V_{CC} = 1.4 \sim 1.6 \text{ V}$ 

Note 10: V<sub>CC</sub> = 1.1~1.3 V

Note 11:  $V_{CC} = 0.9 V$ 

Note 12:  $V_{IN} = 0.8 \sim 2.0 \text{ V}, V_{CC} = 3.0 \text{ V}$ 

# **DC Electrical Characteristics**

Characteristics	Characteristics Symbol Test Condition				Ta = 25°C			Ta = -40~85°C		Unit
Symbol Test contained		Condition	V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Offic	
		_		0.9	V <sub>CC</sub>			V <sub>CC</sub>		V
High-level input voltage	1.1~1.3			V <sub>CC</sub> × 0.7	_	_	V <sub>CC</sub> × 0.7	_		
	1.4~1.6			V <sub>CC</sub> × 0.65	_	_	V <sub>CC</sub> × 0.65			
	1.65~1.95			V <sub>CC</sub> × 0.65	_	_	V <sub>CC</sub> × 0.65			
				2.3~2.7	1.7	_	_	1.7	_	
				3.0~3.6	2.0	_	_	2.0	_	
				0.9	_	_	GND	_	GND	
				1.1~1.3			V <sub>CC</sub> × 0.3		V <sub>CC</sub> × 0.3	V
Low-level	$V_{IL}$		_				V <sub>CC</sub> × 0.35	_	V <sub>CC</sub> × 0.35	
input voltage				1.65~1.95	١	I	V <sub>CC</sub> × 0.35		V <sub>CC</sub> × 0.35	
				2.3~2.7			0.7		0.7	
				3.0~3.6	_		0.8		0.8	
		V <sub>OH</sub> V <sub>IN</sub> = V <sub>IH</sub> or V <sub>IL</sub>	I <sub>OH</sub> =-0.02 mA	0.9	0.75			0.75	_	
			$I_{OH} = -0.3 \text{ mA}$	1.1~1.3	V <sub>CC</sub> × 0.75		_	V <sub>CC</sub> × 0.75	l	V
High-level output voltage	V <sub>OH</sub>		$I_{OH} = -1.7 \text{ mA}$	1.4~1.6	V <sub>CC</sub> × 0.75	I	_	V <sub>CC</sub> × 0.75	١	
			$I_{OH} = -3.0 \text{ mA}$	1.65~ 1.95	V <sub>CC</sub> -0.45		_	V <sub>CC</sub> -0.45		
			$I_{OH} = -4.0 \text{ mA}$	2.3~2.7	2.0		_	2.0	_	
			$I_{OH} = -8.0 \text{ mA}$	3.0~3.6	2.48		_	2.48		
		V <sub>IN</sub> = V <sub>IL</sub>	$I_{OL} = 0.02 \text{ mA}$	0.9			0.1	_	0.1	V
			I <sub>OL</sub> = 0.3 mA	1.1~1.3			V <sub>CC</sub> × 0.25	_	V <sub>CC</sub> × 0.25	
Low-level output voltage	V <sub>OL</sub>		I <sub>OL</sub> = 1.7 mA	1.4~1.6			V <sub>CC</sub> × 0.25	_	V <sub>CC</sub> × 0.25	
			I <sub>OL</sub> = 3.0 mA	1.65~ 1.95		_	0.45	_	0.45	
			I <sub>OL</sub> = 4.0 mA	2.3~2.7			0.4	_	0.4	
			I <sub>OL</sub> = 8.0 mA	3.0~3.6			0.4	_	0.4	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 0~5.5V		0~3.6			±0.1	_	±1.0	μА
Power off leakage current	l <sub>OFF</sub>	V <sub>IN</sub> = 0~5.5V V <sub>OUT</sub> = 0~3.6V		0	_		1.0	_	10.0	μА
Quiescent supply current	Icc	V <sub>IN</sub> = V <sub>CC</sub> or GND		3.6		_	1.0	_	10.0	μА

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## AC Electrical Characteristics (input $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition		Ta = 25°C		Ta = -40~85°C		Unit	
Onaracienstics			C <sub>L (</sub> pF)	Min	Тур.	Max	Min	Max	Offic
		$\begin{array}{l} C_L = 10 \ pF, \\ R_L = 1 \ M\Omega \end{array}$	0.9	_	17.0	_	_	_	
			1.1~1.3	_	8.8	18.4	1.0	34.2	
			1.4~1.6	_	5.0	8.5	1.0	10.0	
			1.65~ 1.95	_	3.8	6.2	1.0	6.7	
			2.3~2.7	_	2.7	3.9	1.0	4.4	
			3.0~3.6	_	2.1	3.1	1.0	3.7	
			0.9	_	20.7	_	_	_	
	tPLH tPHL	$C_L$ = 15 pF, $R_L$ = 1 M $\Omega$	1.1~1.3	_	10.6	21.5	1.0	37.2	ns
			1.4~1.6	_	5.9	9.3	1.0	11.2	
Propagation delay time			1.65~ 1.95	_	4.5	6.9	1.0	7.1	
			2.3~2.7	_	3.0	4.4	1.0	5.0	
			3.0~3.6	_	2.4	3.4	1.0	3.9	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9	_	29.6	_	_	_	
			1.1~1.3	_	14.8	29.6	1.0	56.0	
			1.4~1.6	_	8.0	13.1	1.0	15.9	
			1.65~ 1.95	_	6.0	9.2	1.0	9.6	
			2.3~2.7	_	3.9	5.7	1.0	6.1	
			3.0~3.6	_	3.0	4.4	1.0	4.8	
Input capacitance	C <sub>IN</sub>		3.6	_	3	_	_	_	pF
Power dissipation capacitance	C <sub>PD</sub>	(Note 13)	0.9~3.6	_	6	_	_	—	pF

Note 13: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

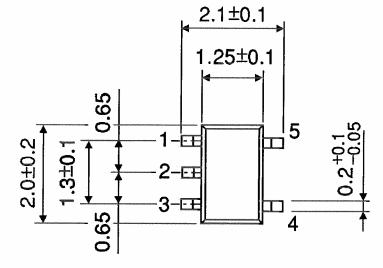
Average operating current can be obtained by the equation:

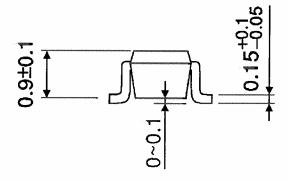
 $I_{CC \ (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$ 

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# **Package Dimensions**

SSOP5-P-0.65A Unit: mm





Weight: 0.006 g (typ.)

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20070701-EN GENERAL

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