TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SZ32F, TC7SZ32FU

2-Input OR Gate

Features

High output current : ±24 mA (min) at V_{CC} = 3 V

• Super high speed operation: tpd=2.4 ns (typ.)

at V_{CC} = 5 V, 50 pF

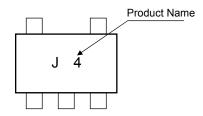
• Operating voltage range : V_{CC} = 1.8 to 5.5 V

• 5.5-V tolerant inputs

• 5.5-V power down protection output

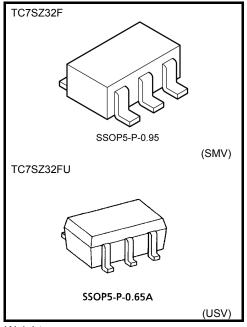
 Matches the performance of TC74LCX series when operated at 2.2 VV/ce

Marking



Absolute Maximum Ratings (Ta = 25°C)

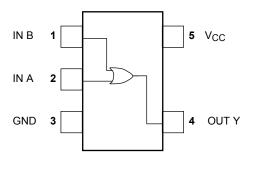
Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	−0.5 to 6	V
DC input voltage	V _{IN}	-0.5 to 6	V
DC output voltage	V _{OUT}	-0.5 to 6 (Note 1)	\ \
DC output voltage		-0.5 to V _{CC} +0.5 (Note 2)	ľ
Input diode current	I _{IK}	-20	mA
Output diode current	lok	-20 (Note 3)	mA
DC output current	lout	±50	mA
DC V _{CC} /ground current	Icc	±50	mA
Power dissipation	PD	200	mW
Storage temperature	T _{stg}	-65 to 150	°C
Lead temperature (10 s)	TL	260	°C



Weight:

SSOP5-P-0.95 : 0.016 g (typ.) SSOP5-P-0.65A : 0.006 g (typ.)

Pin Assignment (top view)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: $V_{CC} = 0 V$

Note 2: High or Low State. Do not exceed I_{OUT} of absolute maximum ratings.

Note 3: V_{OUT} < GND



IEC Logic Symbol



Truth Table

Α	В	Y
L	L	L
L	Н	Н
Н	L	Н
Н	Н	Н

Operating Ranges

Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC}	1.8 to 5.5	V	
Supply voltage		1.5 to 5.5 (Note 4)	V	
Input voltage	V _{IN}	0 to 5.5	V	
Output voltage	V _{OUT}	0 to 5.5 (Note 5)	>	
		0 to V _{CC} (Note 6)		
Operating temperature	T _{opr}	−40 to 85	°C	
	dt/dv	0 to 20 (V _{CC} = 1.8 V, 2.5 V \pm 0.2 V)		
Input rise and fall time		0 to 10 (V _{CC} = 3.3 V \pm 0.3 V)	ns/V	
		0 to 5 (V _{CC} = 5.0 V \pm 0.5 V)		

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Note 4: Data retention only

Note 5: $V_{CC} = 0 V$

Note 6: High or Low state

Electrical Characteristics

DC Characteristics

Characteristics Symbol Test Co		Condition	Condition		Ta = 25°C			Ta = -40 to 85°C		
		1650	Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
High-level input voltage V _{IH}					V _{CC} × 0.88	_	_	V _{CC} × 0.88		· v
		_		2.3 to 5.5	V _{CC} × 0.75	_	_	V _{CC} × 0.75	_	
			1.8	_	_	V _{CC} × 0.12	_	V _{CC} × 0.12	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	
Low-level input voltage	V _{IL}		_	2.3 to 5.5	_	_	V _{CC} × 0.25	_	V _{CC} × 0.25	V
				1.8	1.7	1.8		1.7		
			I _{OH} = -100 μA	2.3	2.2	2.3	_	2.2		1
			ΙΟΗ = – 100 μΑ	3.0	2.9	3.0	_	2.9		
High-level output	V	V _{IN} = V _{IH}		4.5	4.4	4.5	_	4.4	_	
voltage	V _{OH}	or V _{IL}	I _{OH} = -8 mA	2.3	1.9	2.15	_	1.9	_	V
			I _{OH} = -16 mA	3.0	2.4	2.8	_	2.4	_	
			I _{OH} = -24 mA	3.0	2.3	2.68	_	2.3	_	
			$I_{OH} = -32 \text{ mA}$	4.5	3.8	4.2	_	3.8		
			I _{OL} = 100 μA	1.8		0	0.1	_	0.1	V
				2.3		0	0.1	_	0.1	
		$V_{IN} = V_{IL}$		3.0		0	0.1	_	0.1	
Low-level	V			4.5	_	0	0.1	_	0.1	
output voltage	V _{OL}		I _{OL} = 8 mA	2.3	_	0.1	0.3	_	0.3	
			I _{OL} = 16 mA	3.0	_	0.15	0.4	_	0.4	
			I _{OL} = 24 mA	3.0	_	0.22	0.55	_	0.55	
			I _{OL} = 32 mA	4.5	_	0.22	0.55	_	0.55	
Input leakage current	I _{IN}	V _{IN} = 5.5 V	V _{IN} = 5.5 V or GND			_	±1	_	±10	μА
Power off leakage current	l _{OFF}	V _{IN} or V _{OUT} = 5.5 V		0.0	_	_	1	_	10	μА
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND		5.5			2	_	20	μА

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AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics S	Symbol	Test Condition		$Ta = 25^{\circ}C$ $Ta = -40 \text{ to } 85^{\circ}C$				Unit	
			V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
Propagation delay time	^t pLH t _{pHL}	C_L = 15 pF, R_L = 1 M Ω	1.8	2.0	4.6	10.0	2.0	10.5	- ns
			2.5 ± 0.2	0.8	3.0	7.0	0.8	7.5	
			3.3 ± 0.3	0.5	2.4	4.7	0.5	5.0	
			5.0 ± 0.5	0.5	1.9	4.1	0.5	4.4	
		$C_L = 50 \text{ pF},$ $R_L = 500 \Omega$	3.3 ± 0.3	1.5	3.0	5.2	1.5	5.5	
			5.0 ± 0.5	0.8	2.4	4.5	0.8	4.8	
Input capacitance	C _{IN}	_	0 to 5.5	1	4	-	_		pF
Power dissipation capacitance	C _{PD}	(Note 7)	3.3	1	20		_		- pF
			5.5		26		_		

Note 7: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

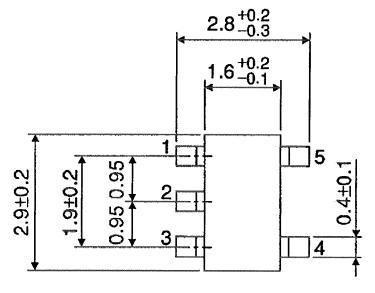
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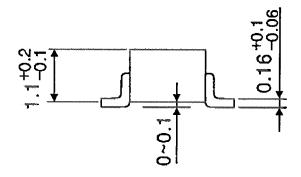
Average operating current can be obtained by the equation:

 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

Package Dimensions

SSOP5-P-0.95 Unit: mm



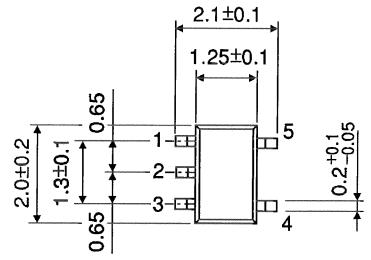


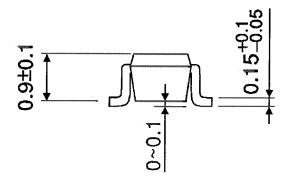
Weight: 0.016 g (typ.)

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Package Dimensions

SSOP5-P-0.65A Unit: mm





Weight: 0.006 g (typ.)

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