TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SH08FE

2 Input AND Gate

Features

• Super high speed operation :tpD = 4.3 ns (typ.)

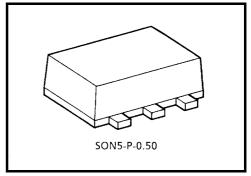
$$@V_{CC} = 5 V$$

• Low power dissipation : $I_{CC} = 2 \mu A$ (Max.)

$$@$$
 Ta = 25°C

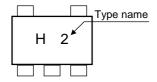
• High noise immunity : $V_{NIH} = V_{NIH}$

- 5.5V tolerant input.
- Wide operation voltage range : V_{CC} (opr) = 2~5.5 V

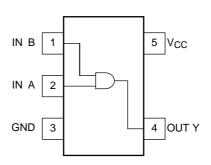


Weight: 0.003 g (typ.)

Marking



Pin Assignment (top view)



Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V _{CC}	-0.5~7	V
DC input voltage	V _{IN}	-0.5~7	V
DC output voltage	V _{OUT}	-0.5~V _{CC} + 0.5	V
Input diode current	l _{IK}	-20	mA
Output diode current	lok	±20	mA
DC output current	lout	±25	mA
DC V _{CC} /ground current	Icc	±50	mA
Power dissipation	PD	150	mW
Storage temperature	T _{stg}	-65~150	°C

Logic Diagram



Truth Table

Α	В	Υ
L	L	L
L	Н	L
Н	L	L
Н	Н	Н

Recommended Operating Conditions

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	2~5.5	V
Input voltage	V _{IN}	0~5.5	V
Output voltage	V _{OUT}	0~ V _{CC}	V
Operating temperature	T _{opr}	-40~85	°C
Input rise and fall time	dt/dv	0~100 (V_{CC} = 3.3 V \pm 0.3 V)	ns/V
	avav	0~20 (V_{CC} = 5 V ± 0.5 V)	113/ V

Electrical Characteristics

DC Characteristics

Characteristics Symbol		Test	Test Condition			Ta = 25°C			Ta = -40~85°C		Unit
		Circuit			V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
High-level input VIH —		_		2.0	1.5	_	_	1.5	_	V	
	_			3.0~5.5	V _{CC} × 0.7	_	_	V _{CC} × 0.7			
Low-level input					2.0	_	_	0.5	_	0.5	
voltage	V_{IL}	_		_	3.0~5.5	_	_	V _{CC} × 0.3	_	V _{CC} × 0.3	V
			V _{IN} = V _{IH}	I _{OH} = -50 μA	2.0	1.9	2.0	_	1.9	_	V
	High-level output voltage	_			3.0	2.9	3.0	_	2.9	_	
					4.5	4.4	4.5	_	4.4	_	
				I _{OH} = -4 mA	3.0	2.58	_	_	2.48	_	
				$I_{OH} = -8 \text{ mA}$	4.5	3.94	_	_	3.80		
	Low-level output voltage			V _{IH} V _{II}	2.0	_	0	0.1	_	0.1	
					3.0	_	0	0.1	_	0.1	
Low-level output voltage		_	V _{IN} = V _{IH} or V _{IL}		4.5	_	0	0.1	_	0.1	
			$I_{OL} = 4 \text{ mA}$	3.0	_	_	0.36	_	0.44		
				$I_{OL} = 8 \text{ mA}$	4.5	_	_	0.36	_	0.44	
Input leakage current	I _{IN}	_	V _{IN} = 5.5 V or GND		0~5.5	_	_	±0.1	_	±1.0	μΑ
Quiescent supply current	Icc	_	V _{IN} = V _{CC} o	or GND	5.5	_	_	2.0	_	20.0	μА

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AC Characteristics (input: $t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol	Test Condition			Ta = 25°C			Ta = -40~85°C		- Unit
			V _{CC} (V)	C _{L (} pF)	Min	Тур.	Max	Min	Max	Offic
Propagation delay time	^t PLH ^t PHL		3.3 ± 0.3	15	_	6.2	8.8	1.0	10.5	ns ns
				50	_	8.7	12.3	1.0	14.0	
				15	_	4.3	5.9	1.0	7.0	
		5.0 ± 0.5	50	_	5.8	7.9	1.0	9.0		
Input capacitance	C _{IN}				_	4	10	_	10	pF
Power dissipation capacitance	C _{PD}		(Note)		_	14	_	_	_	pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation.

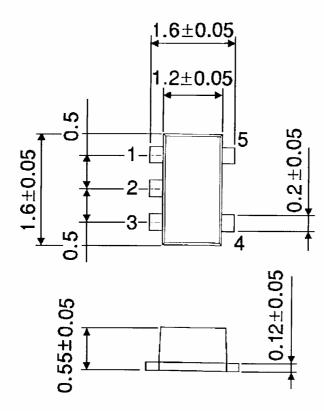
$$I_{CC\;(opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

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Package Dimensions

SON5-P-0.50 Unit: mm



Weight: 0.003 g (typ.)

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