TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7WZ04FU,TC7WZ04FK

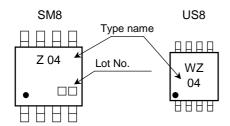
Triple Inverter

Features

- High output drive: ±24 mA (min) @V_{CC} = 3 V
- Super high speed operation: $t_{pd} 2.3 \text{ ns} (typ.) @V_{CC} = 5 \text{ V}, 50 \text{ pF}$
- Operation voltage range: V_{CC} (opr) = 1.65~5.5 V
- Latch-up performance: ±500 mA or more
- ESD performance: ±200 V or more (JEITA)
 - ± 2000 V or more (MIL)
- Power down protection is provided on all inputs and outputs.
- Matches the performance of TC74LCX series when operated at 3.3 V VCC.

Marking

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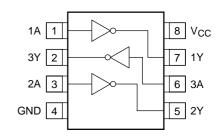
Maximum Ratings (Ta = 25°C)

TC7WZ04FU
SSOP8-P-0.65
TC7WZ04FK
A CONTRACT
SSOP8-P-0.50A
Weight

SSOP8-P-0.65 SSOP8-P-0.50A

: 0.02 g (typ.) : 0.01 g (typ.)

Pin Assignment (top view)



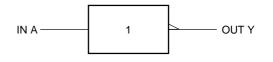
Characteristics	Symbol	Rating	Unit
Power supply voltage	V _{CC}	-0.5~6	V
DC input voltage	V _{IN}	-0.5~6	V
DC output voltage	V _{OUT}	-0.5~6	V
Input diode current	IIK	-20	mA
Output diode current	IOK	-20	mA
DC output current	I _{OUT}	±50	mA
DC V _{CC} /ground current	ICC	±50	mA
Power dissipation	PD	300 (SM8) 200 (US8)	mW
Storage temperature	T _{stg}	-65~150	°C
Lead temperature (10s)	ΤL	260	°C

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Truth Table

	Diagram
LUYIC	Diagram

А	Y
L	Н
Н	L



Recommended Operating Conditions

Characteristics	Symbol	Rating	Unit	
Supply voltage	Vcc	1.65~5.5	V	
Supply vollage	vcc	1.5~5.5 (Note 1)	v	
Input voltage	V _{IN}	0~5.5	V	
Output voltage	V _{OUT}	0~5.5 (Note 2)	v	
		0~V _{CC} (Note 3)	v	
Operating temperature	T _{opr}	-40~85	°C	
	d _t /d _v	0~20 (V _{CC} = 1.8 V \pm 0.15 V, 2.5 V \pm 0.2 V)	ns/V	
Input rise and fall time		0~10 (V_{CC} = 3.3 V \pm 0.3 V)		
		0~5 (V _{CC} = 5.5 V \pm 0.5 V)		

Note 1: Data retention only

Note 2: $V_{CC} = 0 V$

Note 3: High or low state

Electrical Characteristics

DC Characteristics

Characteristics		Symbol	Symbol Test Condition			Ta = 25°C			Ta = -40~85°C		Unit
Charac	clensucs	Symbol	Test Condition		V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
High lovel		Mar			1.65~ 1.95	$0.75 \\ \times V_{CC}$		_	0.75 × V _{CC}	_	
High level	V _{IH}			2.3~5.5	$0.7 \\ \times V_{CC}$	_	_	$0.7 \times V_{CC}$	_		
voltage					1.65~ 1.95	_		$\begin{array}{c} 0.25 \\ \times \ V_{CC} \end{array}$	_	$_{\times V_{CC}}^{0.25}$	V
Low level		VIL		—		_		$0.3 \\ \times V_{CC}$	_	$0.3 \\ \times V_{CC}$	
					1.65	1.55	1.65	_	1.55	_	
				I _{OH} = -100 μA	2.3	2.2	2.3		2.2	—	
				10Η 100 μΛ	3.0	2.9	3.0		2.9		
					4.5	4.4	4.5		4.4		
	High level	V _{OH}	V _{IN} = V _{IL}	$I_{OH} = -4 \text{ mA}$	1.65	1.29	1.52		1.29	—	
				$I_{OH} = -8 \text{ mA}$	2.3	1.9	2.15		1.9	—	
				I _{OH} = -16 mA	3.0	2.4	2.8	_	2.4	—	
Output				$I_{OH} = -24 \text{ mA}$	3.0	2.3	2.68		2.3	—	
				I _{OH} = -32 mA	4.5	3.8	4.2	_	3.8	—	
voltage			OL VIN = VIH	I _{OL} = 100 μA	1.65		0	0.1		0.1	
		-ow level V _{OL}			2.3	—	0	0.1	—	0.1	
					3.0	—	0	0.1		0.1	
					4.5		0	0.1		0.1	
	Low level			$I_{OL} = 4 \text{ mA}$	1.65	—	0.08	0.24	—	0.24	
				$I_{OL} = 8 \text{ mA}$	2.3		0.1	0.3		0.3	
				I _{OL} = 16 mA	3.0		0.15	0.4		0.4	
				$I_{OL} = 24 \text{ mA}$	3.0	—	0.22	0.55		0.55	
				$I_{OL} = 32 \text{ mA}$	4.5		0.22	0.55		0.55	
Input leakage		I _{IN}	$V_{IN} = 5.5 V \text{ or GND}$		0~5.5			±1		±10	μA
Power off lea	akage current	I_{OFF} V _{IN} or V _{OUT} = 5.5 V		0.0			1		10	μA	
Quiescent su	pply current	ICC	$V_{IN} = 5.5 \text{ V or GND}$		1.65~5.5	—	—	1		10	μA

AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40~85°C		Unit
Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
		C_L = 15 pF, R_L = 1 M Ω	1.8 ± 0.15	1.8	4.4	9.5	2.0	10.0	ns
			$\textbf{2.5}\pm\textbf{0.2}$	1.2	3.0	5.1	1.2	5.6	
Propagation delay time	^t pLH		$\textbf{3.3}\pm\textbf{0.3}$	0.8	2.2	3.4	0.8	3.8	
Fropagation delay time			5.0 ± 0.5	0.5	1.8	2.8	0.5	3.1	
	+	$C_{I} = 50 \text{ pF}, R_{I} = 500 \Omega$	$\textbf{3.3}\pm\textbf{0.3}$	1.2	2.9	4.5	1.2	5.0	
	t_{pHL} $C_{\text{L}} = 50 \text{ pF}, \text{ R}_{\text{L}} = 50$	$C_{L} = 50 \text{ pr}, \text{ K}_{L} = 500 \text{ s}_{2}$	5.0 ± 0.5	0.8	2.3	3.6	0.8	4.0	
Input capacitance	C _{IN}	_	0~5.5	_	3.0		_	_	pF
Power dissipation CPD	0	(Noto)	3.3	_	18	_	_	_	- 5
	(Note)	5.5		23				pF	

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

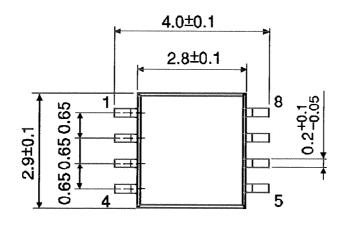
 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/3$

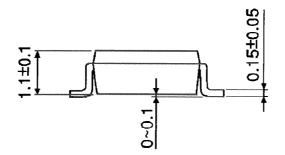
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Package Dimensions

SSOP8-P-0.65

Unit : mm





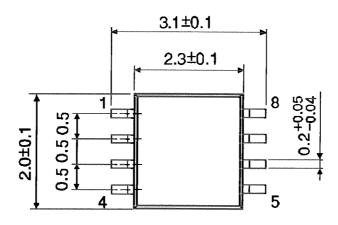
Weight: 0.02 g (typ.)

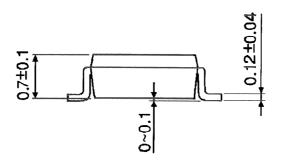
TOSHIBA

Package Dimensions

SSOP8-P-0.50A

Unit : mm





Weight: 0.01 g (typ.)

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Handbook" etc..

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