# RT1P141X SERIES

Transistor With Resistor For Switching Application Silicon PNP Epitaxial Type

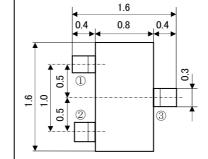
OUTLINE DRAWING

### DESCRIPTION

RT1P141X is a one chip transistor with built-in bias resistor,NPN type is RT1N141X.

### FEATURE

•Built-in bias resistor (R1=10k $\Omega$ ,R2=10k $\Omega$ ).



0.1

JEITA : -

JEDEC : -

**Terminal Connector** 

1):Base

2: Emitter

3: Collector

RT1P141M

2.1

1.25

0.425

0.3

0.425

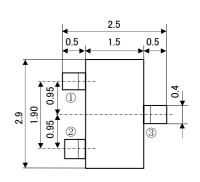
1

0.65

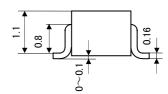
0.65

2.0 1.3 2

0.7 0.55 RT1P141U



RT1P141C



JEITA: SC-59 JEDEC: Similar to TO-236

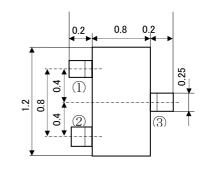
**Terminal Connector** 

①:Base

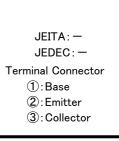
2: Emitter

③:Collector

RT1P141T

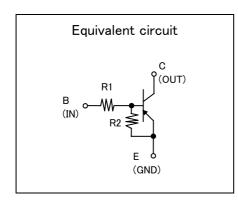


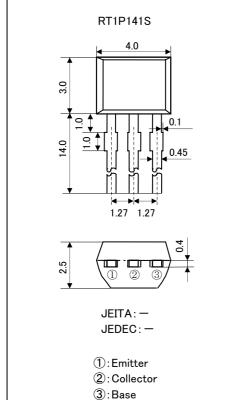




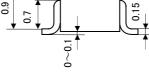
## **APPLICATION**

Inverted circuit, switching circuit, interface circuit, driver circuit.





ISAHAYA ELECTRONICS CORPORATION



JEITA: SC-70 JEDEC: -Terminal Connector (1):Base (2): Emitter 3: Collector

(Transistor)

UNIT:mm

# RT1P141X SERIES

**(Transistor)** 

Transistor With Resistor

For Switching Application

Silicon PNP Epitaxial Type

### MAXIMUM RATING (Ta=25°C)

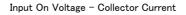
SYMBOL	PARAMETER	RATING					
		RT1P141T	RT1P141U	RT1P141M	RT1P141C	RT1P141S	UNIT
V <sub>CBO</sub>	Collector to Base voltage	-50					
V <sub>EBO</sub>	Emitter to Base voltage	-10					
V <sub>CEO</sub>	Collector to Emitter voltage	-50					V
Ι <sub>c</sub>	Collector current	-100					
I <sub>CM</sub>	Peak Collector current	-200					mA
Pc	Collector dissipation(Ta=25°C)	125(※)	125	1	50	450	mW
Tj	Junction temperature	+125		+150			°C
Tstg	Storage temperature	-55~+125		-55~+150			°C

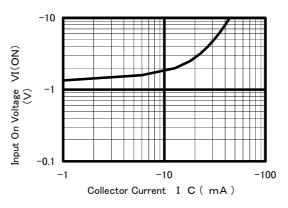
ELECTRICAL CHARACTERISTICS (Ta=25°C)

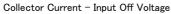
(%) package mounted on 9mm × 19mm × 1mm glass-epoxy substrate.

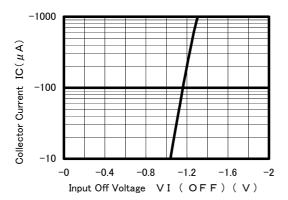
SYMBOL	PARAMETER	TEST CONDITION	LIMIT			UNIT
		TEST CONDITION	MIN	TYP	MAX	UNIT
V <sub>(BR)CEO</sub>	C to E break down voltage	$I_{c} = -100 \mu A, R_{BE} = \infty$	-50			V
I <sub>CBO</sub>	Collector cut off current	V <sub>CB</sub> =-50V, I <sub>E</sub> =0			-0.1	μA
h <sub>FE</sub>	DC forward current gain	V <sub>CE</sub> =-5V, I <sub>C</sub> =-10mA	50			_
$V_{CE(sat)}$	C to E saturation voltage	I <sub>c</sub> =–10mA, I <sub>B</sub> =–0.5mA		-0.1	-0.3	V
V <sub>I(ON)</sub>	Input on voltage	V <sub>CE</sub> =-0.2V, I <sub>C</sub> =-5mA		-1.5	-3.0	V
$V_{I(OFF)}$	Input off voltage	$V_{ce}$ =-5V, I <sub>c</sub> =-100 $\mu$ A	-0.8	-1.1		V
R <sub>1</sub>	Input resistance		7.0	10	13	kΩ
$R_2 / R_1$	Resistance ratio		0.9	1.0	1.1	
f⊤	Gain band width product	V <sub>CE</sub> =-6V, I <sub>E</sub> =10mA		150		MHz

### **TYPICAL CHARACTERISTICS**

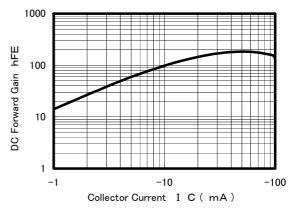








DC Forward Gain - Collector Current





Marketing division, Marketing planning department

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