

UNR221W

Silicon NPN epitaxial planar type

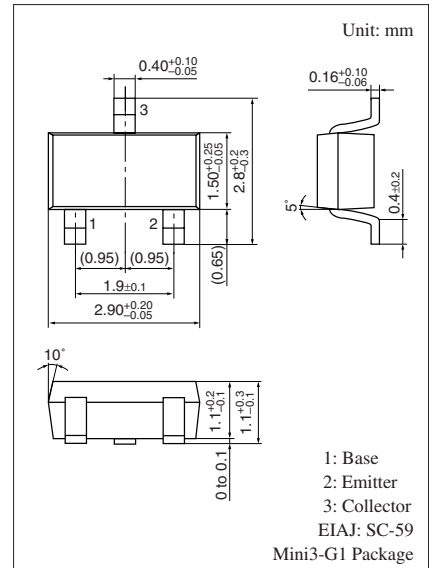
For digital circuits

■ Features

- Base-emitter resistance R_{BE} : 100 k Ω ,
- Mini type package, allowing downsizing of the equipment.
- Allowing automatic insertion through tape packing.

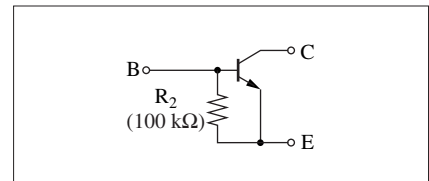
■ Absolute Maximum Ratings $T_a = 25^\circ\text{C}$

Parameter	Symbol	Rating	Unit
Collector-base voltage (Emitter open)	V_{CBO}	50	V
Collector-emitter voltage (Base open)	V_{CEO}	50	V
Collector current	I_C	100	mA
Total power dissipation	P_T	200	mW
Junction temperature	T_J	150	$^\circ\text{C}$
Storage temperature	T_{stg}	-55 to +150	$^\circ\text{C}$



Marking Symbol: 9F

Internal Connection



■ Electrical Characteristics $T_a = 25^\circ\text{C} \pm 3^\circ\text{C}$

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Collector-base voltage (Emitter open)	V_{CBO}	$I_C = 10 \mu\text{A}$, $I_E = 0$	50			V
Collector-emitter voltage (Base open)	V_{CEO}	$I_C = 2 \text{ mA}$, $I_B = 0$	50			V
Collector-base cutoff current (Emitter open)	I_{CBO}	$V_{CB} = 50 \text{ V}$, $I_E = 0$			0.1	μA
Collector-emitter cutoff current (Base open)	I_{CEO}	$V_{CE} = 50 \text{ V}$, $I_B = 0$			0.5	μA
Emitter-base cutoff current (Collector open)	I_{EBO}	$V_{EB} = 6 \text{ V}$, $I_C = 0$			100	μA
Forward current transfer ratio	h_{FE}	$V_{CE} = 10 \text{ V}$, $I_C = 5 \text{ mA}$	80			
Collector-emitter saturation voltage	$V_{CE(sat)}$	$I_C = 10 \text{ mA}$, $I_B = 0.3 \text{ mA}$			0.25	V
Input resistance	R_2		-30%	100	+30%	k Ω
Transition frequency	f_T	$V_{CB} = 10 \text{ V}$, $I_E = -2 \text{ mA}$, $f = 200 \text{ MHz}$		100		MHz

Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

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