

DATA SHEET

SMV1763-079, SMV1763-079LF: Hyperabrupt Junction Tuning Varactor

Features

- Designed for high-volume, low-cost battery applications
- Low series resistance
- High capacitance ratio at low reverse voltage
- Available lead (Pb)-free and RoHS-compliant MSL-1 @ 260 °C per JEDEC J-STD-020
- Ultrasmall SC-79 package
- Available in tape and reel packaging

Description

The SMV1763-079 is a silicon hyperabrupt junction varactor diode specifically designed for 3 V platforms. The specified high capacitance ratio and low $R_{\rm S}$ of this varactor make it attractive for low phase noise VCOs in wireless systems up to and beyond 2.5 GHz. Applications include low noise and wideband UHF and VHF VCO for GSM, PCS, CDMA and analog phones.



Skyworks offers lead (Pb)-free, RoHS (Restriction of Hazardous Substances)-compliant packaging.





Absolute Maximum Ratings

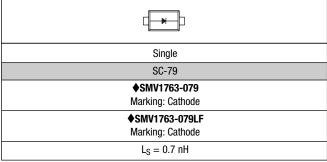
Characteristic	Value
Forward current (I _F)	20 mA
Power dissipation (P _D)	250 mW
Storage temperature (T _{ST})	-55 °C to +150 °C
Operating temperature (T _{OP})	-55 °C to +125 °C
ESD human body model	Class 0

Performance is guaranteed only under the conditions listed in the specifications table and is not guaranteed under the full range(s) described by the Absolute Maximum specifications. Exceeding any of the absolute maximum/minimum specifications may result in permanent damage to the device and will void the warranty.

CAUTION: Although this device is designed to be as robust as possible, ESD (Electrostatic Discharge) can damage this device. This device must be protected at all times from ESD. Static charges may easily produce potentials of several kilovolts on the human body or equipment, which can discharge without detection. Industry-standard ESD precautions must be employed at all times.

Electrical Specifications at 25 °C

Parameter	Condition	Min.	Тур.	Max.	Unit
Reverse current (I _R)	V _R = 8 V			20	nA
Capacitance (C _T)	$C_T @ 0.5 \text{ V}, V_R = 0.5 \text{ V}, F = 1 \text{ MHz}$	6.2	6.7	7.2	pF
Capacitance (C _T)	C_T @ 2.5 V, V_R = 2.5 V, F = 1 MHz	2.3	2.6	2.9	pF
Capacitance ratio (C _{TR})	C _T (0.5 V)/C _T (2.5 V)	2.3	2.5		
Series resistance (R _S)	$V_R = 1 \text{ V, F} = 900 \text{ MHz}$		0.5	0.7	Ω
Breakdown voltage (V _{BR})	I _R = 10 μA	10			V





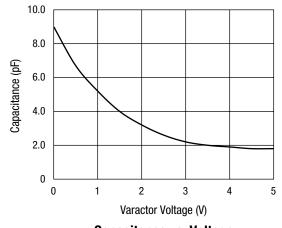
LF denotes lead (Pb)-free, RoHS-compliant packaging option as an alternative to our standard tin/lead (Sn/Pb) packaging.



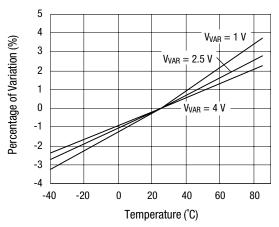
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Typical Performance Data

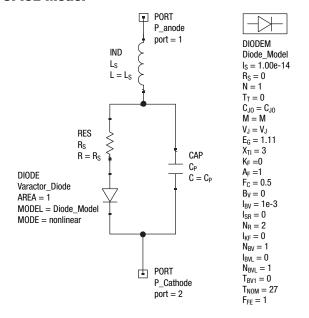


Capacitance vs. Voltage



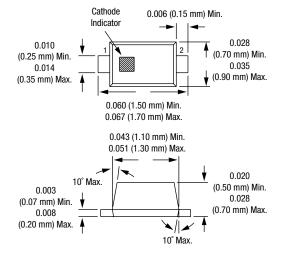
Relative Capacitance Change vs. Temperature

SPICE Model



Part	C _{J0}	V _J	М	C _P	R _S	L _S
Number	(pF)	(V)		(pF)	(Ω)	(nH)
SMV1763	8.2	15	9.5	0.67	0.5	0.8

SC-79



Capacitance vs. Voltage

V _R (V)	C _T (pF)		
0	9		
0.5	6.7		
1	5.2		
1.5	4		
2	3.2		
2.5	2.6		
3	2.2		
3.5	2		
4	1.9		
4.5	1.8		
5	1.8		

Recommended Solder Reflow Profiles

Refer to the "<u>Recommended Solder Reflow Profile</u>" Application Note.

Tape and Reel Information

Refer to the "Discrete Devices and IC Switch/Attenuators Tape and Reel Package Orientation" Application Note.

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