UNR91A2G

Silicon PNP epitaxial planar type

For digital circuits

■ Features

- Optimum for high-density mounting and downsizing of the equipment
- Contribute to low power consumption

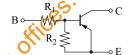
■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit	
Collector-base voltage (Emitter open)	V _{CBO}	-50	V	
Collector-emitter voltage (Base open)	V _{CEO}	-50	V	
Collector current	I_{C}	-80	mA	
Total power dissipation	P _T	125	mW	
Junction temperature	T _j	125	°C	
Storage temperature	T _{stg}	-55 to +125	Sicon	

Package

- Code
- SSMini3-F3
- Pin Name
 - 1: Base
 - 2: Emitter
 - 3: Collector

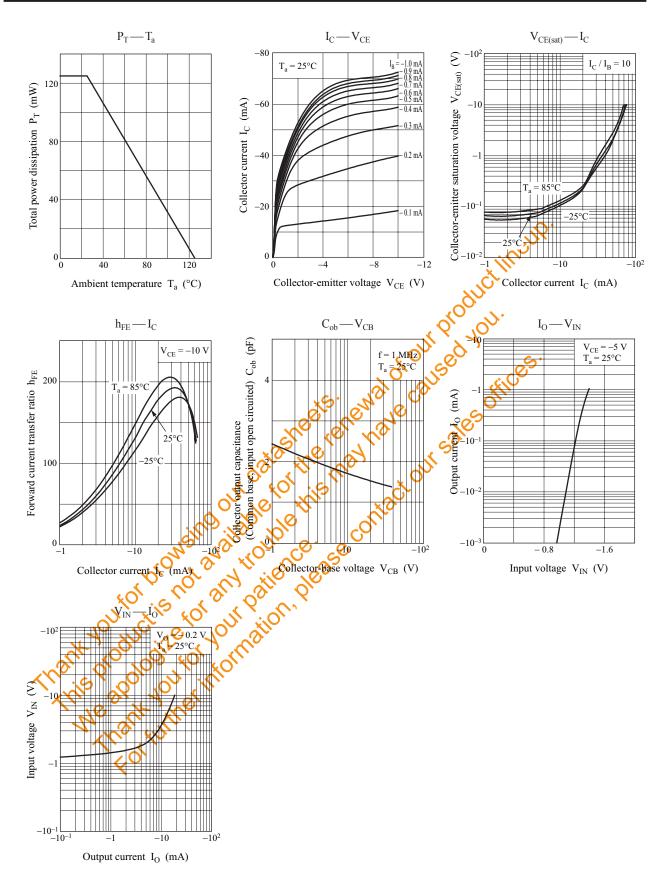
■ Marking Symbol: CF



Collector-base voltage (Emitter open)	V _{CBO}	-50 V ■ Markon Symbol: CF							
Collector-emitter voltage (Base open)	V _{CEO}	-50	V Walking Symbol. Ci						
Collector current	I_{C}	-80	mA Inte	ernal Con	nection				
Total power dissipation	P_{T}	125	mW CO	,0	R ₁ S				
Junction temperature	T _j	125	°C 000000000000000000000000000000000000	B ∽	W -				
Storage temperature	T _{stg}	-55 to +125	12.0 Mg. 60	(0)	$R_2 \leq$	→ E			
Collector-base voltage (Emitter open) V_{CBO} -50 V Collector-emitter voltage (Base open) V_{CEO} -50 V Collector current I_C -80 mA Total power dissipation P_T 125 mW Junction temperature T_j 125 \circ C Storage temperature T_{stg} -55 to $+125$ \circ C Storage temperature T_{stg} -55 to $+125$ \circ C The parameter T_{stg} -50 \circ C Parameter T_{stg}									
■ Electrical Characteristics T _a = 23	Symbo	8 7 6	Conditions	Min	Тур	Max	Unit		
Collector-base voltage (Emitter open)	V _{CBO}	$Q = -10 \mu$	$_{1}A, I_{E} = 0$	-50			V		
Collector-emitter-voltage (Base open)	VCEO	$I_C = -2 \text{ m.}$	$A, I_B = 0$	-50			V		
Collector-base cutoff current (Emitter open		$V_{CB} = -50$	$V, I_{E} = 0$			- 0.1	μΑ		
Collector-emitter cutoff current (Base oper		$V_{CE} = -50$	$V, I_{B} = 0$			- 0.5	μΑ		
Emitter-base cutoff current (Collector open	I _{EBO}	$V_{EB} = -6$	$V, I_C = 0$			- 0.2			
Forward current transfer ratio	h_{FE}	$V_{CE} = -10$					mA		
Collector-emitter saturation Voltage	FE	· CE	$V, I_C = -5 \text{ mA}$	60			mA —		
Concetor ennities saturation fortage	V _{CE(sat)}		$0 \text{ V, } I_C = -5 \text{ mA}$ nA, $I_B = -0.3 \text{ mA}$	60		- 0.25	mA V		
Output voltage high-level		$I_{\rm C} = -10 \rm m$		-4.9		- 0.25			
	V _{CE(sat)}	$I_{\rm C} = -10 \text{m}$ $V_{\rm CC} = -5 \text{m}$	$_{\rm mA}$, $_{\rm I_B}$ = -0.3 mA			- 0.25 - 0.2	V		
Output voltage high-level	V _{CE(sat)}	$I_{\rm C} = -10 \text{m}$ $V_{\rm CC} = -5 \text{m}$	$_{\rm nA}, I_{\rm B} = -0.3 \text{ mA}$ $V, V_{\rm B} = -0.5 \text{ V}, R_{\rm L} = 1 \text{ k}\Omega$		22				
Output voltage high-level Output voltage low-level	V _{CE(sat)} V _{OH} V _{OL}	$I_{C} = -10 \text{ n}$ $V_{CC} = -5$ $V_{CC} = -5$	$_{\rm nA}, I_{\rm B} = -0.3 \text{ mA}$ $V, V_{\rm B} = -0.5 \text{ V}, R_{\rm L} = 1 \text{ k}\Omega$	-4.9	22	- 0.2	V V V		

Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

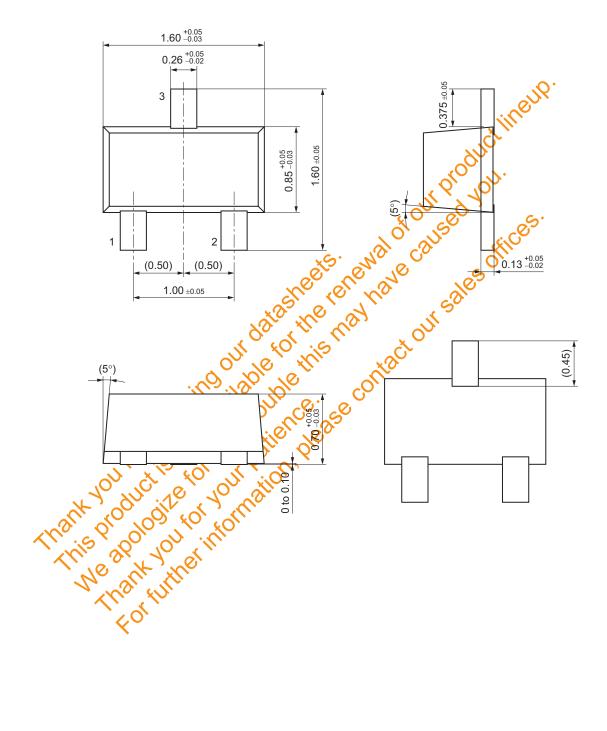
UNR91A2G Panasonic



2 SJH00225AED

Panasonic UNR91A2G

SSMini3-F3 Unit: mm



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