UP04212G

Silicon NPN epitaxial planar type

For digital circuits

■ Features

- Two elements incorporated into one package (Transistors with built-in resistor)
- SSMini type package, reduction of the mounting area and assembly cost

■ Basic Part Number

• UNR2212 × 2

■ Absolute Maximum Ratings $T_a = 25$ °C

Parameter	Symbol	Rating	Unit
Collector-base voltage (Emitter open)	V _{CBO}	50	V
Collector-emitter voltage (Base open)	V _{CEO}	50	V
Collector current	I_{C}	100	mA
Total power dissipation	P _T	125	6 mW
Junction temperature	T _j	125	, 660
Storage temperature	T _{stg}	-55 t6+125	, °C

■ Package

• Code

SSMini6-F2

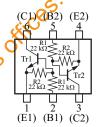
• Pin Name

Emitter (Tr2) 1: Emitter (Tr1)

5: Base (Tr2) 2: Base (Tr1)

3: Collector (Th2) 6: Collector (Tr1)

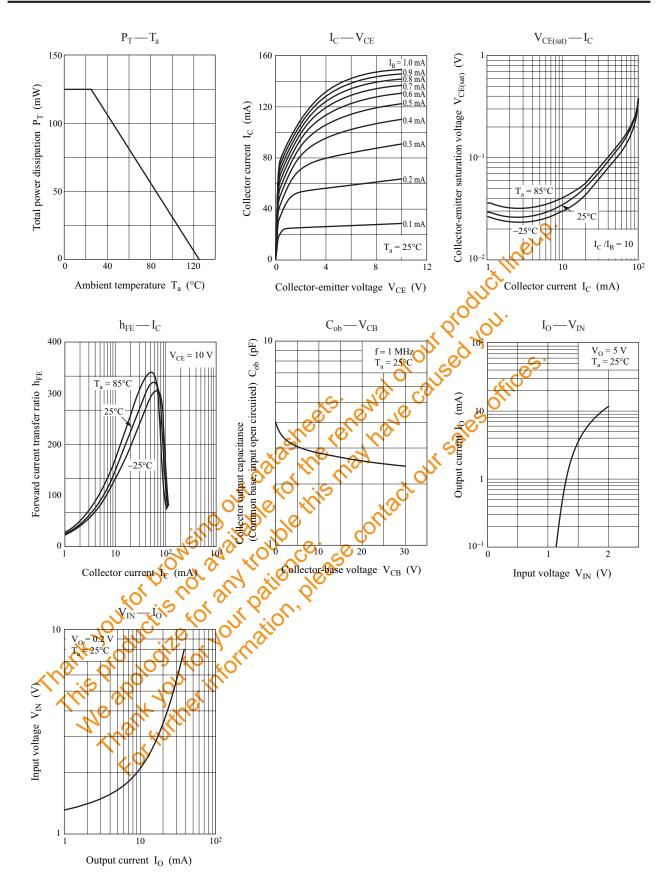
Marking Symbol: 8R



Parameter	Symbol	Rating	Marking Symbol: 8R O V O mA Some of the control o							
Collector-base voltage (Emitter open)	V_{CBO}	50	V	ernal Cor	nection					
Collector-emitter voltage (Base open)	V _{CEO}	50	V	Chiancon	inicotion					
Collector current	I_{C}	100	mA O		C1) (B2)	(E2) 4				
Total power dissipation	P_{T}	125	SmW NO CO	ő	R1 22 kΩ ≤ R	2				
Junction temperature	T _j	125	100 No	100	Tri Tri					
Storage temperature	T _{stg}	-55 t6+125	18C / 1/0	die [$R_{22 \text{ k}\Omega}$	Tr2 kΩ				
Total power dissipation P_T 125										
Parameter (N)	Symbo	P CO	onditions	Min	Тур	Max	Unit			
Collector-base voltage (Emitter open)	У СВО	10 m	$I_E = 0$	50			V			
Collector-emitter voltage Base open	V _{CEO}	$I_C = 2 \text{ mA}$	$I_{B} = 0$	50			V			
Collector-base cutoff current (Emitter ope	n) Lebo	$V_{\rm CB} = 50$	$V, I_E = 0$			0.1	μΑ			
Collector-emitter cutoff current (Base ope	n) I _{CEQ}	$V_{\rm CE} = 50$	$V, I_B = 0$			0.5	μΑ			
Emitter-base cutoff current (Collector oper) C EBO	$V_{EB} = 6 V$	$I_{C} = 0$			0.2	mA			
Forward current transfer ratio	h _{FE}	$V_{\rm CE} = 10^{\circ}$	$V, I_C = 5 \text{ mA}$	60			_			
Collector-emitter saturation voltage	V _{CE(sat}	$I_{\rm C} = 10 {\rm m}$	$A, I_B = -0.3 \text{ mA}$			0.25	V			
Output voltage high-level	V _{OH}	$V_{\rm CC} = 5 \text{ V}$	$V_{\rm B} = 0.5 \text{ V}, R_{\rm L} = 1 \text{ k}\Omega$	4.9			V			
Output voltage low-level	V _{OL}	$V_{\rm CC} = 5 \text{ V}$	$V_{\rm B} = 2.5 \text{ V}, R_{\rm L} = 1 \text{ k}\Omega$			0.2	V			
Input resistance	R ₁			-30%	22	+30%	kΩ			
Resistance ratio	R_1/R_2	2		0.8	1.0	1.2	_			
Transition frequency	f_{T}	$V_{\rm CB} = -10$	$V, I_E = -2 \text{ mA}, f = 200 \text{ MHz}$		150		MHz			

Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.

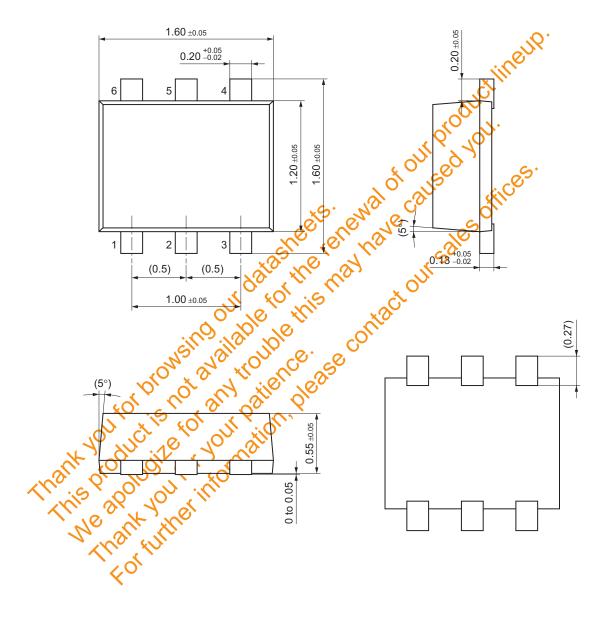
UP04212G Panasonic



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Panasonic UP04212G

SSMini6-F2 Unit: mm



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