# General purpose (dual digital transistors) EMB10/UMB10N/IMB10A

# Features

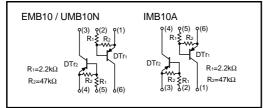
- 1) Two DTA123J chips in a EMT or UMT or SMT package.
- 2) Mounting possible with EMT3 or UMT3 or SMT3 automatic mounting machines.
- Transistor elements are independent, eliminating interference.
- 4) Mounting cost and area can be cut in half.

# Structure

Epitaxial planar type PNP silicon transistor (Built-in resistor type)

The following characteristics apply to both DTr1 and DTr2.

# Equivalent circuit



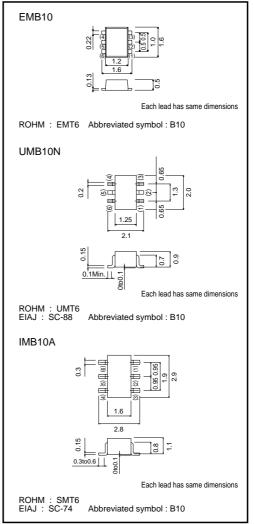
# ●Absolute maximum ratings (Ta = 25°C)

		•	/		
Parameter		Symbol Limits		Unit	
Supply voltage		Vcc	-50	V	
Input voltage		Vin	-12	V	
		VIN	5		
Output current		lo	-100	mA	
		IC (Max.)	-100		
Power dissipation	EMB10, UMB10N	Pd	150 (TOTAL)	mW_*1	
	IMB10A	Fu	300 (TOTAL)		
Junction temperature		Tj	150	°C	
Storage temperature		Tstg	-55 to +150	°C	
Storage temperature		0	-55 to +150	Ċ	

\*1 120mW per element must not be exceeded

\*2 200mW per element must not be exceeded

## •External dimensions (Unit : mm)



# Transistors

#### •Electrical characteristics (Ta = 25°C)

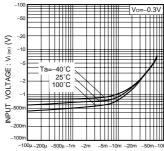
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Innut voltogo	VI (off)	-	-	-0.5	v	Vcc=–5V, lo=–100µA	
Input voltage	VI (on)	-1.1	-	-	v	Vo=-0.3V, Io=-5mA	
Output voltage	Vo (on)	_	-0.1	-0.3	V	lo/l=-5mA/-0.25mA	
Input current	h	-	-	-3.6	mA	VI=-5V	
Output current	IO (off)	-	-	-0.5	μΑ	Vcc=-50V, Vi=0V	
DC current gain	Gi	80	-	-	-	Vo=-5V, Io=-10mA	
Transition frequency	f⊤	-	250	-	MHz	Vce=-10V, Ie=5mA, f=100MHz *	
Input resistance	R1	1.54	2.2	2.86	kΩ	_	
Resistance ratio	R2/R1	17	21	26	-	_	

\* Transition frequency of the device

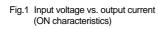
## Packaging specifications

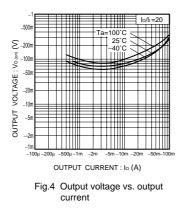
	Package	Taping			
	Code	T2R	TN	T148	
Туре	Basic ordering unit (pieces)	8000	3000	3000	
EMB10		0	—	—	
UMB10N		—	0	—	
IMB10A		—	—	0	

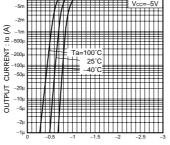
### •Electrical characteristic curves



OUTPUT CURRENT : Io (A)







INPUT VOLTAGE : VI (off) (V)

Fig.2 Output current vs. input voltage (OFF characteristics)

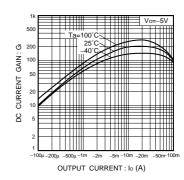


Fig.3 DC current gain vs. output current

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