RENESAS HD74LV1G04A

Inverter

REJ03D0064-0800 Rev.8.00 Mar 21, 2008

Description

The HD74LV1G04A has an inverter in a 5 pin package. Low voltage and high-speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

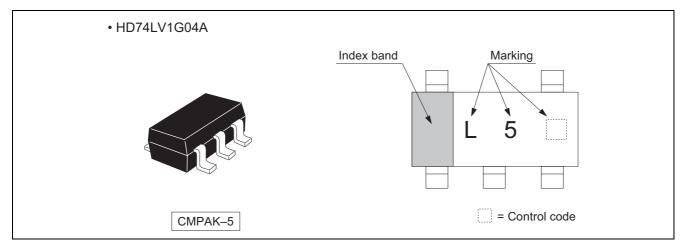
Features

- The basic gate function is lined up as Renesas uni logic series.
- Supplied on emboss taping for high-speed automatic mounting.
- Electrical characteristics equivalent to the HD74LV04A Supply voltage range : 1.65 to 5.5 V Operating temperature range : -40 to +85°C
- All inputs V_{IH} (Max.) = 5.5 V (@V_{CC} = 0 V to 5.5 V) All outputs V_O (Max.) = 5.5 V (@V_{CC} = 0 V)
- Output current $\pm 6 \text{ mA}$ (@V_{CC} = 3.0 V to 3.6 V), $\pm 12 \text{ mA}$ (@V_{CC} = 4.5 V to 5.5 V)
- All the logical input has hysteresis voltage for the slow transition.
- Ordering Information

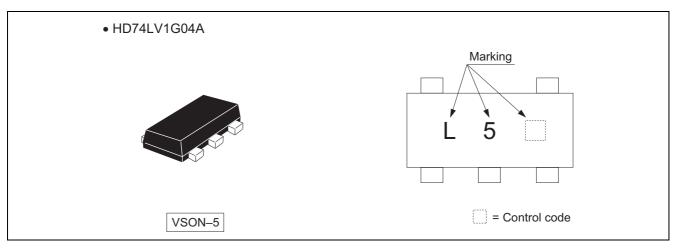
Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LV1G04ACME	CMPAK–5 pin	PTSP0005ZC-A (CMPAK-5V)	СМ	E (3000 pcs/reel)
HD74LV1G04AVSE	VSON–5 pin	PUSN0005KA-A (TNP-5DV)	VS	E (3000 pcs/reel)

Note: Please consult the sales office for the above package availability.

Outline and Article Indication



Outline and Article Indication



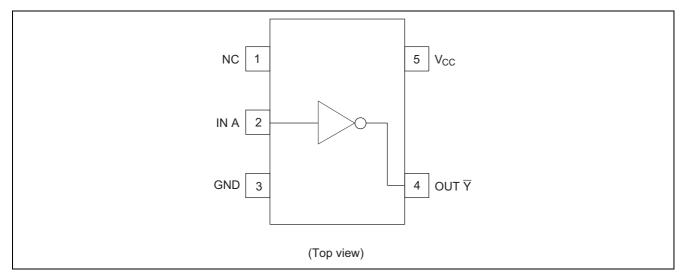
Function Table

Input A	Output Y				
Н	L				
L	Н				

H : High level

L : Low level

Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Test Conditions
Supply voltage range	V _{CC}	-0.5 to 7.0	V	
Input voltage range *1	VI	-0.5 to 7.0	V	
Output voltage renge *1,2	V	-0.5 to V _{CC} + 0.5	V	Output : H or L
Output voltage range *1, 2	Vo	-0.5 to 7.0	V	V _{CC} : OFF
Input clamp current	I _{IK}	-20	mA	V ₁ < 0
Output clamp current	Ι _{ΟΚ}	±50	mA	$V_0 < 0$ or $V_0 > V_{CC}$
Continuous output current	lo	±25	mA	$V_0 = 0$ to V_{CC}
Continuous current through V _{CC} or GND	I _{CC} or I _{GND}	±50	mA	
Maximum power dissipation at Ta = 25° C (in still air) ^{*3}	PT	200	mW	
Storage temperature	Tstg	-65 to 150	°C	

Notes: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore no two of which may be realized at the same time.

1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.

- 2. This value is limited to 5.5 V maximum.
- 3. The maximum package power dissipation was calculated using a junction temperature of 150°C.

Recommended Operating Conditions

Item	Symbol	Min	Мах	Unit	Conditions
Supply voltage range	V _{cc}	1.65	5.5	V	
Input voltage range	VI	0	5.5	V	
Output voltage range	Vo	0	V _{CC}	V	
		_	1		V _{CC} = 1.65 to 1.95 V
		_	2		V _{CC} = 2.3 to 2.7 V
	IoL	_	6	- mA	V _{CC} = 3.0 to 3.6 V
Output ourrest		_	12		V _{CC} = 4.5 to 5.5 V
Output current	Іон	_	-1		V _{CC} = 1.65 to 1.95 V
		_	-2		V _{CC} = 2.3 to 2.7 V
		_	-6		V _{CC} = 3.0 to 3.6 V
		_	-12		V _{CC} = 4.5 to 5.5 V
		0	300		V _{CC} = 1.65 to 1.95 V
Input transition rise or fall rate	A# / A	0	200	ns / V	V _{CC} = 2.3 to 2.7 V
Input transition rise or fall rate	$\Delta t / \Delta v$	0	100	ns/v	V _{CC} = 3.0 to 3.6 V
		0	20		V_{CC} = 4.5 to 5.5 V
Operating free-air temperature	Ta	-40	85	°C	

Note: Unused or floating inputs must be held high or low.

Electrical Characteristic

• $Ta = -40 \text{ to } 85^{\circ}C$

Item	Symbol	V _{cc} (V) *	Min	Тур	Max	Unit	Test condition
		1.65 to 1.95	V _{CC} ×0.75	_	_		
	V	2.3 to 2.7	V _{CC} ×0.7	_	—		
	V _{IH}	3.0 to 3.6	V _{CC} ×0.7	_	—		
		4.5 to 5.5	V _{CC} ×0.7	_	—	V	
Input voltage		1.65 to 1.95	_	_	V _{CC} ×0.25	v	
	VIL	2.3 to 2.7	_	_	V _{CC} ×0.3		
	VIL	3.0 to 3.6	_	_	V _{CC} ×0.3		
		4.5 to 5.5	_	_	V _{CC} ×0.3		
		1.8	_	0.25	_		
Hysteresis voltage	V _H	2.5	_	0.30	—	V	$V_T^+ - V_T^-$
	∨н	3.3	_	0.35	—	v	
		5.0	_	0.45	—		
		Min to Max	V _{CC} -0.1	_	—		I _{OH} = –50 μA
		1.65	1.4	_	—		$I_{OH} = -1 \text{ mA}$
	V _{он}	2.3	2.0	_	—		$I_{OH} = -2 \text{ mA}$
		3.0	2.48	_	—		I _{OH} = -6 mA
Output voltage		4.5	3.8	_	—	V	I _{OH} = -12 mA
Oulput voltage		Min to Max	_	—	0.1	v	I _{OL} = 50 μA
		1.65	_	_	0.3		I _{OL} = 1 mA
	V _{OL}	2.3	_	—	0.4		$I_{OL} = 2 \text{ mA}$
		3.0	_	—	0.44		I _{OL} = 6 mA
		4.5	_	—	0.55		I _{OL} = 12 mA
Input current	I _{IN}	0 to 5.5	_	_	±1	μA	$V_{IN} = 5.5 \text{ V or GND}$
Quiescent supply current	I _{CC}	5.5	—	—	10	μA	$V_{IN} = V_{CC} \text{ or } GND,$ $I_O = 0$
Output leakage current	I _{OFF}	0	—	—	5	μA	V_{IN} or $V_O = 0$ to 5.5 V
Input capacitance	C _{IN}	3.3	_	3.0		pF	$V_{IN} = V_{CC}$ or GND

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.

Switching Characteristics

•	$V_{CC} =$	$1.8 \pm$	0.15	V
---	------------	-----------	------	---

ltem	Symbol	•	Ta = 25°C	;	Ta = -40	to 85°C	Unit	Test	FROM	то
nem	Symbol	Min	Тур	Max	Min	Max	Onit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	—	12.6	22.0	1.0	24.0		C∟ = 15 pF	۸	$\overline{\mathbf{v}}$
delay time	t _{PHL}		19.7	33.0	1.0	36.0	ns	$C_L = 50 \text{ pF}$	A	I

• $V_{CC} = 2.5 \pm 0.2 \text{ V}$

ltem	Symbol	•	Ta = 25°C	;	Ta = -40	to 85°C	Unit	Test	FROM	то
nem	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	—	7.0	11.7	1.0	14.0	ns	C _L = 15 pF	۸	$\overline{\mathbf{v}}$
delay time	t _{PHL}		10.5	15.5	1.0	18.0	115	C _L = 50 pF	A	I

• $V_{CC} = 3.3 \pm 0.3 V$

ltem	Symbol	-	Ta = 25°C	;	Ta = -40	to 85°C	Unit	Test	FROM	то
item	Symbol	Min	Тур	Max	Min	Max	Onit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	Ι	5.0	7.1	1.0	8.5	20	C _L = 15 pF	۸	$\overline{\mathbf{v}}$
delay time	t _{PHL}	_	7.5	10.6	1.0	12.0	ns	$C_L = 50 \text{ pF}$	Ā	I

$\bullet \quad V_{CC} = 5.0 \pm 0.5 \ V$

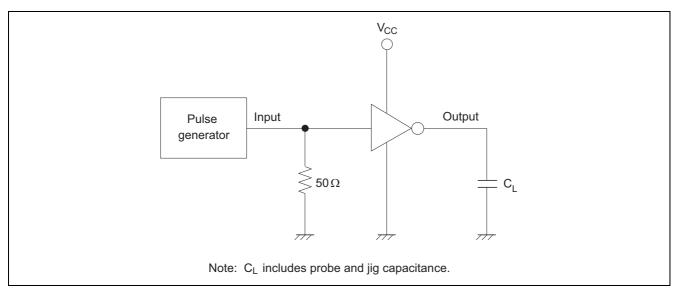
Item	Symbol	-	Ta = 25°C	;	Ta = -40	to 85°C	Unit	Test	FROM	то
item	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	—	3.8	5.5	1.0	6.5	ns	C _L = 15 pF	۸	$\overline{\mathbf{v}}$
delay time	t _{PHL}		5.3	7.5	1.0	8.5	115	$C_L = 50 \text{ pF}$	A	I

Operating Characteristics

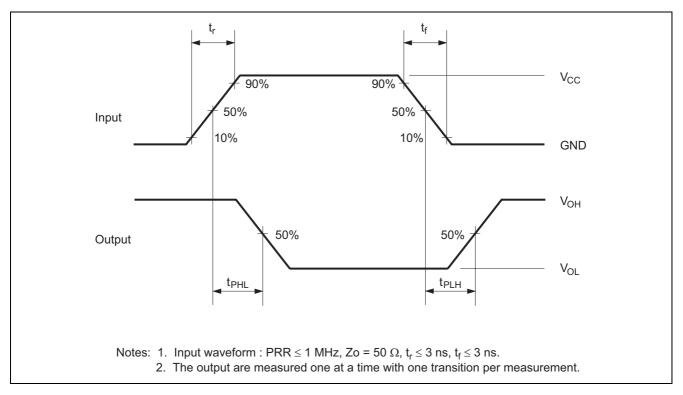
• $C_L = 50 \text{ pF}$

ltem	Symbol	V _{cc} (V)	-	Ta = 25°C	;	Unit	Test Conditions	
nem	Symbol	VCC (V)	Min	Тур	Max	Onit	Test conditions	
Power dissipation	C	3.3	_	8.5	_	pF	f = 10 MHz	
capacitance	C _{PD}	5.0	_	10.0	_	μr		

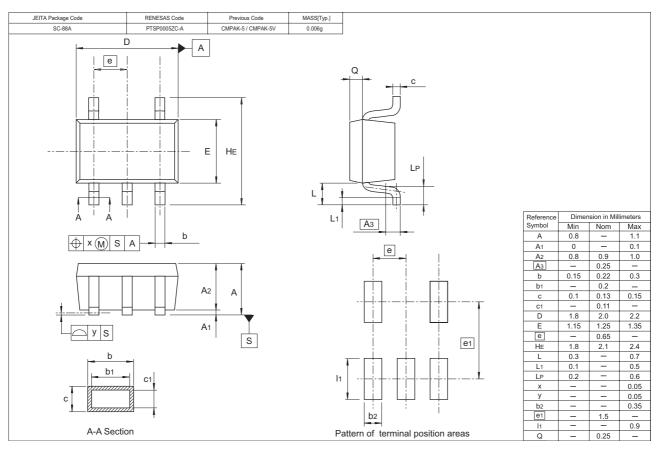
Test Circuit

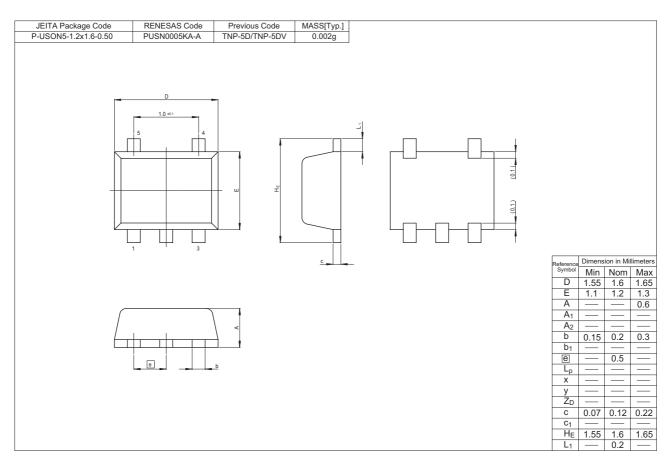


Waveforms



Package Dimensions





RENESAS

RenesasTechnology Corp. sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

- Benesas lechnology Corp. sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan
 Pines
 This document is provided for reference purposes only so that Renesas customers may select the appropriate Renesas products for their use. Renesas neither makes warranties or representations with respect to the accuracy or completeness of the information in this document.
 But not infinited to, product data. diagrams, charts, programs, algorithms, and application scuch as the development of weapons of mass and regulations, and proceedures required by such laws and regulation.
 All information in this document, included in this document for the purpose of military application scuch as the development of weapons of mass and regulations, and proceedures required by such laws and regulations.
 All information included in this document such as product data, diagrams, charts, programs, algorithms, and application carcuit examples, is current as of the date this document, when exporting the products or the technology described herein, you should follow the applicable export control laws and regulations, and proceedures required by such laws and regulations.
 Renesas has used reasonable care in compiling the information in this document, but Renesas assumes no liability whattowere for any damages incurred as a fast used in this document, but Renesas assumes no liability whattowere of neitary application states are the explorability of the total system before deciding about the applicability or otherwise in systems the failue on malfunction of which may cause a direct threads for the purpose, leave and mediation in the date this document. Jou should evaluate the information in this document, and the purpose of any damages incurred as a state of the total system before deciding about the applicability or therwise in systems the failue or malfunction of which may cause a direct threads to human life or create a tak of human nijury or whic



RENESAS SALES OFFICES

Refer to "http://www.renesas.com/en/network" for the latest and detailed information.

Renesas Technology America, Inc.

450 Holger Way, San Jose, CA 95134-1368, U.S.A Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K. Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology (Shanghai) Co., Ltd. Unit 204, 205, AZIACenter, No.1233 Lujiazui Ring Rd, Pudong District, Shanghai, China 200120 Tel: <86> (21) 5877-1818, Fax: <86> (21) 6887-7858/7898

Renesas Technology Hong Kong Ltd. 7th Floor, North Tower, World Finance Centre, Harbour City, Canton Road, Tsimshatsui, Kowloon, Hong Kong Tel: <852> 2265-6688, Fax: <852> 2377-3473

Renesas Technology Taiwan Co., Ltd. 10th Floor, No.99, Fushing North Road, Taipei, Taiwan Tel: <886> (2) 2715-2888, Fax: <886> (2) 3518-3399

Renesas Technology Singapore Pte. Ltd.

1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632 Tel: <65> 6213-0200, Fax: <65> 6278-8001

Renesas Technology Korea Co., Ltd. Kukje Center Bldg. 18th Fl., 191, 2-ka, Hangang-ro, Yongsan-ku, Seoul 140-702, Korea Tel: <82> (2) 796-3115, Fax: <82> (2) 796-2145

Renesas Technology Malaysia Sdn. Bhd Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia Tel: <603> 7955-9390, Fax: <603> 7955-9510

http://www.renesas.com