RENESAS

HD74LV1G86A 2-input Exclusive-OR Gate

REJ03D0070-0700 Rev.7.00 Mar 21, 2008

Description

The HD74LV1G86A performs the Boolean functions $Y = A \oplus B$ or $Y = \overline{AB} + A\overline{B}$ in positive logic. A common application is as a true / complement element. If one of the inputs is low, the other input will be reproduced in true form at the output. If one of the inputs is high, the signal on the other input will be reproduced inverted form at the output. Low voltage and high-speed operation is suitable for the battery powered products (e.g., notebook computers), and the low power consumption extends the battery life.

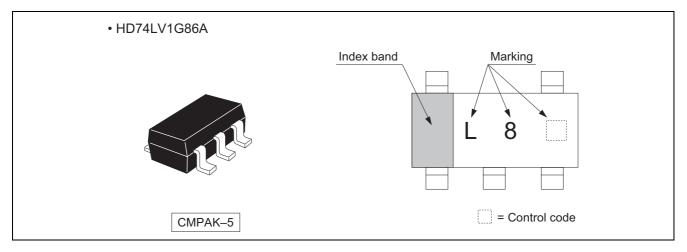
Features

- The basic gate function is lined up as Renesas uni logic series.
- Supplied on emboss taping for high-speed automatic mounting.
- Electrical characteristics equivalent to the HD74LV86A Supply voltage range : 1.65 to 5.5 V Operating temperature range : -40 to +85°C
- All inputs V_{IH} (Max.) = 5.5 V (@V_{CC} = 0 V to 5.5 V) All outputs V_0 (Max.) = 5.5 V (@V_{CC} = 0 V)
- Output current $\pm 6 \text{ mA}$ (@V_{CC} = 3.0 V to 3.6 V), $\pm 12 \text{ mA}$ (@V_{CC} = 4.5 V to 5.5 V)
- All the logical input has hysteresis voltage for the slow transition.
- Ordering Information

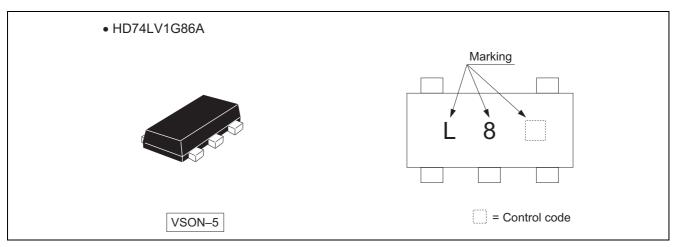
Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)
HD74LV1G86ACME	CMPAK–5 pin	PTSP0005ZC-A (CMPAK-5V)	СМ	E (3000 pcs/reel)
HD74LV1G86AVSE	VSON–5 pin	PUSN0005KA-A (TNP-5DV)	VS	E (3000 pcs/reel)

Note: Please consult the sales office for the above package availability.

Outline and Article Indication



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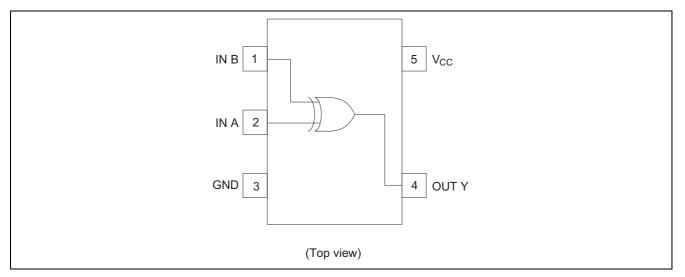
Function Table

Inp	Output Y			
A	В			
L	L	L		
L	Н	н		
Н	L	Н		
Н	Н	L		

H : High level

L : Low level

Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Test Conditions
Supply voltage range	V _{CC}	-0.5 to 7.0	V	
Input voltage range *1	VI	-0.5 to 7.0	V	
Output voltage range *1, 2	V	-0.5 to V _{CC} + 0.5	V	Output : H or L
Output voltage range	Vo	-0.5 to 7.0	V	V _{CC} : OFF
Input clamp current	I _{IK}	-20	mA	V ₁ < 0
Output clamp current	Ι _{ΟΚ}	±50	mA	$V_0 < 0$ or $V_0 > V_{CC}$
Continuous output current	lo	±25	mA	$V_0 = 0$ to V_{CC}
Continuous current through V _{CC} or GND	I _{CC} or I _{GND}	±50	mA	
Maximum power dissipation at Ta = 25° C (in still air) ^{*3}	PT	200	mW	
Storage temperature	Tstg	-65 to 150	°C	

Notes: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore no two of which may be realized at the same time.

1. The input and output voltage ratings may be exceeded if the input and output clamp-current ratings are observed.

- 2. This value is limited to 5.5 V maximum.
- 3. The maximum package power dissipation was calculated using a junction temperature of 150°C.

Recommended Operating Conditions

Item	Symbol	Min	Мах	Unit	Conditions
Supply voltage range	V _{cc}	1.65	5.5	V	
Input voltage range	VI	0	5.5	V	
Output voltage range	Vo	0	V _{CC}	V	
		_	1		V _{CC} = 1.65 to 1.95 V
		_	2		V _{CC} = 2.3 to 2.7 V
	I _{OL}	_	6		V _{CC} = 3.0 to 3.6 V
Output ourrest		_	12	- mA	V_{CC} = 4.5 to 5.5 V
Output current		_	-1		V _{CC} = 1.65 to 1.95 V
		_	-2		V _{CC} = 2.3 to 2.7 V
	I _{OH}	_	V _{CC} = 3.0 to 3.6 V		
		_	-12		V_{CC} = 4.5 to 5.5 V
		0	300		V _{CC} = 1.65 to 1.95 V
Input transition rise or fall rate	A# / A	0	200	ns / V	V _{CC} = 2.3 to 2.7 V
Input transition rise or fall rate	$\Delta t / \Delta v$	0	100	ns/v	V _{CC} = 3.0 to 3.6 V
		0	20		V_{CC} = 4.5 to 5.5 V
Operating free-air temperature	Ta	-40	85	°C	

Note: Unused or floating inputs must be held high or low.

Electrical Characteristic

• $Ta = -40 \text{ to } 85^{\circ}C$

Item	Symbol	V _{cc} (V) *	Min	Тур	Max	Unit	Test condition
		1.65 to 1.95	V _{CC} ×0.75	_	_		
	V	2.3 to 2.7	V _{CC} ×0.7	_	—		
	V _{IH}	3.0 to 3.6	V _{CC} ×0.7	_	—		
		4.5 to 5.5	V _{CC} ×0.7	_	—	V	
Input voltage		1.65 to 1.95	_	_	V _{CC} ×0.25	v	
	VIL	2.3 to 2.7	_	_	V _{CC} ×0.3		
	VIL	3.0 to 3.6	_	_	V _{CC} ×0.3		
		4.5 to 5.5	_	_	V _{CC} ×0.3		
		1.8	_	0.25	—		
Hysteresis voltage	V _H	2.5	_	0.30	—	V	$V_T^+ - V_T^-$
Tysteresis voltage	VH	3.3	_	0.35	—	v	
		5.0	_	0.45	—		
		Min to Max	V _{CC} -0.1	_	—		I _{OH} = –50 μA
		1.65	1.4	_	—	-	$I_{OH} = -1 \text{ mA}$
	V _{OH}	2.3	2.0	_	—		$I_{OH} = -2 \text{ mA}$
		3.0	2.48	—	—		I _{OH} = -6 mA
Output voltage		4.5	3.8	—	—	V	I _{OH} = -12 mA
Output voltage		Min to Max	_	—	0.1	v	I _{OL} = 50 μA
		1.65	_	_	0.3		I _{OL} = 1 mA
	V _{OL}	2.3	_	_	0.4		$I_{OL} = 2 \text{ mA}$
		3.0	_	—	0.44		I _{OL} = 6 mA
		4.5	—	—	0.55		I _{OL} = 12 mA
Input current	I _{IN}	0 to 5.5	_	_	±1	μA	$V_{IN} = 5.5 \text{ V or GND}$
Quiescent supply current	I _{CC}	5.5	—	_	10	μA	$V_{IN} = V_{CC} \text{ or } GND,$ $I_O = 0$
Output leakage current	I _{OFF}	0	—	_	5	μA	V_{IN} or $V_O = 0$ to 5.5 V
Input capacitance	CIN	3.3		2.5		pF	$V_{IN} = V_{CC}$ or GND

Note: For conditions shown as Min or Max, use the appropriate values under recommended operating conditions.

Switching Characteristics

• $V_{CC} = 1.8 \pm 0.15 V$

Item	Symbol	٦	a = 25°C)		Ta = -40) to 85°C	Unit	Test	FROM	то
nem	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}		15.8	29.4	1.0	33.0	20	C∟ = 15 pF	A or B	v
delay time	t _{PHL}		22.6	40.9	1.0	45.0	ns	C _L = 50 pF	AUB	I

• $V_{CC} = 2.5 \pm 0.2 V$

Item	Symbol	Ta = 25° C Ta = -40 to 85° C Upit		Unit	Test	FROM	то			
nem	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}	_	9.4	17.6	1.0	21.0	ns	C _L = 15 pF	A or B	v
delay time	t _{PHL}		12.6	22.6	1.0	26.5	-	$C_L = 50 \text{ pF}$		1

• $V_{CC} = 3.3 \pm 0.3 V$

ltem	Symbol	٦	a = 25°C		Ta = –40) to 85°C	Unit	Test	FROM	то
item	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}		7.0	11.0	1.0	13.0	ns	C _L = 15 pF	A or B	v
delay time	t _{PHL}		9.5	14.5	1.0	16.5	115	$C_L = 50 \text{ pF}$	AUB	I

$\bullet \quad V_{CC} = 5.0 \pm 0.5 \ V$

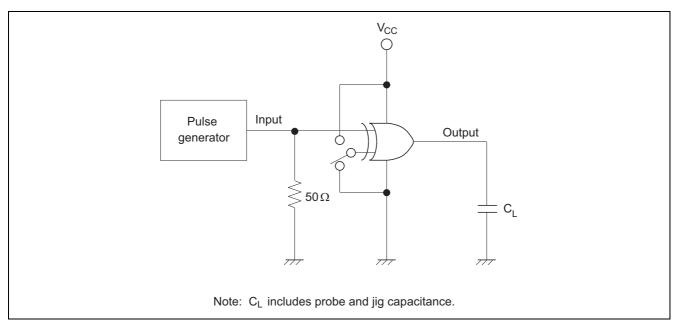
Item	Symbol	٦	a = 25°C	C	Ta = -40) to 85°C	Unit	Test	FROM	то
item	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions	(Input)	(Output)
Propagation	t _{PLH}		4.8	6.8	1.0	8.0	20	C _L = 15 pF	A or B	v
delay time	t _{PHL}	_	6.3	8.8	1.0	10.0	ns	$C_L = 50 \text{ pF}$	AUB	I

Operating Characteristics

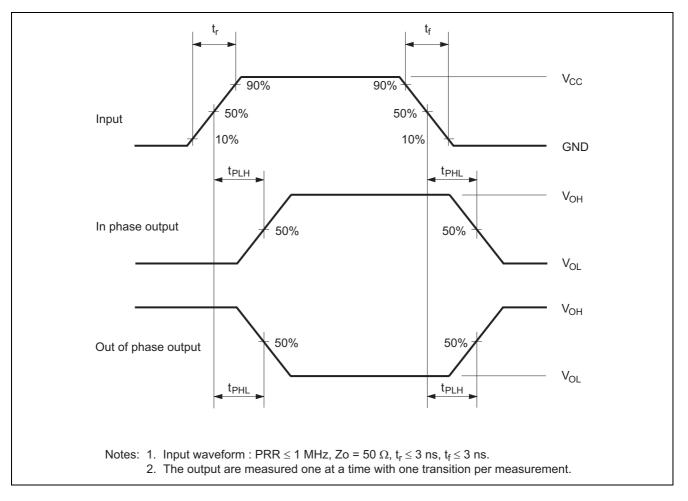
• $C_L = 50 \text{ pF}$

ltem	Symbol	V _{cc} (V)		Ta = 25°C	;	Unit	Test Conditions		
nem	Symbol	VCC (V)	Min	Тур	Max	Unit	Test Conditions		
Power dissipation	C _{PD}	3.3	—	9.5		۶F	f = 10 MHz		
capacitance	CPD	5.0	—	11.0		μr			

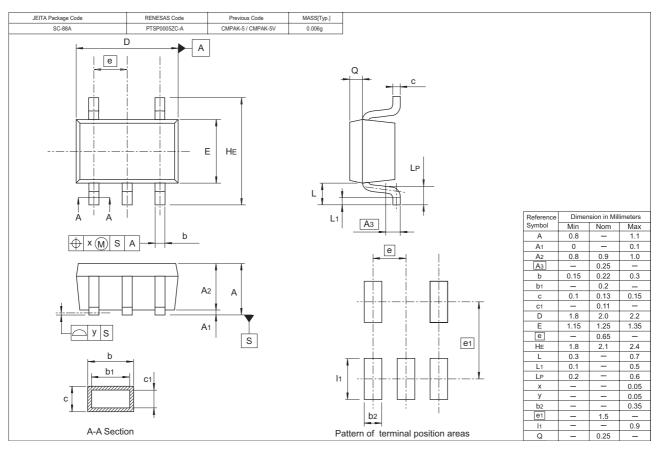
Test Circuit

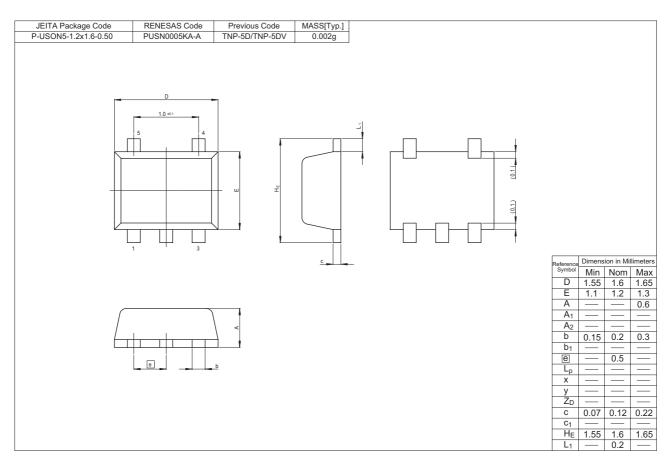


Waveforms



Package Dimensions





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