RT3CLLM

Compound Transistor For Low Frequency Amplify Application Silicon Npn Epitaxial Type

DESCRIPTION

RT3CLLM is a compound transistor built with two 2SC3052 chips in SC-88 package.

FEATURE

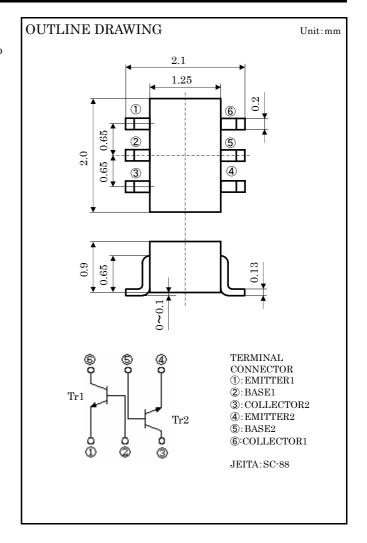
Silicon npn epitaxial type

Each transistor elements are independent.

Mini package for easy mounting

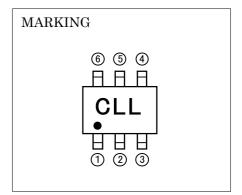
APPLICATION

For low frequency amplify application



MAXIMUM RATING (Ta=25°C)

SYMBOL	PARAMETER	RATING	UNIT	
VCBO	Collector to Base voltage	50	V	
VEBO	Emitter to Base voltage	6	V	
VCEO	Collector to Emitter voltage	50	V	
Ic	Collector current	200	mA	
Pc(Total)	Collector dissipation(Ta=25°C)	150	mW	
T_{j}	Junction temperature	+125	°C	
$T_{ m stg}$	Storage temperature	-55~+125	°C	



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ELECTRICAL CHARACTERISTICS (Ta=25°C)

Symbol	Parameter	Test conditions	Limits			Unit
	Parameter	lest conditions	Min	Тур	Max	Unit
V(BR)CEO	Collector to Emitter break down voltage	I _C =100 μ A,R _{BE} =∞	50	-	-	V
Icbo	Collector cut off current	V _{CB} =50V,I _E =0	-	-	0.1	μΑ
IEBO	Emitter cut off current	V_{EB} =6 V , I_{C} =0	-	-	0.1	μΑ
hFE*	DC forward current gain	V _{CE} =6V,I _C =1mA	150	-	800	-
h_{FE}	DC forward current gain	V _{CE} =6V,I _C =0.1mA	90	-	-	-
VCE(sat)	Collector to Emitter saturation voltage	Ic=100mA,I _B =10mA	-	-	0.3	V
fT	Gain band width product	V _{CE} =6V,I _E =-10mA	-	200	-	MHz
C_{ob}	Collector output capacitance	V_{CB} =6 V_{IE} =0, f =1 MH_{Z}	-	2.5	-	pF
NF	Noise figure	V_{CE} =6 V_{IE} =-0.1 mA_{f} =1 kH_{Z} , R_{G} =2 $k\Omega$	-	-	15	dB

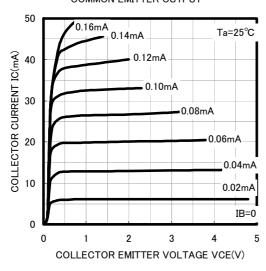
^{*:} It shows hee classification in right table.

Item	E	F	G
hFE	150~300	250~500	400~800

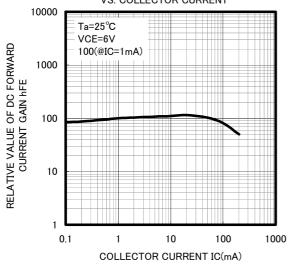
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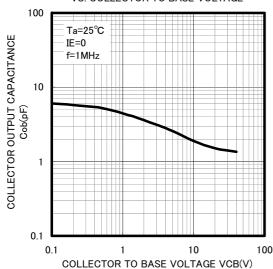
TYPICAL CHARACTERISTICS COMMON EMITTER OUTPUT



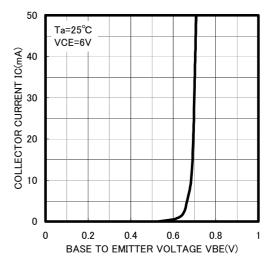
DC FORWARD CURRENT GAIN VS. COLLECTOR CURRENT



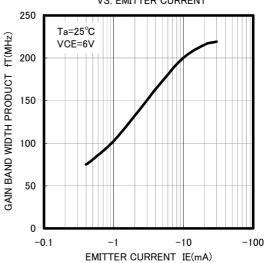
COLLECTOR OUTPUT CAPACITANCE VS. COLLECTOR TO BASE VOLTAGE



COMMON EMITTER TRANSFER



GAIN BAND WIDTH PRODUCT VS. EMITTER CURRENT





Marketing division, Marketing planning department 6-41 Tsukuba, Isahaya, Nagasaki, 854-0065 Japan

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