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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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PRELIMINARY DATA SHEET

NPN SILICON + SiGe RF TWIN TRANSISTOR

μ**PA869TD**

NPN SILICON + SiGe RF TRANSISTOR (WITH 2 DIFFERENT ELEMENTS) IN A 6-PIN LEAD-LESS MINIMOLD (M16, 1208 PACKAGE)

FEATURES

• 2 different built-in transistors (NESG2046M33, 2SC5800)

Q1: High gain SiGe transistor

 $f_T = 18 \text{ GHz TYP.}, |S_{21e}|^2 = 13 \text{ dB TYP.} @ V_{CE} = 1 \text{ V}, \text{ Ic} = 15 \text{ mA}, \text{ f} = 2 \text{ GHz}$

Q2: Low phase distortion transistor suited for OSC applications

ft = 6.5 GHz TYP., $|S_{21e}|^2$ = 5.5 dB TYP. @ Vce = 1 V, lc = 15 mA, f = 2 GHz

• 6-pin lead-less minimold (M16, 1208 package)

BUILT-IN TRANSISTORS

	Q1	Q2
3-pin super lead-less minimold part No.	NESG2046M33	-
3-pin thin-type ultra super minimold part No.	_	2SC5800

ORDERING INFORMATION

Part Number	Quantity	Supplying Form
μ ΡΑ869TD	50 pcs (Non reel)	• 8 mm wide embossed taping
μPA869TD-T3	10 kpcs/reel	• Pin 1 (Q1 Collector), Pin 6 (Q1 Base) face the perforation side of the tape

Remark To order evaluation samples, contact your nearby sales office. The unit sample quantity is 50 pcs.

Caution Observe precautions when handling because these devices are sensitive to electrostatic discharge.

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ABSOLUTE MAXIMUM RATINGS (TA = +25°C)

Parameter	Symbol	Ratings		Unit
		Q1	Q2	
Collector to Base Voltage	Vсво	13	13	V
Collector to Emitter Voltage	VCEO	5	5	V
Emitter to Base Voltage	VEBO	1.5	1.5	V
Collector Current	lc	40	100	mA
Total Power Dissipation	Ptot ^{Note}	190	190	mW
		210 in 2 elements		
Junction Temperature	Tj	150		°C
Storage Temperature	Tstg	-65 to +150		°C

Note Mounted on 1.08 $\text{cm}^2 \times 1.0 \text{ mm}$ (t) glass epoxy PCB

ELECTRICAL CHARACTERISTICS (TA = +25°C)

(1) Q1

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Collector Cut-off Current	Ісво	$V_{CB} = 5 V, I_E = 0 mA$	-	-	100	nA
Emitter Cut-off Current	Іево	VEB = 0.5 V, Ic = 0 mA	-	-	100	nA
DC Current Gain	hfe Note 1	Vce = 1 V, lc = 2 mA	140	180	220	-
Gain Bandwidth Product	f⊤	Vce = 1 V, lc = 15 mA, f = 2 GHz	15	18	-	GHz
Insertion Power Gain	S _{21e} ²	Vce = 1 V, lc = 15 mA, f = 2 GHz	11	13	-	dB
Noise Figure	NF	$\label{eq:Vce} \begin{array}{l} V_{\text{CE}} = 1 \ V, \ I_{\text{C}} = 3 \ \text{mA}, \ f = 2 \ \text{GHz}, \\ Z_{\text{S}} = Z_{\text{opt}} \end{array}$	-	0.8	1.5	dB
Associated Gain	Ga	$\label{eq:Vce} \begin{split} V_{CE} &= 1 \ V, \ I_C = 3 \ mA, \ f = 2 \ GHz, \\ Z_S &= Z_{opt} \end{split}$	9.5	11.5	-	dB
Reverse Transfer Capacitance	Cre Note 2	$V_{CB} = 1 \text{ V}, I_E = 0 \text{ mA}, f = 1 \text{ MHz}$	-	0.2	0.4	pF

(2) Q2

Parameter	Symbol	Test Conditions	MIN.	TYP.	MAX.	Unit
Collector Cut-off Current	Ісво	$V_{CB} = 5 V$, $I_E = 0 mA$	-	_	600	nA
Emitter Cut-off Current	Іево	$V_{EB} = 0.5 \text{ V}, \text{ Ic} = 0 \text{ mA}$	-	-	600	nA
DC Current Gain	hfe Note 1	Vce = 1 V, Ic = 5 mA	100	120	145	_
Gain Bandwidth Product (1)	fт	Vce = 1 V, lc = 5 mA, f = 2 GHz	3	4.5	-	GHz
Gain Bandwidth Product (2)	fт	Vce = 1 V, lc = 15 mA, f = 2 GHz	5	6.5	-	GHz
Insertion Power Gain (1)	S _{21e} ²	Vce = 1 V, lc = 5 mA, f = 2 GHz	3	4	-	dB
Insertion Power Gain (2)	S21e ²	Vce = 1 V, lc = 15 mA, f = 2 GHz	4.5	5.5	-	dB
Noise Figure	NF	$\label{eq:Vce} \begin{split} V_{CE} &= 1 \ V, \ I_C = 10 \ mA, \ f = 2 \ GHz, \\ Z_S &= Z_{opt} \end{split}$	-	1.9	2.5	dB
Reverse Transfer Capacitance	Cre ^{Note 2}	Vсв = 0.5 V, IE = 0 mA, f = 1 MHz	-	0.6	0.8	pF

Notes 1. Pulse measurement: PW \leq 350 μ s, Duty Cycle \leq 2%

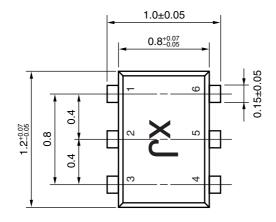
2. Collector to base capacitance when the emitter grounded

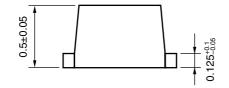
hfe CLASSIFICATION

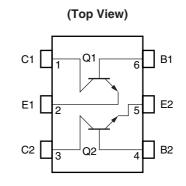
Rank	FB
Marking	хJ
hFE Value of Q1	140 to 220
hFE Value of Q2	100 to 145

PACKAGE DIMENSIONS

6-PIN LEAD-LESS MINIMOLD (M16, 1208 PACKAGE) (UNIT: mm)







PIN CONNECTIONS

- 1. Collector (Q1)
- 2. Emitter (Q1)
- 3. Collector (Q2)
- 4. Base (Q2)
- 5. Emitter (Q2)
- 6. Base (Q1)

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M8E 00.4-0110

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