

T-31-15

SILICON PLANAR EPITAXIAL TRANSISTOR

P-N-P transistor in a microminiature plastic envelope, intended for applications in thick and thin-film circuits such as self-oscillating mixer in u.h.f. tuners in conjunction with bipolar transistors or with MOS fets.

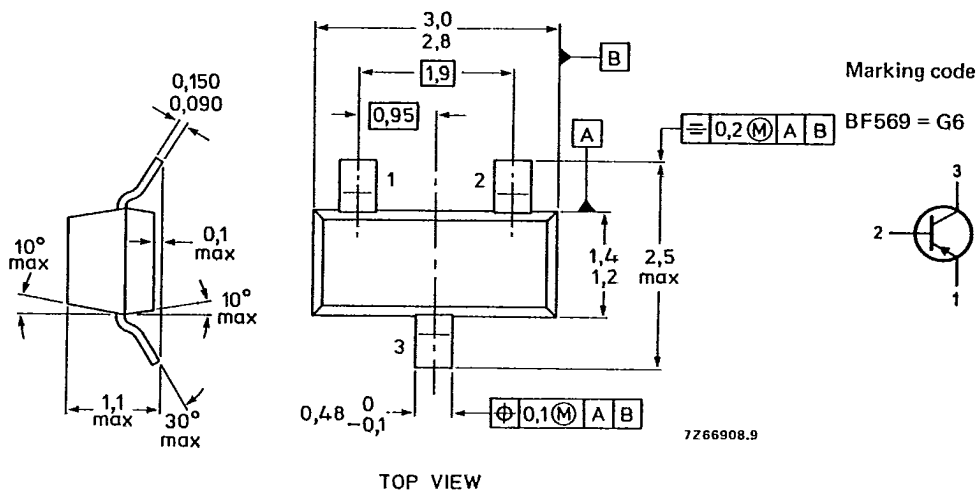
QUICK REFERENCE DATA

Collector-base voltage (open emitter)	$-V_{CBO}$	max.	40 V
Collector-emitter voltage (open base)	$-V_{CEO}$	max.	35 V
Collector current (d.c.)	$-I_C$	max.	30 mA
Total power dissipation up to $T_{amb} = 60\text{ }^\circ\text{C}$	P_{tot}	max.	200 mW
Junction temperature	T_j	max.	150 $^\circ\text{C}$
Transition frequency at $f = 100\text{ MHz}$ $I_E = 3\text{ mA}; -V_{CB} = 10\text{ V}$	f_T	typ.	900 MHz

MECHANICAL DATA

Dimensions in mm

Fig. 1 SOT-23



See also *Soldering recommendations.*

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RATINGS

Limiting values in accordance with the Absolute Maximum System (IEC 134)

Collector-base voltage (open emitter)	$-V_{CBO}$	max.	40 V
Collector-emitter voltage (open base)	$-V_{CEO}$	max.	35 V
Emitter-base voltage (open collector)	$-V_{EBO}$	max.	3 V
Collector current (d.c.)	$-I_C$	max.	30 mA
Total power dissipation up to $T_{amb} = 60\text{ }^\circ\text{C}^{**}$	P_{tot}	max.	200 mW
Storage temperature	T_{stg}		-65 to +150 $^\circ\text{C}$
Junction temperature	T_j	max.	150 $^\circ\text{C}$

THERMAL CHARACTERISTICS*

$$T_j = P \times (R_{th\ j-t} + R_{th\ t-s} + R_{th\ s-a}) + T_{amb}$$

Thermal resistance

From junction to tab	$R_{th\ j-t}$	=	60 K/W
From tab to soldering points	$R_{th\ t-s}$	=	280 K/W
From soldering points to ambient**	$R_{th\ s-a}$	=	90 K/W

CHARACTERISTICS $T_j = 25\text{ }^\circ\text{C}$ unless otherwise specified.**Collector cut-off current**

$$I_E = 0; -V_{CB} = 20\text{ V} \quad -I_{CBO} < 100\text{ nA}$$

D.C. current gain

$$I_E = 3\text{ mA}; -V_{CB} = 10\text{ V} \quad h_{FE} > \begin{matrix} 25 \\ \text{typ.} \\ 50 \end{matrix}$$

Transition frequency at $f = 100\text{ MHz}$

$$I_E = 3\text{ mA}; -V_{CB} = 10\text{ V} \quad f_T \text{ typ. } 900\text{ MHz}$$

Feedback capacitance at $f = 1\text{ MHz}$

$$I_E = 1\text{ mA}; -V_{CB} = 10\text{ V} \quad C_{re} \text{ typ. } 0,33\text{ pF}$$

Noise figure at $f = 800\text{ MHz}$

$$I_E = 3\text{ mA}; -V_{CB} = 10\text{ V}; R_S = 60\ \Omega; R_L = 500\ \Omega \quad F \text{ typ. } 4,5\text{ dB}$$

Power gain at $f = 800\text{ MHz}$

$$I_E = 3\text{ mA}; -V_{CB} = 10\text{ V}; R_S = 60\ \Omega; R_L = 500\ \Omega \quad G_{pb} \text{ typ. } 14,5\text{ dB}$$

* See *Thermal characteristics*.

** Mounted on a ceramic substrate of 8 mm x 10 mm x 0,7 mm.

Silicon planar epitaxial transistor

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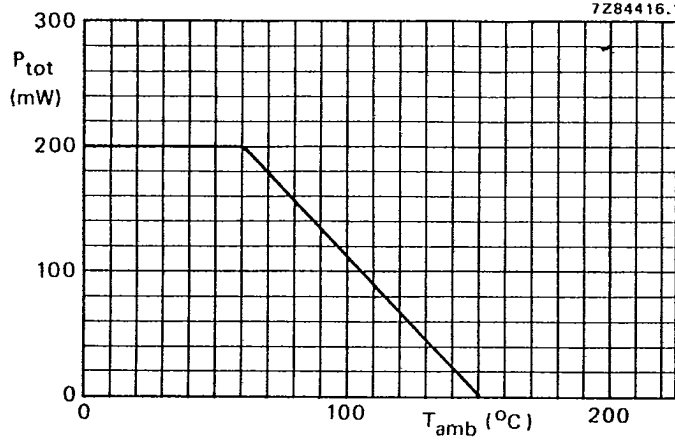


Fig. 2 Power derating curve.