

HSC285

Silicon Schottky Barrier Diode for Detector

REJ03G0011-0200 Rev.2.00 May 17, 2006

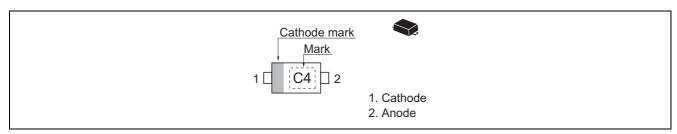
Features

- Low forward voltage, Low capacitance and High detection sensitivity.
- Ultra small Flat Lead Package (UFP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Package Name	Package Code
HSC285	C4	UFP	PWFS0002ZA-A

Pin Arrangement



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Reverse voltage	V_R	2	V
Average rectified current	Io	5	mA
Junction temperature	Tj	125	°C
Storage temperature	Tstg	-55 to +125	°C

Electrical Characteristics

 $(Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Forward voltage	V_{F1}	_	_	0.15	V	I _F = 0.1 mA
	V_{F2}	_	_	0.27		I _F = 1 mA
Capacitance	С	_	0.3	_	pF	$V_R = 0.5 \text{ V}, f = 1 \text{ MHz}$
ESD-Capability *1	_	10	_	_	V	$C = 200 \text{ pF}, R_L = 0 \Omega$, Both forward
						and reverse direction 1 pulse.

Note: 1. Failure criterion ; $I_R \ge 100 \ \mu A$ at $V_R = 0.5 \ V$

Main Characteristic

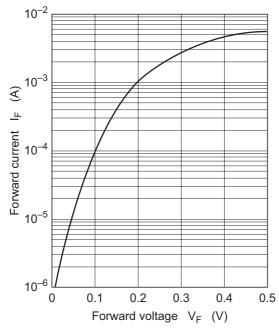


Fig.1 Forward current vs. Forward voltage

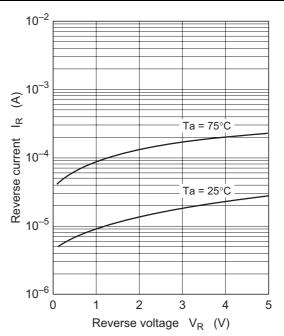
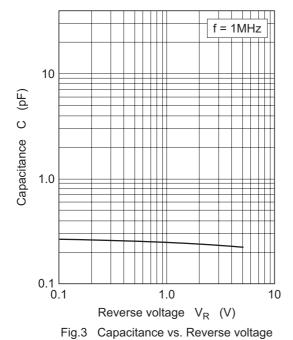
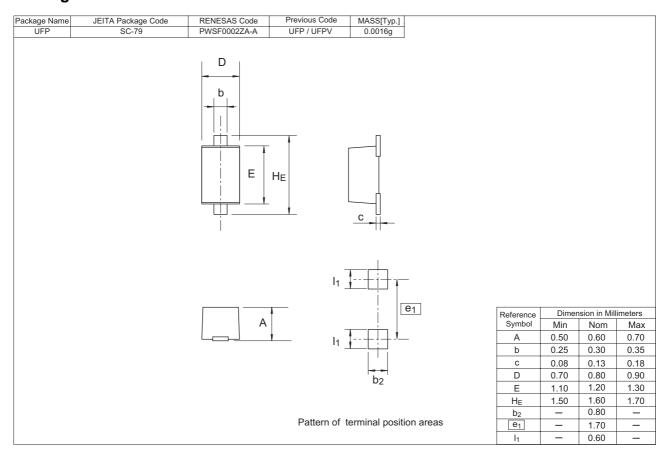


Fig.2 Reverse current vs. Reverse voltage



Package Dimensions



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