

# HVD350B

## Variable Capacitance Diode for VCO

REJ03G0497-0100  
(Previous: ADE-208-848)  
Rev.1.00  
Jan 20, 2005

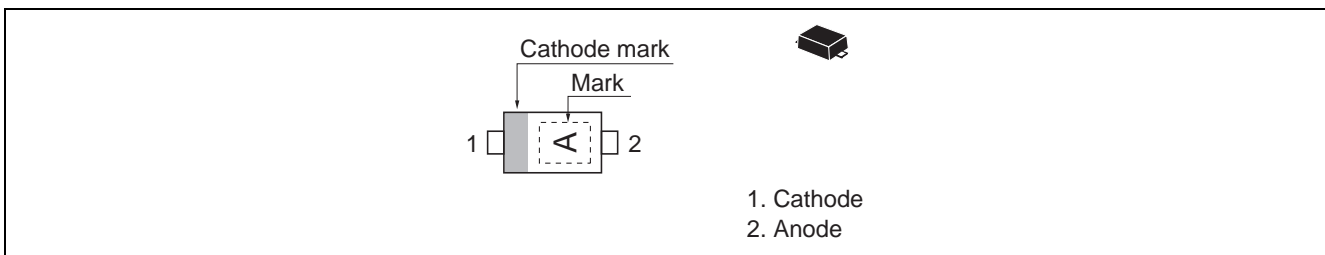
### Features

- High capacitance ratio. ( $n = 2.80$  min)
- Low series resistance. ( $r_s = 0.50 \Omega$  max)
- Good C-V linearity.
- Super small Flat Lead Package (SFP) is suitable for surface mount design.

### Ordering Information

Type No.	Laser Mark	Package Code
HVD350B	A	SFP

### Pin Arrangement



## Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Reverse voltage	V <sub>R</sub>	15	V
Junction temperature	T <sub>j</sub>	125	°C
Storage temperature	T <sub>stg</sub>	-55 to +125	°C

## Electrical Characteristics

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse current	I <sub>R1</sub>	—	—	10	nA	V <sub>R</sub> = 15 V
	I <sub>R2</sub>	—	—	100		V <sub>R</sub> = 15 V, Ta = 60°C
Capacitance	C <sub>1</sub>	15.50	—	17.00	pF	V <sub>R</sub> = 1 V, f = 1 MHz
	C <sub>4</sub>	5.00	—	6.00		V <sub>R</sub> = 4 V, f = 1 MHz
Capacitance ratio	n	2.80	—	—	—	C <sub>1</sub> / C <sub>4</sub>
Series resistance	r <sub>s</sub>	—	—	0.50	Ω	V <sub>R</sub> = 1 V, f = 470 MHz

Note: 1. Please do not use the soldering iron due to avoid high stress to the SFP package.

Main Characteristic

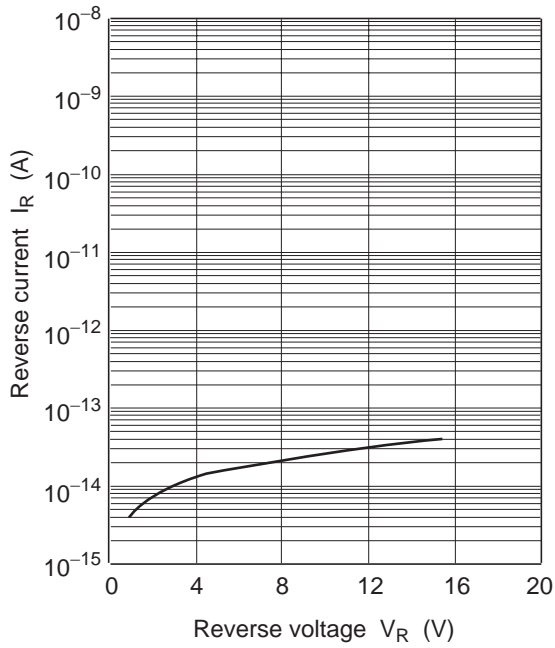


Fig.1 Reverse current vs. Reverse voltage

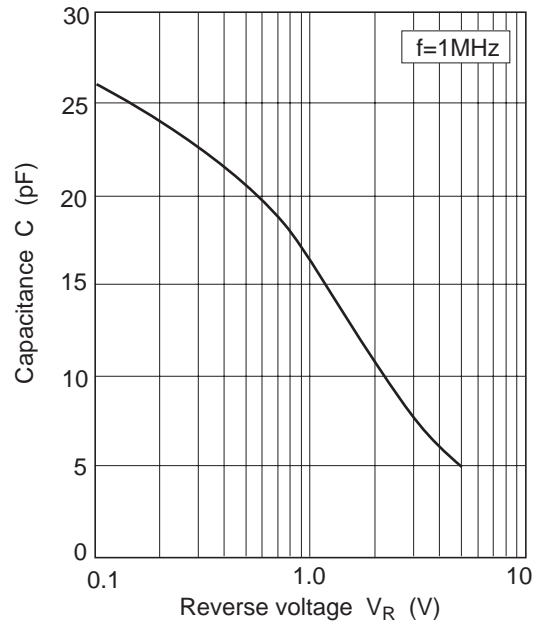


Fig.2 Capacitance vs. Reverse voltage

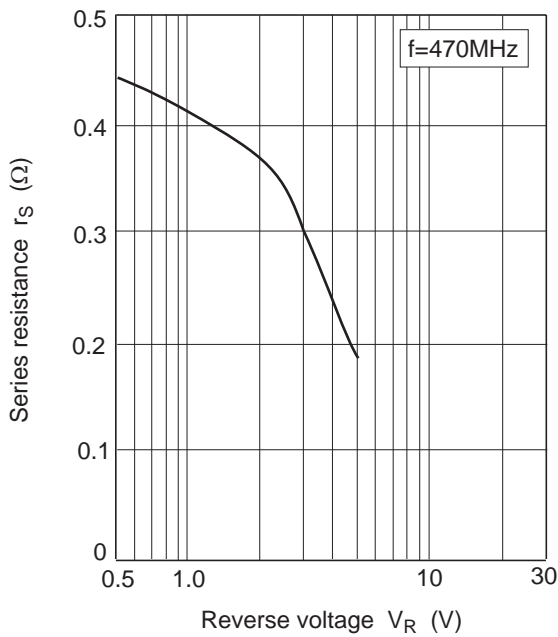


Fig.3 Series resistance vs. Reverse voltage

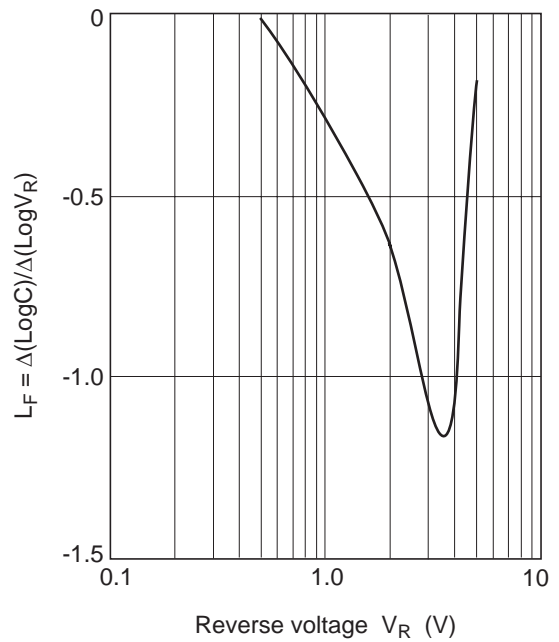
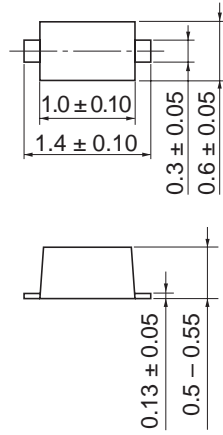


Fig.4  $L_F$  vs. Reverse voltage

Package Dimensions

As of January, 2003  
Unit: mm



Package Code	SFP
JEDEC	—
JEITA	—
Mass (reference value)	0.0010 g

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#### **Renesas Technology Singapore Pte. Ltd.**

1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632  
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