RENESAS

HSU119

Silicon Epitaxial Planar Diode for High Speed Switching

REJ03G0190-0200 Rev.2.00 Feb 04, 2009

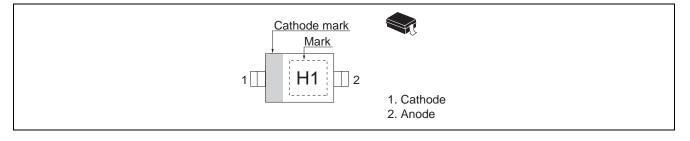
Features

- Low capacitance. (C = 2.0 pF max)
- Short reverse recovery time. ($t_{rr} = 3.0 \text{ ns max}$)
- Lineup of environmental friendly Halogen free type (HSU119-N)
- Ultra small Resin Package (URP) is suitable for compact and high-density surface mount design.

Ordering Information

Part No.	Laser Mark	Package Name	Package Code	Taping Abbreviation (Quantity)
HSU119TRF				
HSU119-NTRF (Halogen-free type)	H1	URP	PTSP0002ZA-A	TRF (3,000 pcs / reel)

Pin Arrangement



Absolute Maximum Ratings

		$(Ta = 25^{\circ}C)$
Symbol	Value	Unit
V _{RM}	85	V
V _R	80	V
I _{FM}	300	mA
I _{FSM} * ¹	4	А
lo	100	mA
Тј	125	°C
Tstg	-55 to +125	°C
	V _{RM} V _R I _{FM} I _{FSM} * ¹ I _O Tj	V _{RM} 85 V _R 80 I _{FM} 300 I _{FSM} * ¹ 4 I _O 100 Tj 125

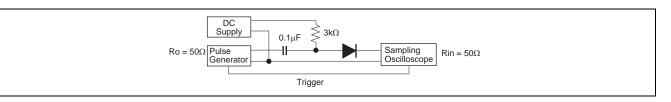
Note: 1. Within $1\mu s$ forward surge current.

Electrical Characteristics

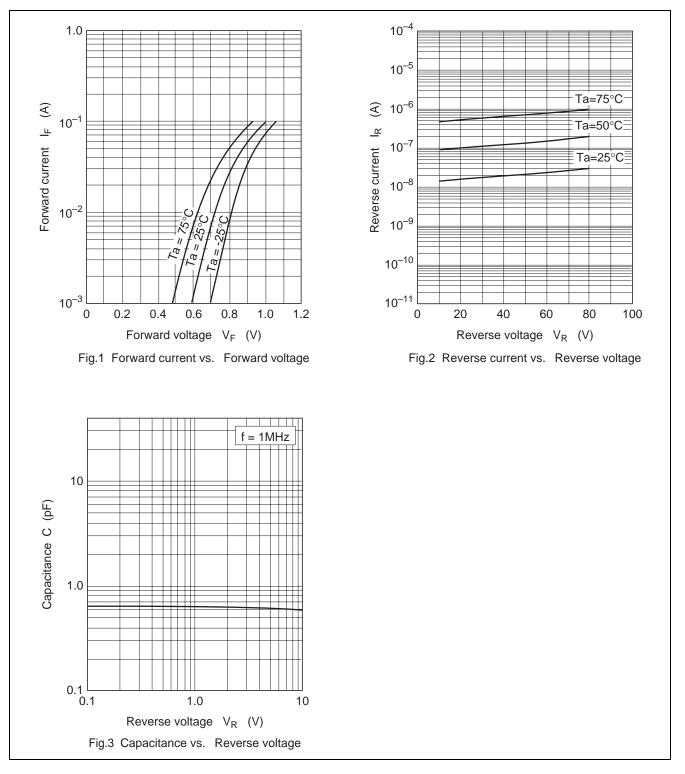
 $(Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Forward voltage	V _{F1}	—	—	0.8	V	I _F = 10 mA
	V _{F2}	—	—	1.2	V	I _F = 100 mA
Reverse current	I _R	—	—	0.1	μΑ	V _R = 80 V
Capacitance	С	—	—	2.0	pF	$V_{R} = 0 V, f = 1 MHz$
Reverse recovery time*1	t _{rr}	_	_	3.0	ns	I_F = 10 mA, V_R = 6 V, R_L = 50 Ω

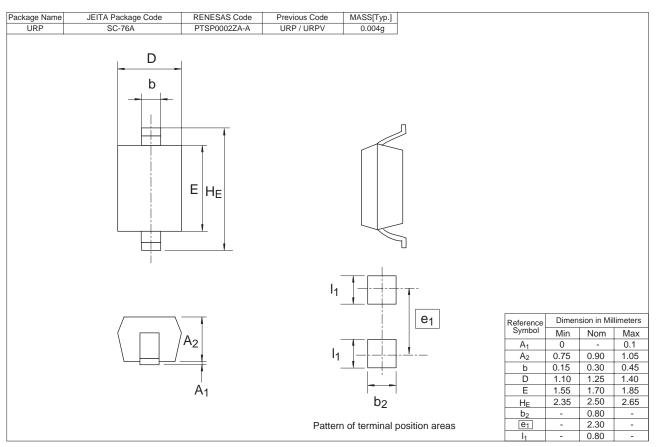
Note: 1. Reverse recovery time test circuit



Main Characteristics



Package Dimensions



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April 1st, 2010 Renesas Electronics Corporation

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