TOSHIBA Transistor Silicon NPN Epitaxial Type

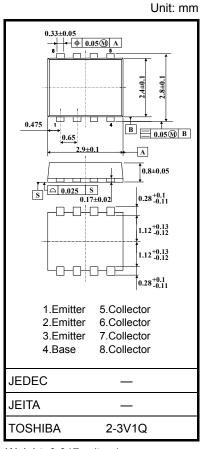
## **TPCP8510**

High-Speed, High-Voltage Switching Applications DC-DC Converter Applications

- High DC current gain:  $h_{FE}$  = 120 to 300 ( $I_C$  = 0.1 A)
- Low collector-emitter saturation: V<sub>CE (sat)</sub> = 0.14 V (max)
- High-speed switching:  $t_f = 0.2 \mu s$  (typ)

## Absolute Maximum Ratings (Ta = 25°C)

Characte	ristic	Symbol	Rating	Unit	
Collector-base voltage		$V_{CBO}$	180	V	
Collector-emitter voltage		V <sub>CEX</sub>	150	V	
		$V_{CEO}$	120	V	
Emitter-base voltage		$V_{EBO}$	7	V	
Collector current	DC (Note 1)	IC	1.0	А	
	Pulse (Note 1)	I <sub>CP</sub>	2.0		
Base current		ΙΒ	0.1	Α	
Collector power dissipation	t = 10s	P <sub>C</sub> (Note 2)	2.25	W	
	DC	FC (Note 2)	1.1		
Junction temperature		Tj	150	°C	
Storage temperature range		T <sub>stg</sub>	−55 to 150	°C	



Weight: 0.017 g (typ.)

- Note 1: Please use devices on condition that the junction temperature is below 150°C.
- Note 2: Mounted on FR4 board (glass epoxy, 1.6 mm thick, Cu area: 645 mm<sup>2</sup>)
- Note 3: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Figure 1. Circuit configuration (top view)

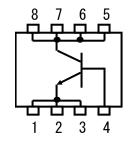
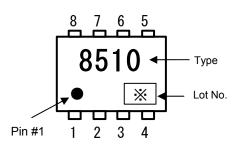


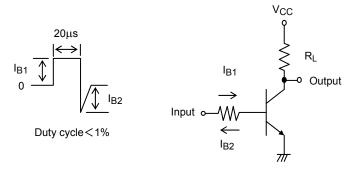
Figure 2. Marking

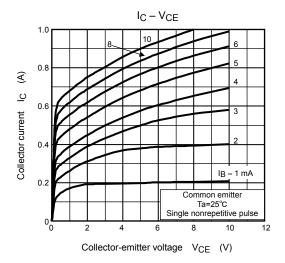


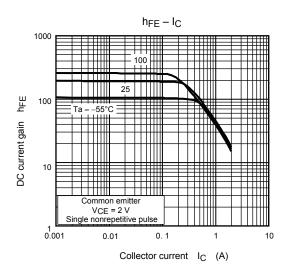
## **Electrical Characteristics (Ta = 25°C)**

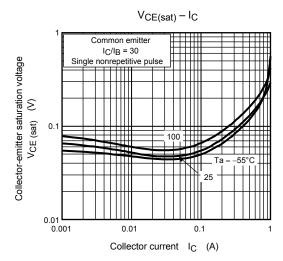
Charac	teristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current		I <sub>CBO</sub>	V <sub>CB</sub> = 180 V, I <sub>E</sub> = 0	_	_	100	nA
Emitter cut-off curr	ent	I <sub>EBO</sub>	V <sub>EB</sub> = 7 V, I <sub>C</sub> = 0	_	_	100	nA
Collector-base brea	akdown voltage	V (BR) CBO	I <sub>C</sub> = 1 mA, I <sub>B</sub> = 0	180	_	_	V
Collector-emitter b	reakdown voltage	V (BR) CEO	I <sub>C</sub> = 10 mA, I <sub>B</sub> = 0	120	_	_	V
DC current gain		h <sub>FE (1)</sub>	V <sub>CE</sub> = 2 V, I <sub>C</sub> = 0.1 A	120	_	300	
		h <sub>FE (2)</sub>	V <sub>CE</sub> = 2 V, I <sub>C</sub> = 0.3 A	60	_	_	
Collector-emitter sa	aturation voltage	V <sub>CE</sub> (sat)	I <sub>C</sub> = 0.3 A, I <sub>B</sub> = 0.01 A	_	_	0.14	V
Base-emitter saturation voltage		V <sub>BE</sub> (sat)	I <sub>C</sub> = 0.3 A, I <sub>B</sub> = 0.01 A	_	_	1.1	V
Switching time	Rise time	t <sub>r</sub>	See Figure 3 circuit diagram	_	0.1	_	
	Storage time	t <sub>stg</sub>	$V_{CC} \approx 72 \text{ V}, R_L = 240 \Omega$	_	1.5	_	μs
	Fall time	t <sub>f</sub>	I <sub>B1</sub> = I <sub>B2</sub> = 10 mA	_	0.2	_	

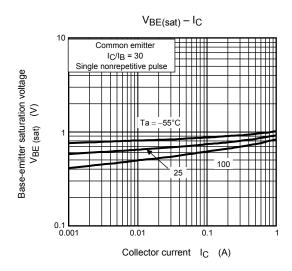
Figure 3. Switching Time Test Circuit

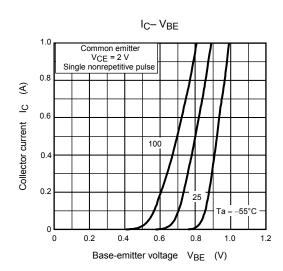


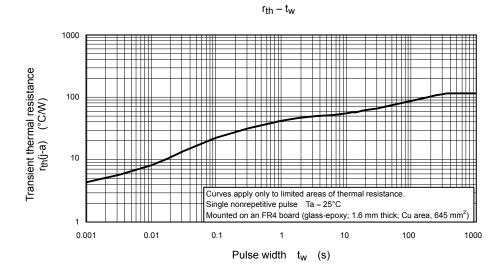


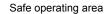


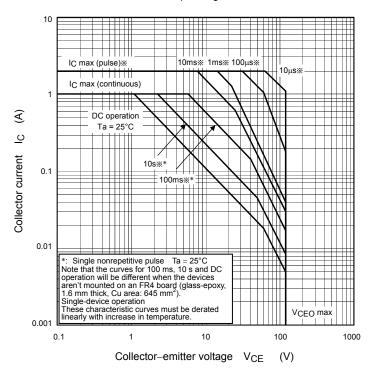












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