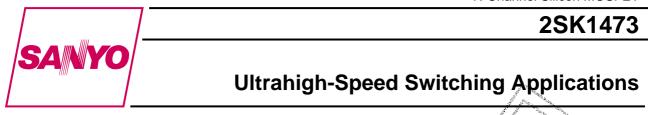
N-Channel Silicon MOSFET



Features

- · Low ON resistance.
- · Ultrahigh-speed switching.
- · Low-voltage drive.

Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Ratings	Unit
Drain-to-Source Voltage	V _D ŞS	100	V
Gate-to-Source Voltage	VGSS	±15	V
Drain Current (DC)	//6	2	A
Drain Current (pulse)	DP PW≤10µs, duty cycle≤1%	8	Α
Allowable Power Dissipation		3.5	W
	Mounted on a ceramic board (250mm ² ×0.8mm)	1.5	W
Channel Temperature	Tch	150	°C
Storage Temperature	a Tstg	-55 to +150	°C

The second second

Electrical Characteristics at Ta = 25°C

Parameter	Symbol C	2 Conditions	Ratings			Unit
r arameter	Cymbol	Conditions	min	typ	max	Onit
Drain-to-Source Breakdown Voltage	V(BR)DSS	lp=1mA, V _{GS} =0	100			V
Zero-Gate Votlage Drain Current	IDSS /	V _{DS} =100V, V _{GS} =0			100	μA
Gate-to-Source Leakage Current	I _{GSS} /	V _{GS} =±12V, V _{DS} =0			±10	μA
Cutoff Voltage	VGS(off)	V _{DS} =10V, I _D =1mA	1.0		2.0	V
Forward Transfer Admittance	∫ ýfs	V _{DS} =10V, I _D =1A	1.2	2.0		S
Static Drain-to-Source On State Resistance	RDS(on) ¹	I _D =1A, V _{GS} =10V		0.7	0.95	Ω
	^R DS(on) ²	I _D =1A, V _{GS} =4V		0.95	1.3	Ω

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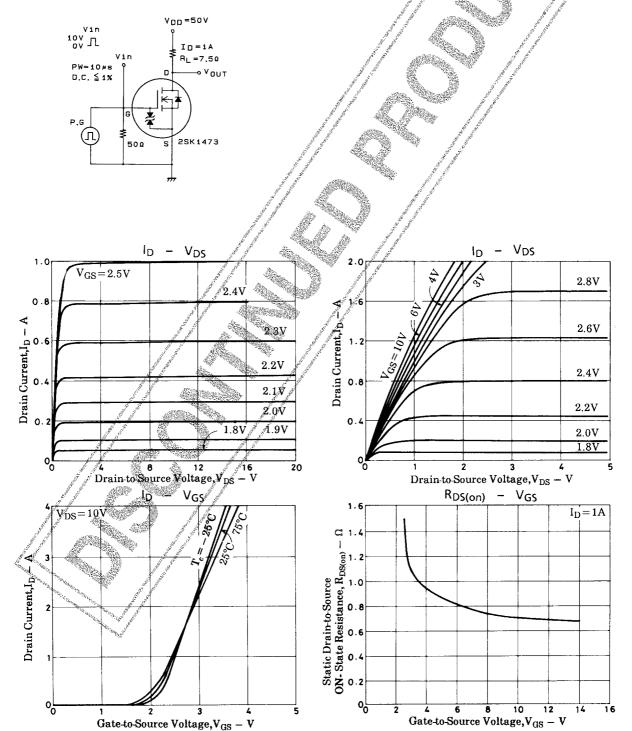
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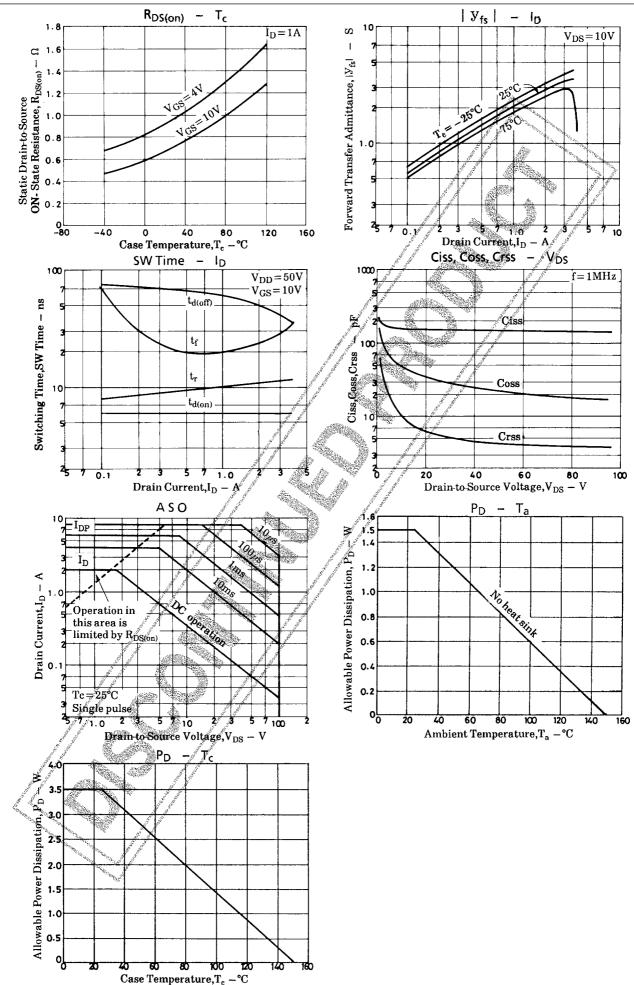
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Continued from preceding page.

Parameter	Symbol	Conditions		Ratings		Unit
Input Capacitance	Ciss	V _{DS} =20V, f=1MHz		150		pF
Output Capacitance	Coss	V _{DS} =20V, f=1MHz		35		pF
Reverse Transfer Capacitance	Crss	V _{DS} =20V, f=1MHz	a	6		pF
Turn-ON Delay Time	^t d(on)	See specified Test Circuit		6		ns
Rise Time	tr	See specified Test Circuit	and a start of the	10		ns
Turn-OFF Delay Time	td(off)	See specified Test Circuit	J. F	60	Sul Street	ns
Fall Time	t _f	See specified Test Circuit		20	W. W. S. Street	ns
Diode Forward Voltage	V _{SD}	I _S =2A, V _{GS} =0		1.0	all	V

Switching Time Test Circuit





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the customer should always evaluate and test devices mounted in the customer's products or equipment.

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