TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

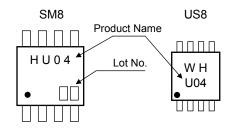
TC7WHU04FU,TC7WHU04FK

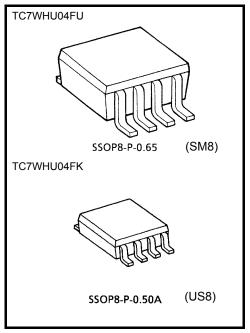
Triple Inverter(Un-Buffer)

Features

- High speed: t_{pd} = 3.5 ns (typ.) at V_{CC} = 5 V, C_L = 15pF
- Low power dissipation: I_{CC} = 2 μA (max) at Ta = 25°C
- High noise immunity: V_{NIH} = V_{NIL} = 10% V_{CC} (min)
- 5.5-V Tolerant inputs.
- Wide operating voltage range: V_{CC} = 2to5.5 V
- Balanced propagation delays :tpLH ≈ tpHL
- · Identical pin assignment and function with TC7WU04

Marking



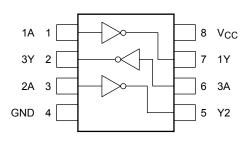


Weight SSOP8-P-0.65: 0.02 g (typ.) SSOP8-P-0.50A: 0.01 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	–0.5 to 7.0	V
DC input voltage	V _{IN}	-0.5 to 7.0	V
DC output voltage	V _{OUT}	-0.5 to V _{CC} + 0.5	V
Input diode current	I _{IK}	-20	mA
Output diode current	lok	±20(Note1)	mA
DC output current	lout	±25	mA
DC V _{CC} /ground current	Icc	±50	mA
Power dissipation	D-	300 (SM8)	mW
	P _D	200 (US8)	IIIVV
Storage temperature	T _{stg}	–65 to 150	°C
Lead temperature (10 s)	TL	260	°C

Pin Assignment (top view)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note1: V_{OUT}<GND,V_{OUT}>V_{CC}



IEC Logic Symbol

IN A _____ OUT Y

Truth Table

Α	Υ
L	Н
Н	L

Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	2.0 to 5.5	V
Input voltage	V _{IN}	0 to 5.5	V
Output voltage	V _{OUT}	0 to V _{CC}	V
Operating temperature	T _{opr}	-40 to 85	°C
Input rise and fall time	dt/dv	0 to 100 (V _{CC} = 3.3 ± 0.3 V)	ns/V
	alluv	0 to 20 ($V_{CC} = 5.0 \pm 0.5 \text{ V}$)	115/V



Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Condition			٦	Ta = 25°C		Ta = -40 to 85°C		Unit
Characteristics	Syllibol			V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
				2.0	1.7	_	_	1.7	_	V
High-level input voltage	V _{IH}		_		V _{CC} × 0.8		_	V _{CC} × 0.8	_	
					_	_	0.3	_	0.3	
Low-level input voltage	V _{IL}		_	3.0 to 5.5	_	_	V _{CC} × 0.2	_	V _{CC} × 0.2	V
			I _{OH} = -50 μA	2.0	1.8	2.0	_	1.8	_	V
High-level output voltage V _{OH}	V _{OH}	$V_{IN} = V_{IL}$		3.0	2.7	3.0	_	2.7	_	
				4.5	4.4	4.5	_	4.0	_	
		V CND	I _{OH} = -4 mA	3.0	2.58	_	_	2.48	_	
	V _{IN} = GND	$I_{OH} = -8 \text{ mA}$	4.5	3.94	_	_	3.80	_		
		$V_{IN} = V_{IH}$	I _{OL} = 50 μA	2.0	_	0.0	0.2	_	0.2	V
				3.0	_	0.0	0.3	_	0.3	
Low-level output voltage V _{OL}	V_{OL}			4.5	_	0.0	0.5	_	0.5	
		$V_{IN} = V_{CC}$	I _{OL} = 4 mA	3.0	_	_	0.36	_	0.44	
			I _{OL} = 8 mA	4.5	_	_	0.36	_	0.44	
Input leakage current	I _{IN}	V _{IN} = 5.5 V or GND		0 to 5.5	_		±0.1	_	±1.0	μА
Quiescent supply current	Icc	V _{IN} = V _{CC} or GND		5.5	_	_	2.0	_	20.0	μА



AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics Sy	Cumbal	7	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
	Syllibol	Symbol	V _{CC} (V)	C _L (pF)	Min	Тур.	Max	Min	Max	Offic
Propagation delay time tplh tphL		· —	3.3 ± 0.3	15	_	5.0	8.9	1.0	10.5	- ns
				50	_	7.5	11.4	1.0	13.0	
			5.0 ± 0.5	15	_	3.5	5.5	1.0	6.5	
				50	_	5.0	7.0	1.0	8.0	
Input capacitance	C _{IN}		_			5	10	_	10	pF
Power dissipation capacitance	C_{PD}			(Note2)		11		_	_	pF

Note 2: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

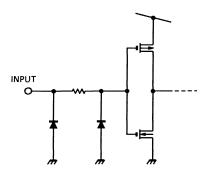
Average operating current can be obtained by the equation:

$$I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/3$$

Noise Characteristics (Ta = 25°C, input: $t_r = t_f = 3$ ns)

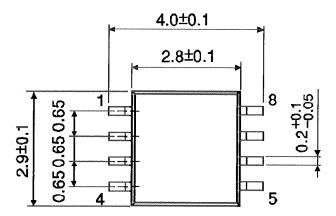
Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Limit	Unit
Quiet output maximum dynamic V _{OL}	V _{OLP}	C _L = 50 pF	5.0	0.3	0.8	V
Quiet output minimum dynamic V _{OL}	V _{OLV}	C _L = 50 pF	5.0	-0.3	-0.8	V
Minimum high level dynamic V _{IH}	V _{IHD}	C _L = 50 pF	5.0	_	4.0	V
Maximum low level dynamic V _{IH}	V _{ILD}	C _L = 50 pF	5.0	_	1.0	V

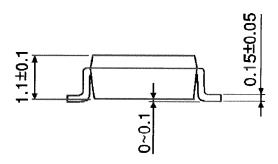
Input Equivalent Circuit



Package Dimensions

SSOP8-P-0.65 Unit: mm



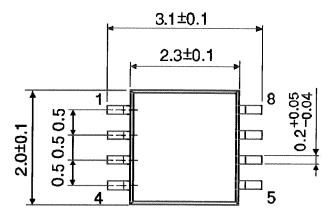


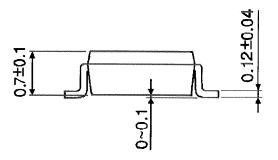
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Weight: 0.02 g (typ.)

Package Dimensions

SSOP8-P-0.50A Unit: mm





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Weight: 0.01 g (typ.)

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